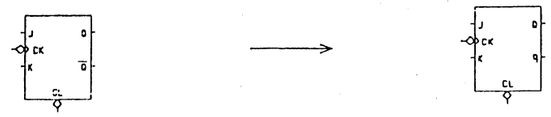
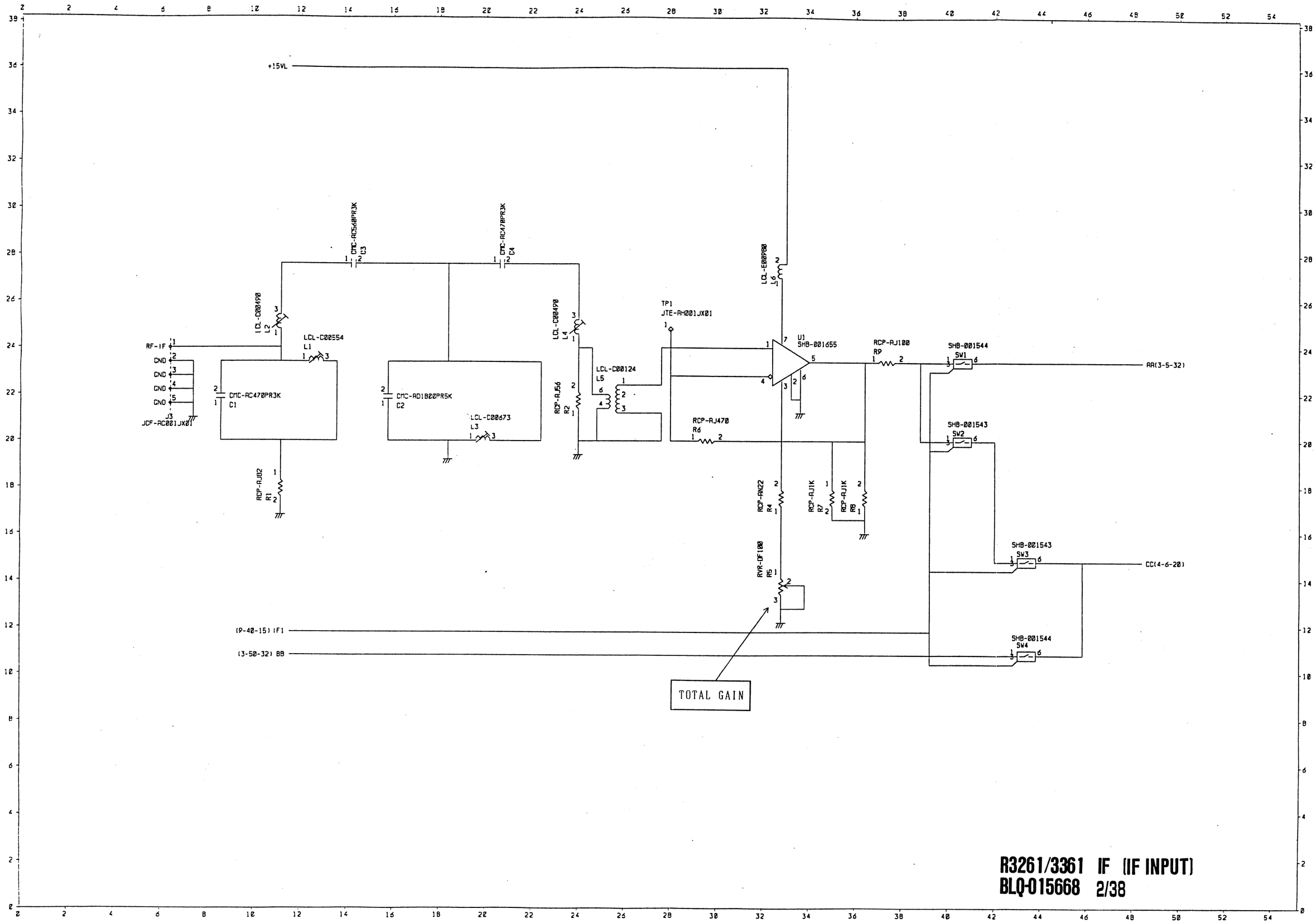
1. DIAGRAMS ILLUSTRATION

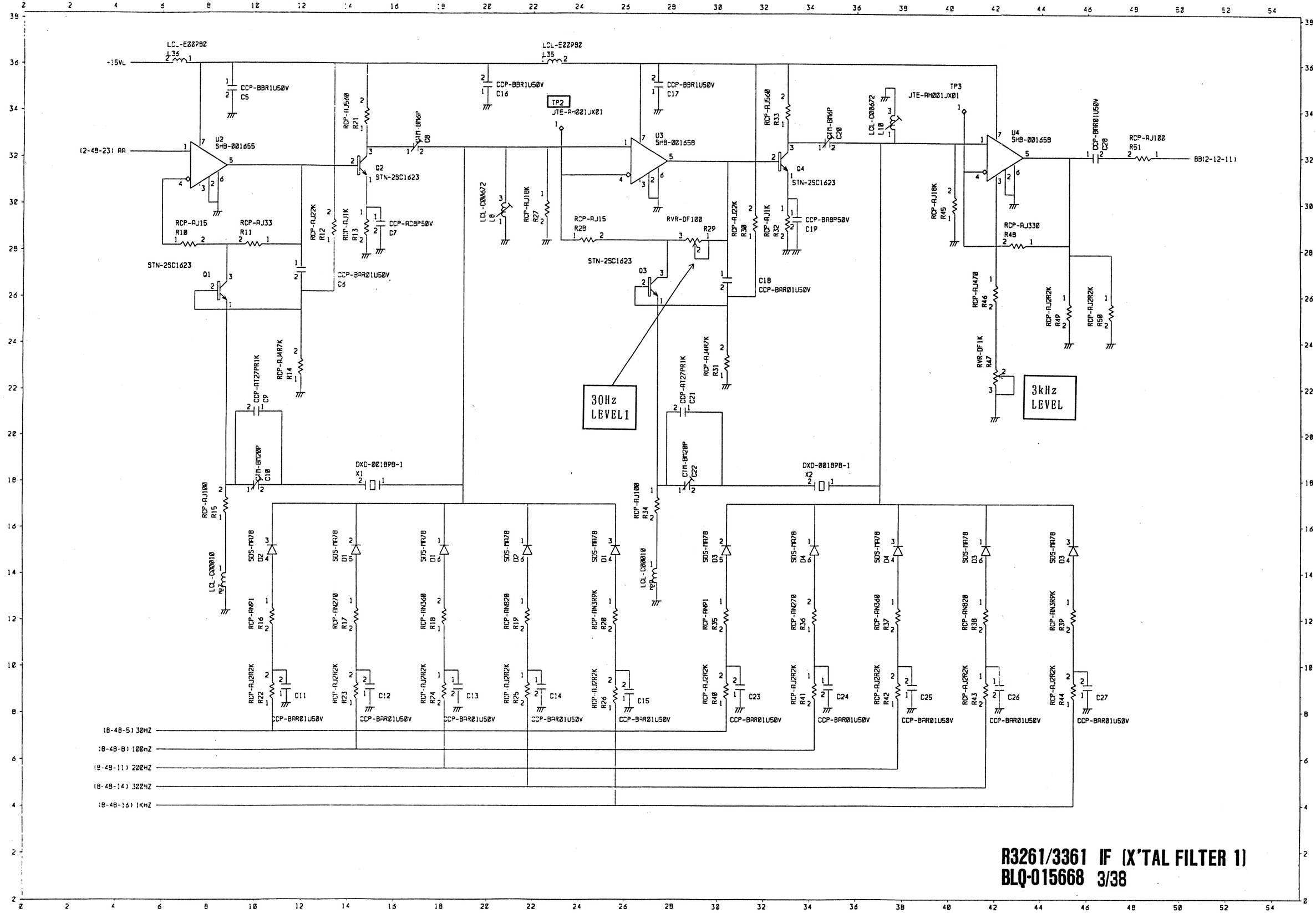
1-1 SYMBOLS REFERENCE DESIGNATORS

1) IC PIN NAME (PIN-NAME OF NEGATIVE LOGIC ARE DISPLAYED BY SMALL LETTER)

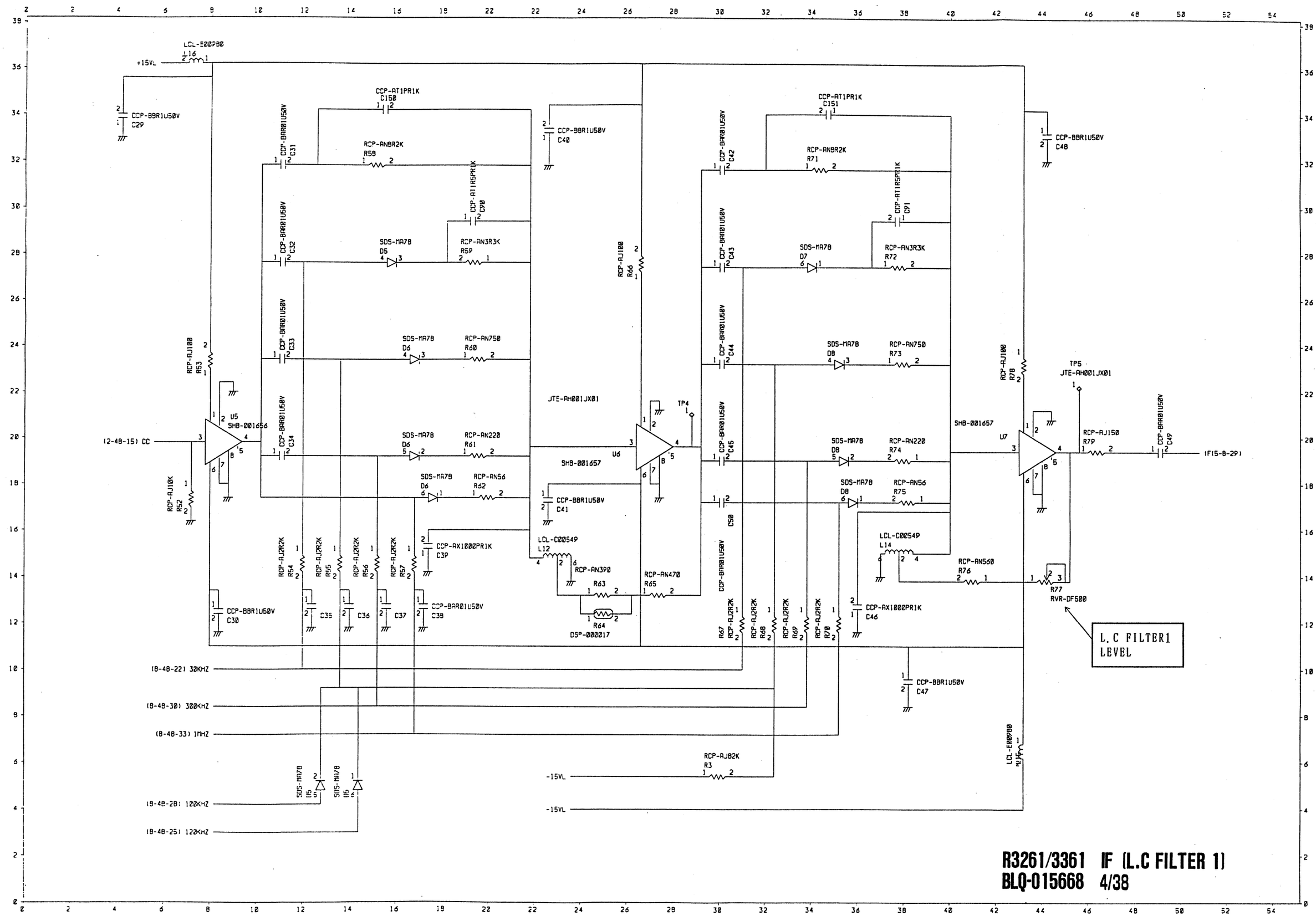




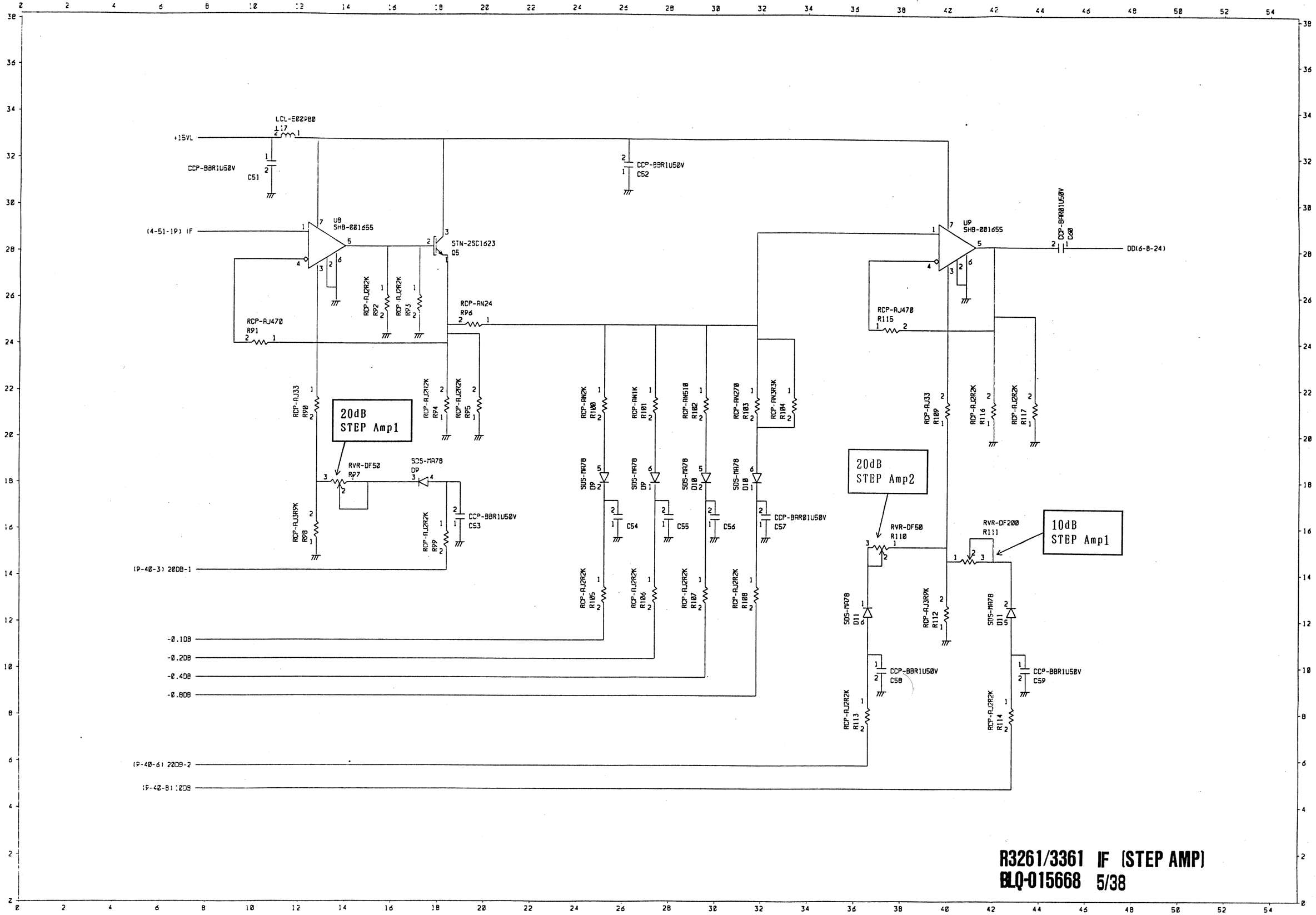
R3261/3361 IF (IF INPUT)
BLQ-015668 2/38



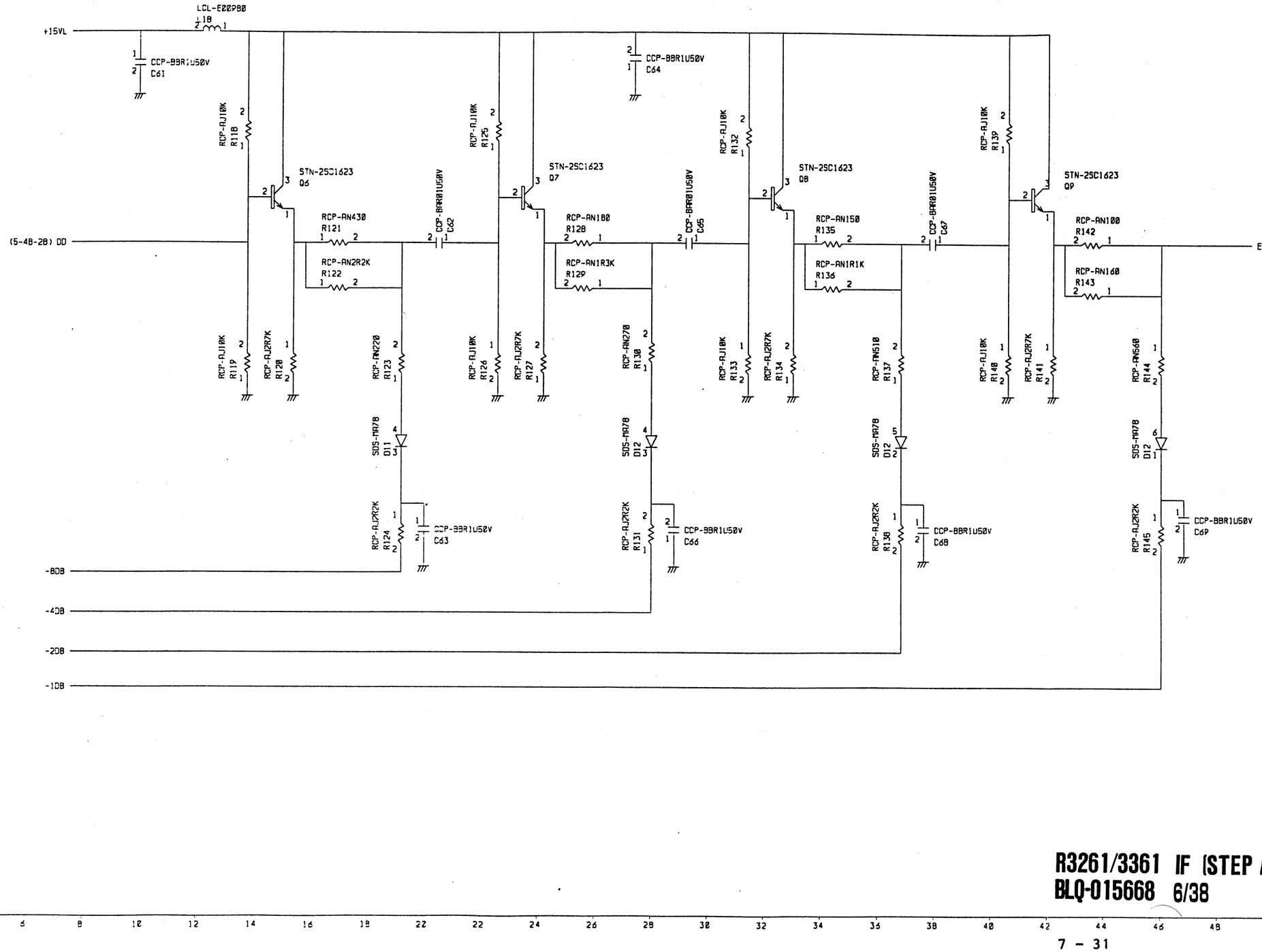
R3261/3361 IF (X'TAL FILTER 1)
BLQ-015668 3/38



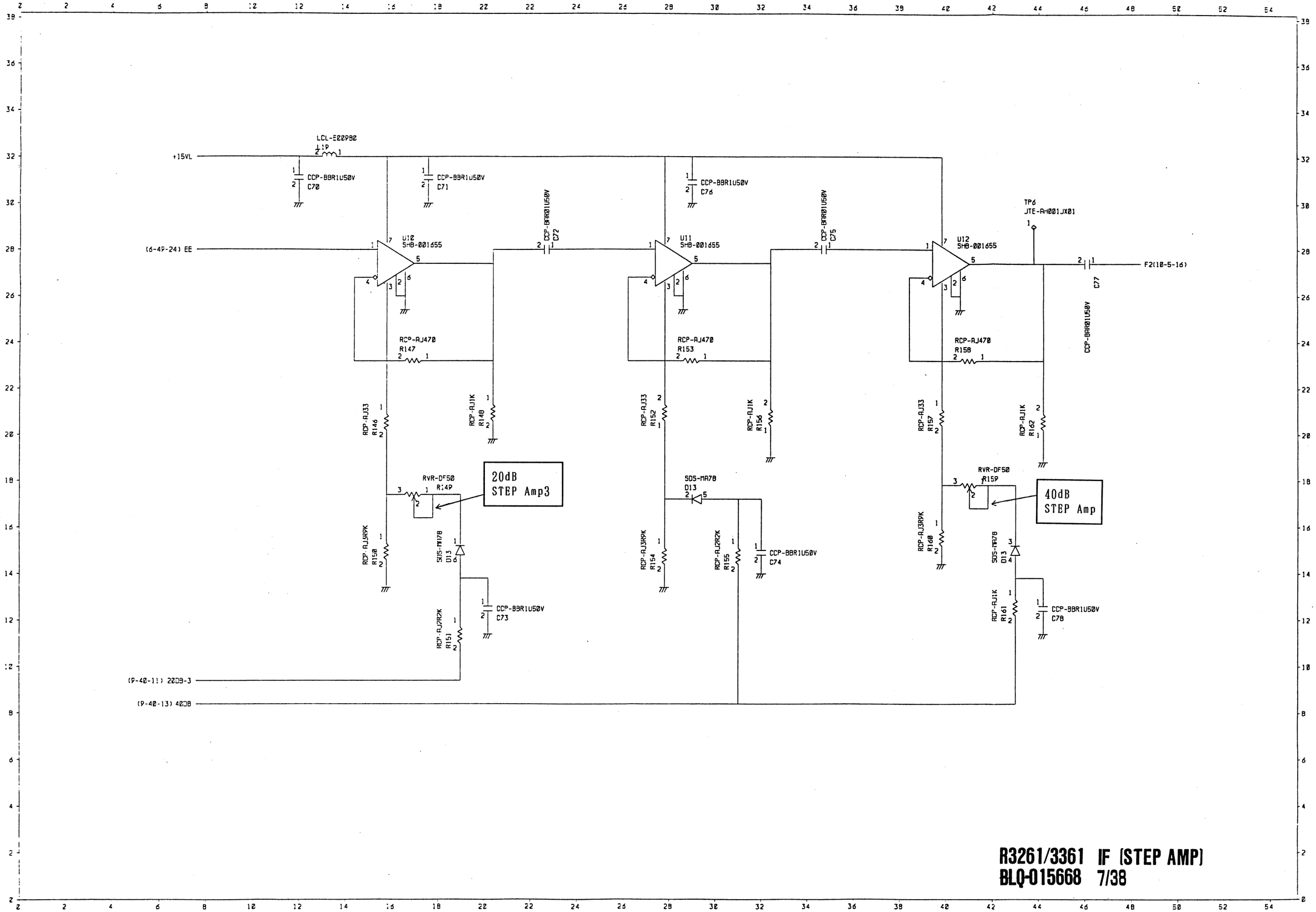
R3261/3361 IF (L.C FILTER 1)
BLQ-015668 4/38



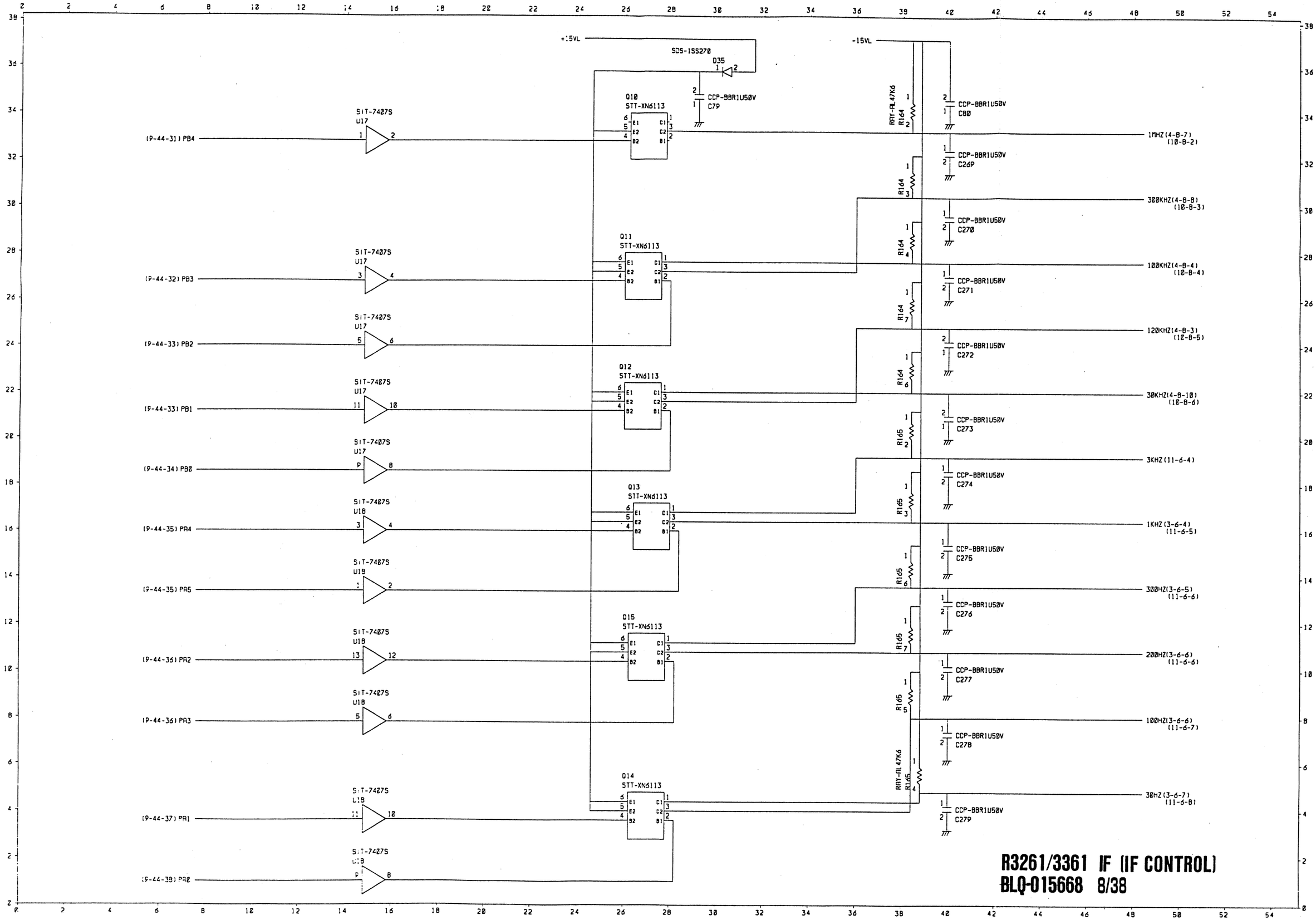
R3261/3361 IF (STEP AMP)
BLQ-015668 5/38



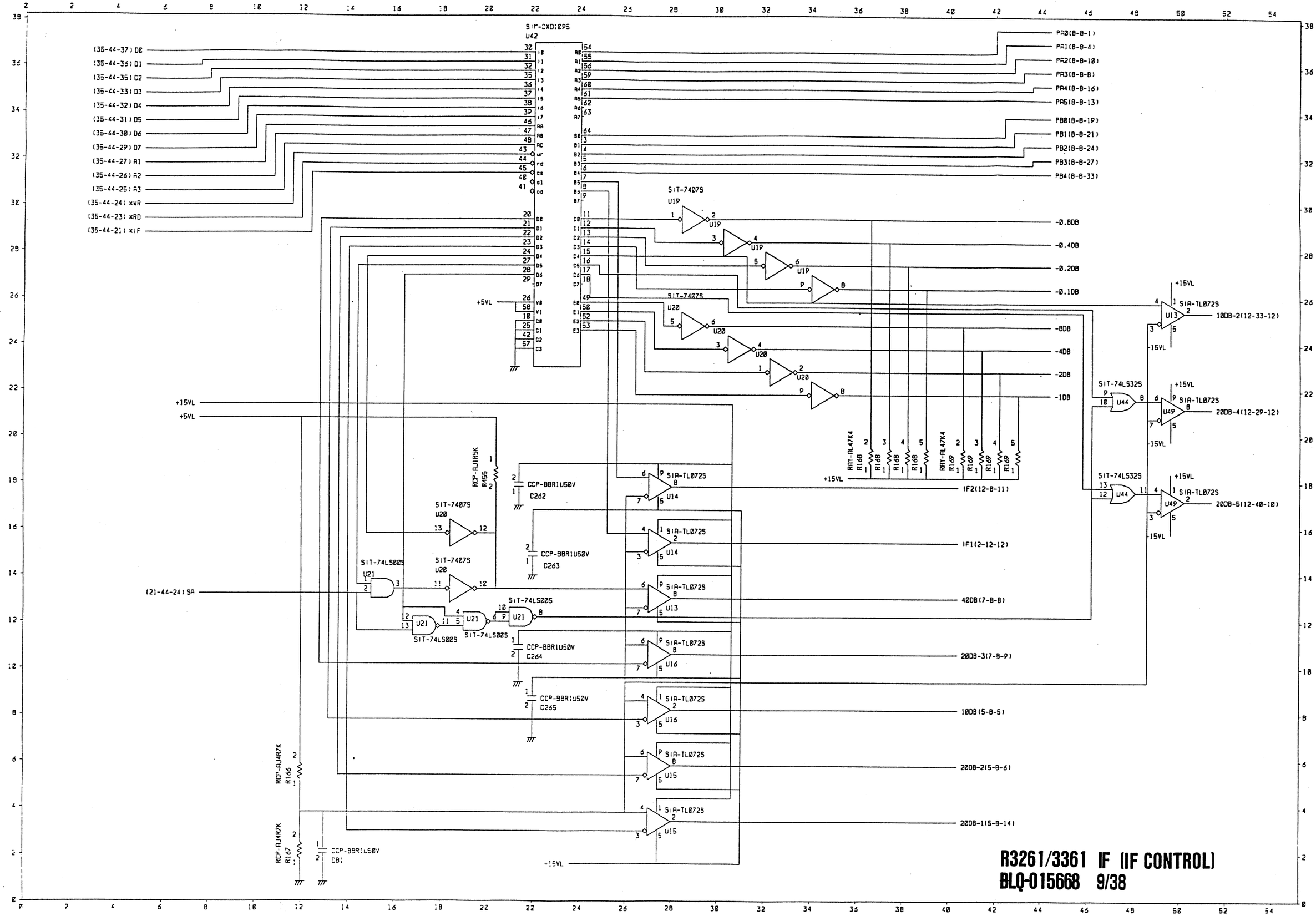
R3261/3361 IF (STEP AMP)
BLQ-015668 6/38



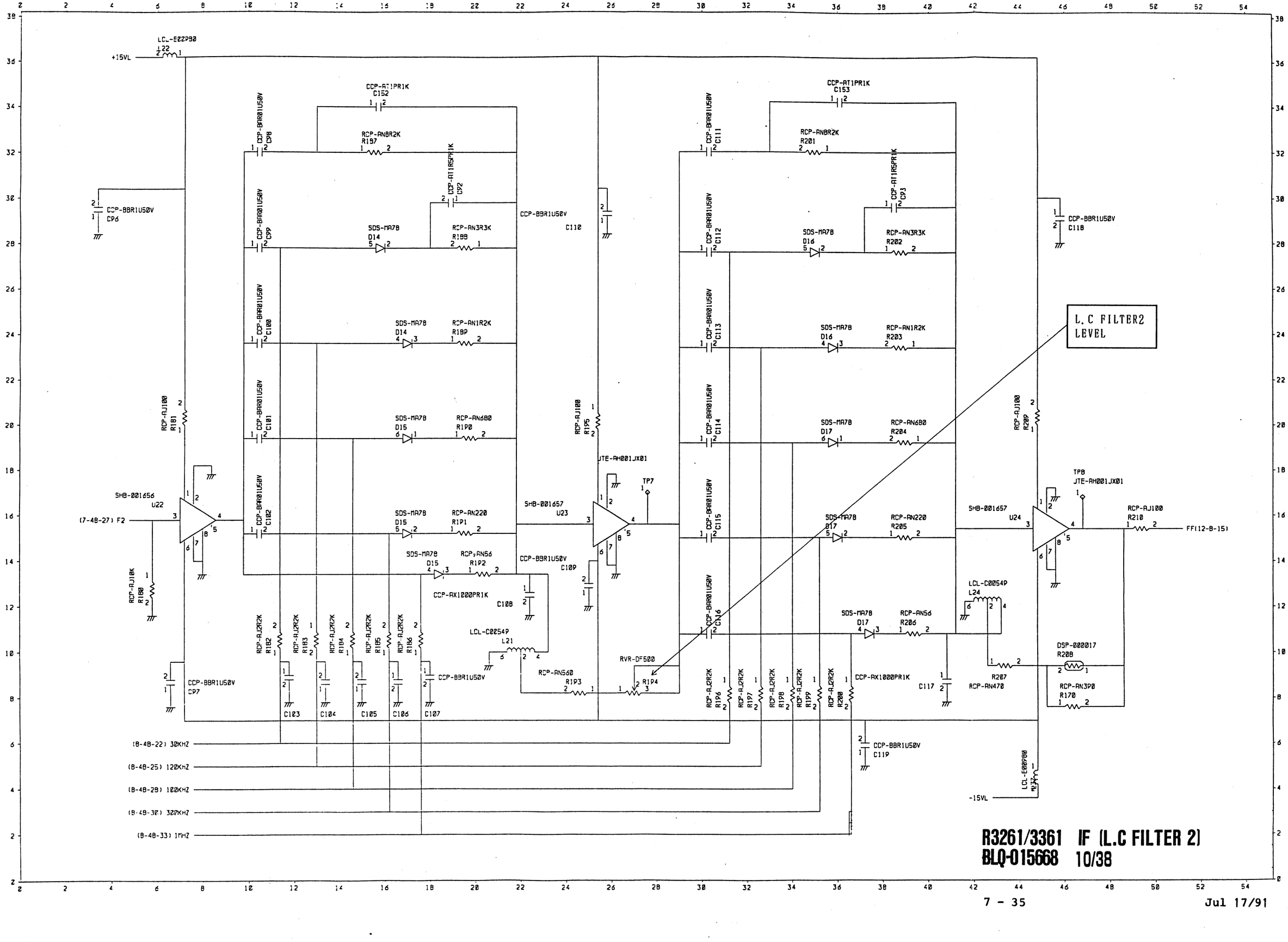
R3261/3361 IF (STEP AMP)
BLQ-015668 7/38



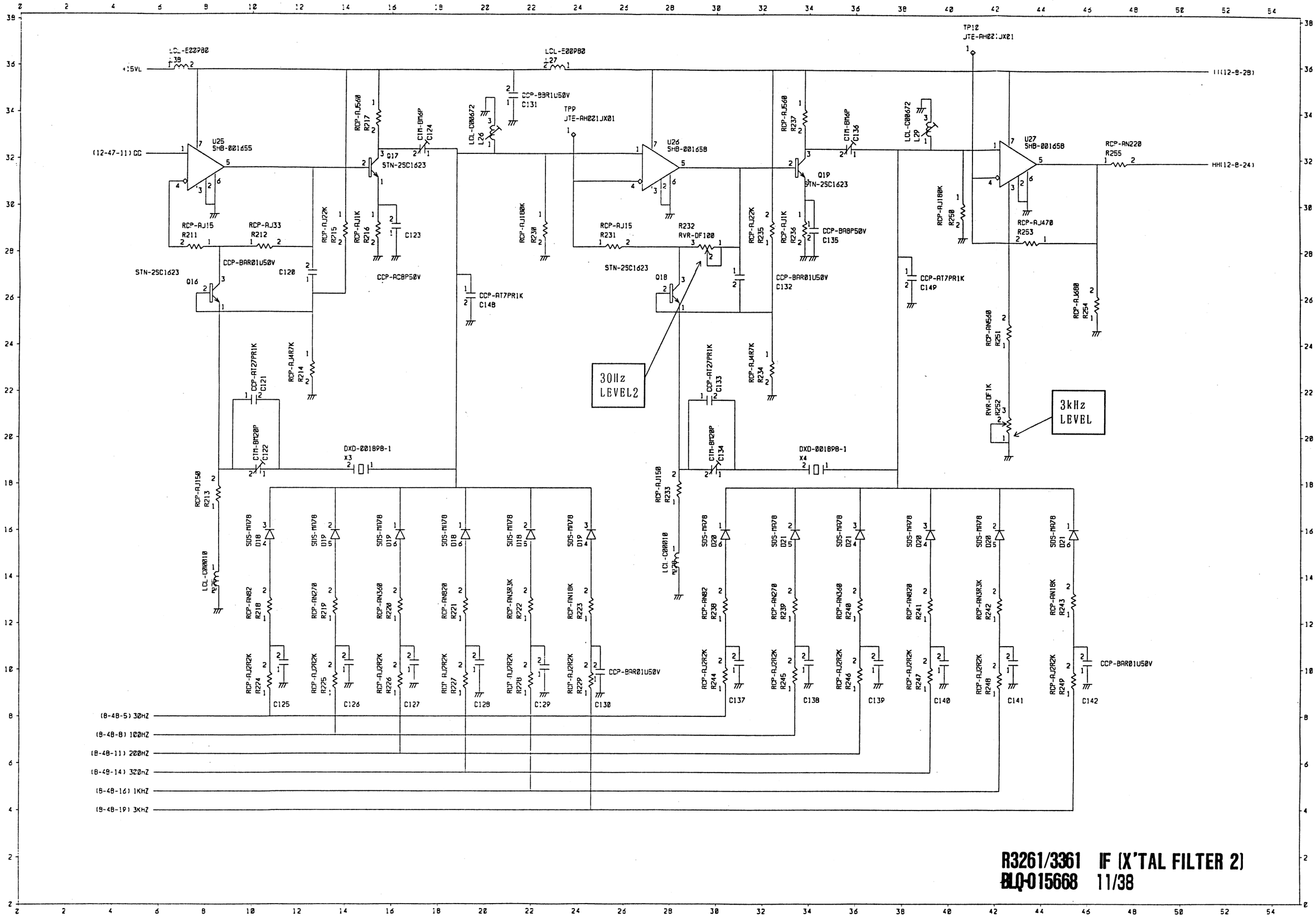
R3261/3361 IF (IF CONTROL)
BLQ-015668 8/38



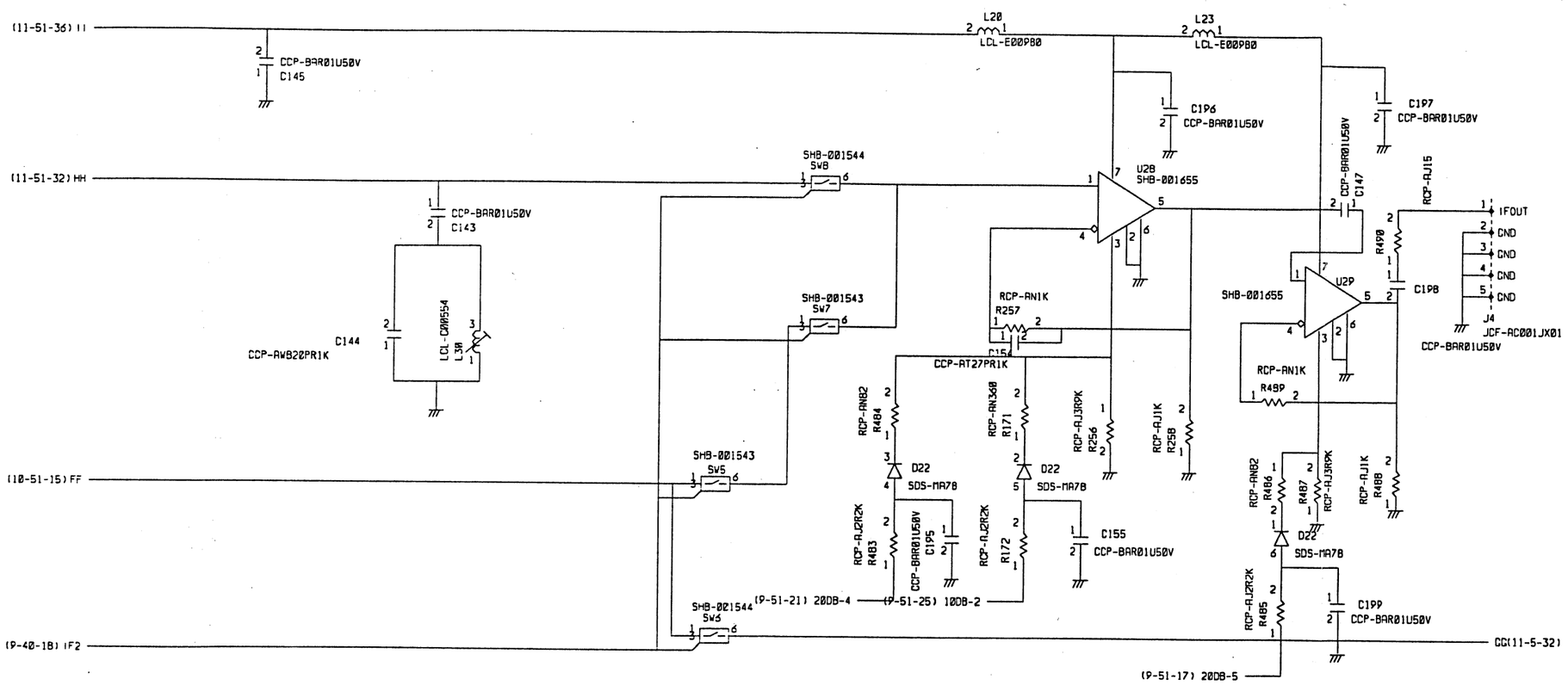
R3261/3361 IF (IF CONTROL)
BLQ-015668 9/38



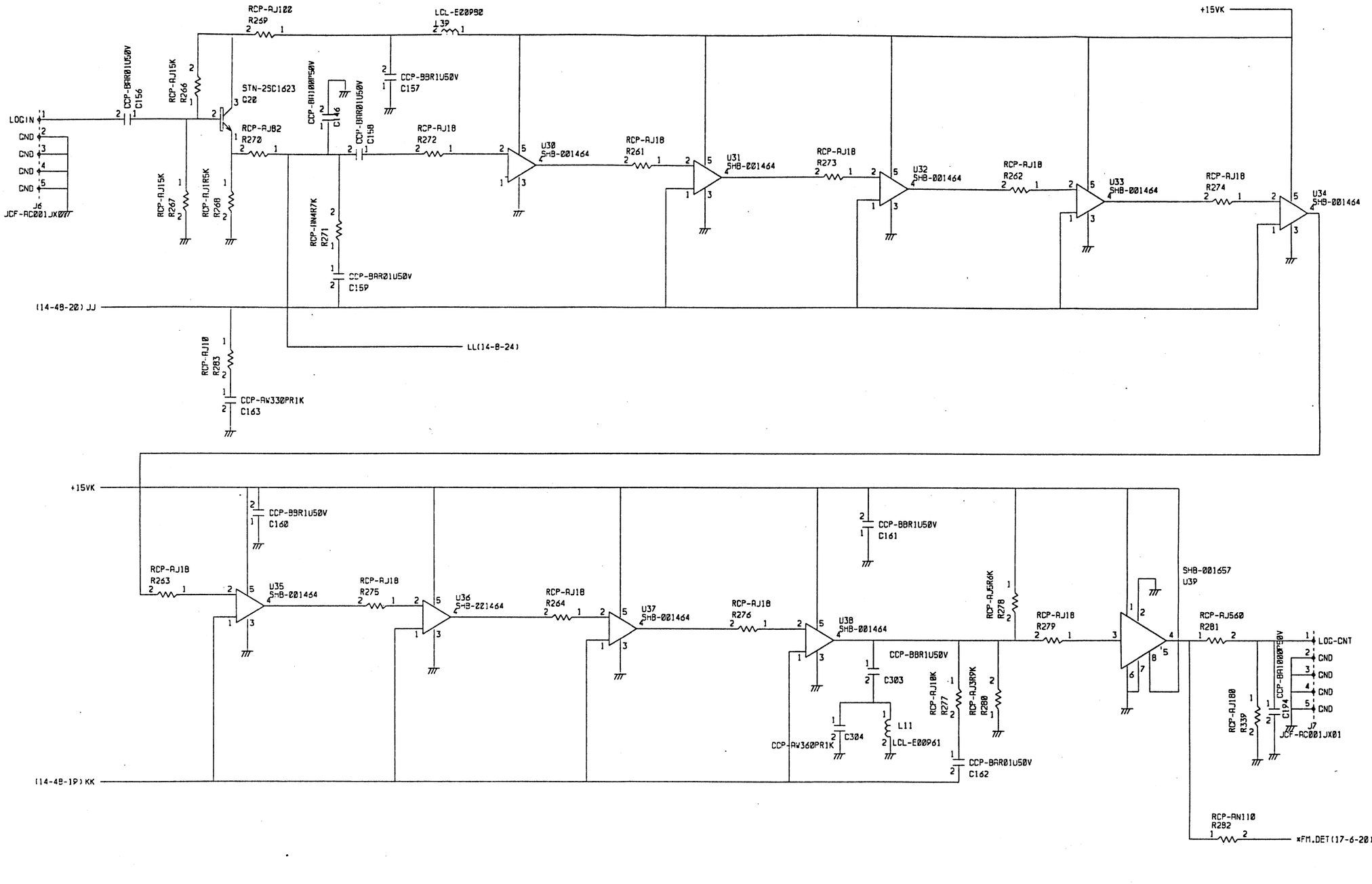
R3261/3361 IF (L.C FILTER 2)
BLQ-015668 10/38



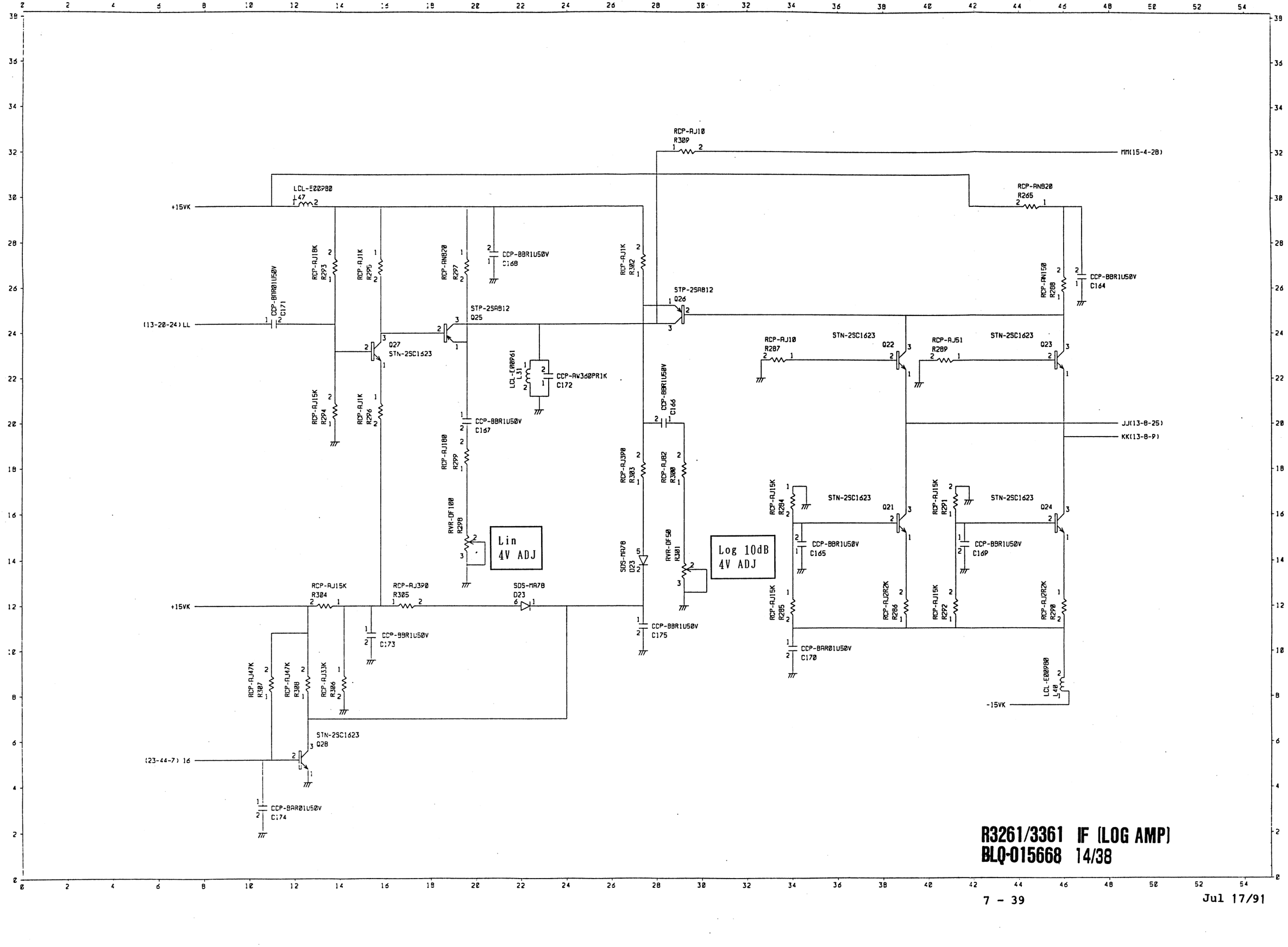
R3261/3361 IF (X'TAL FILTER 2)
BLQ-015668 11/38



R3261/3361 IF (IF OUTPUT)
BLQ-015668 12/38

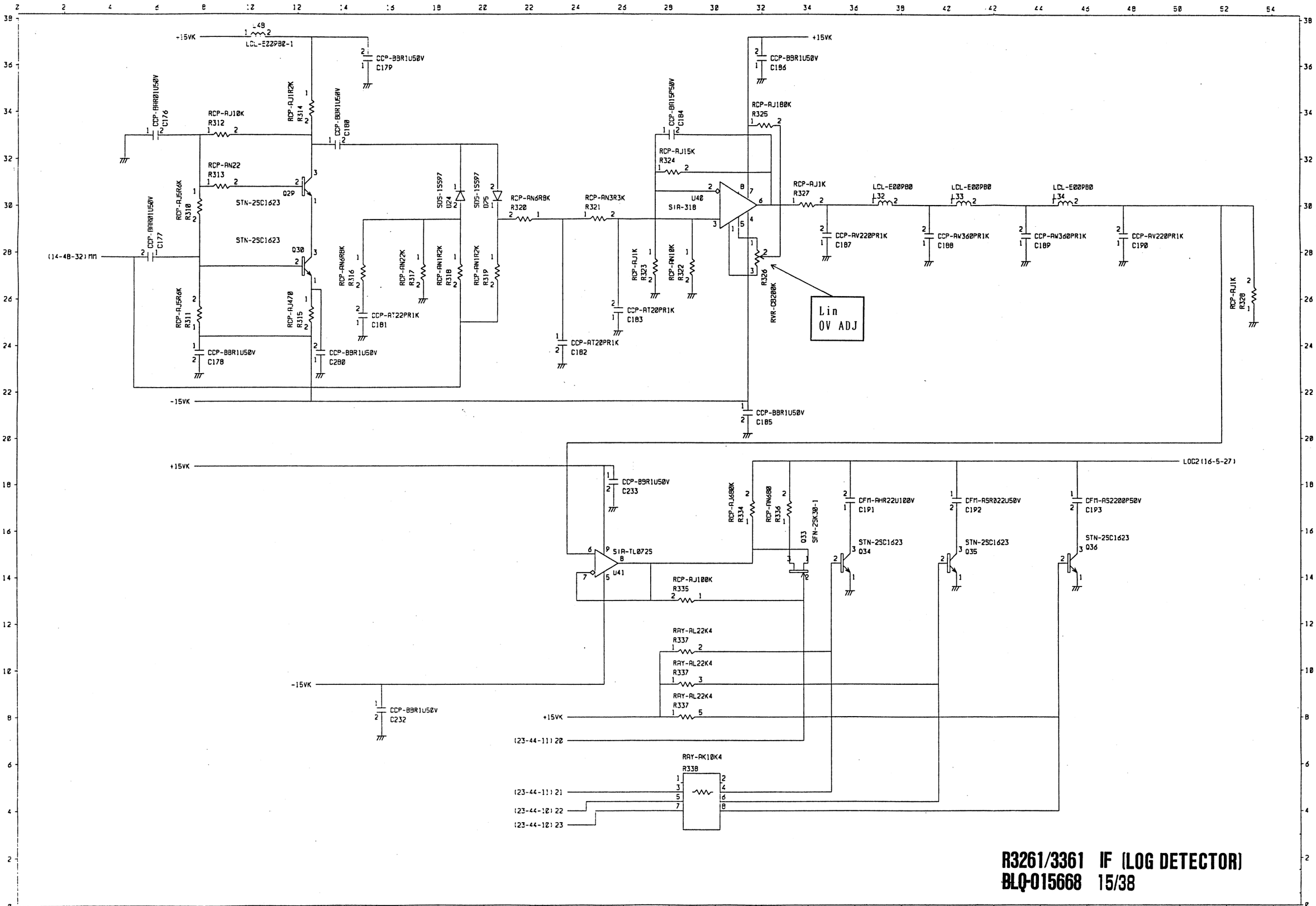


R3261/3361 IF (LOG AMP)
BLQ-015668 13/38

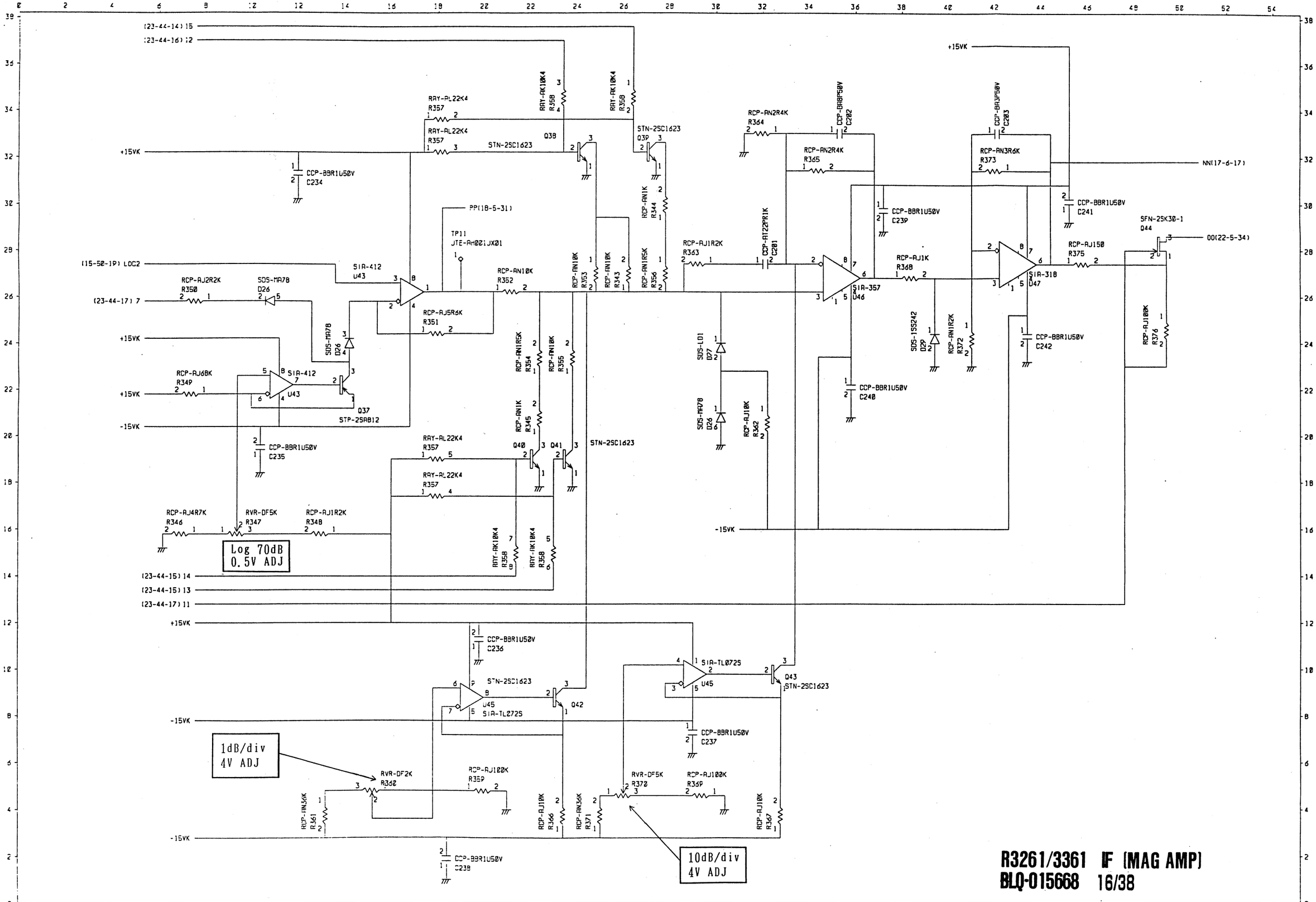


R3261/3361 IF (LOG AMP)
BLQ-015668 14/38

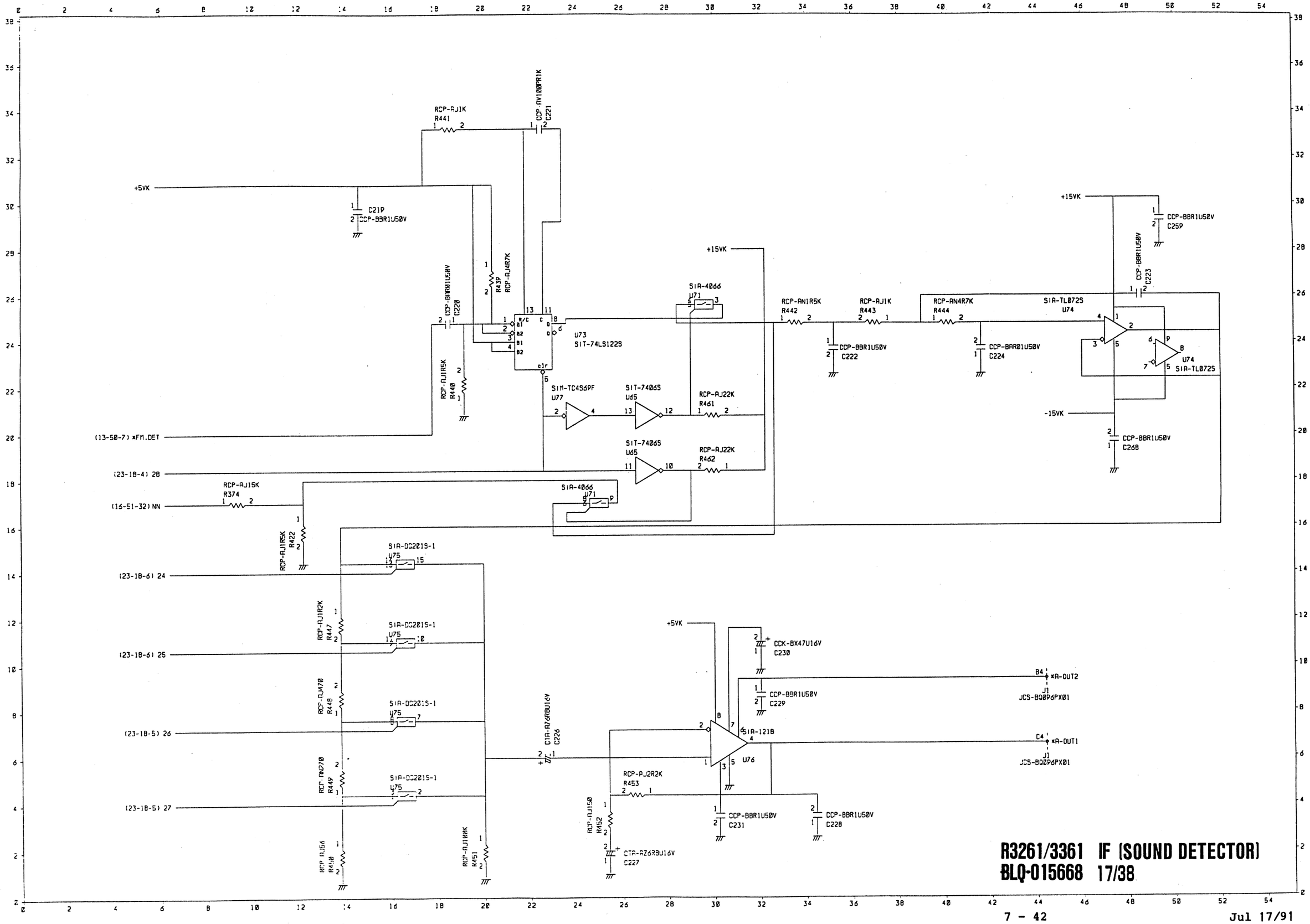
Jul 17/91



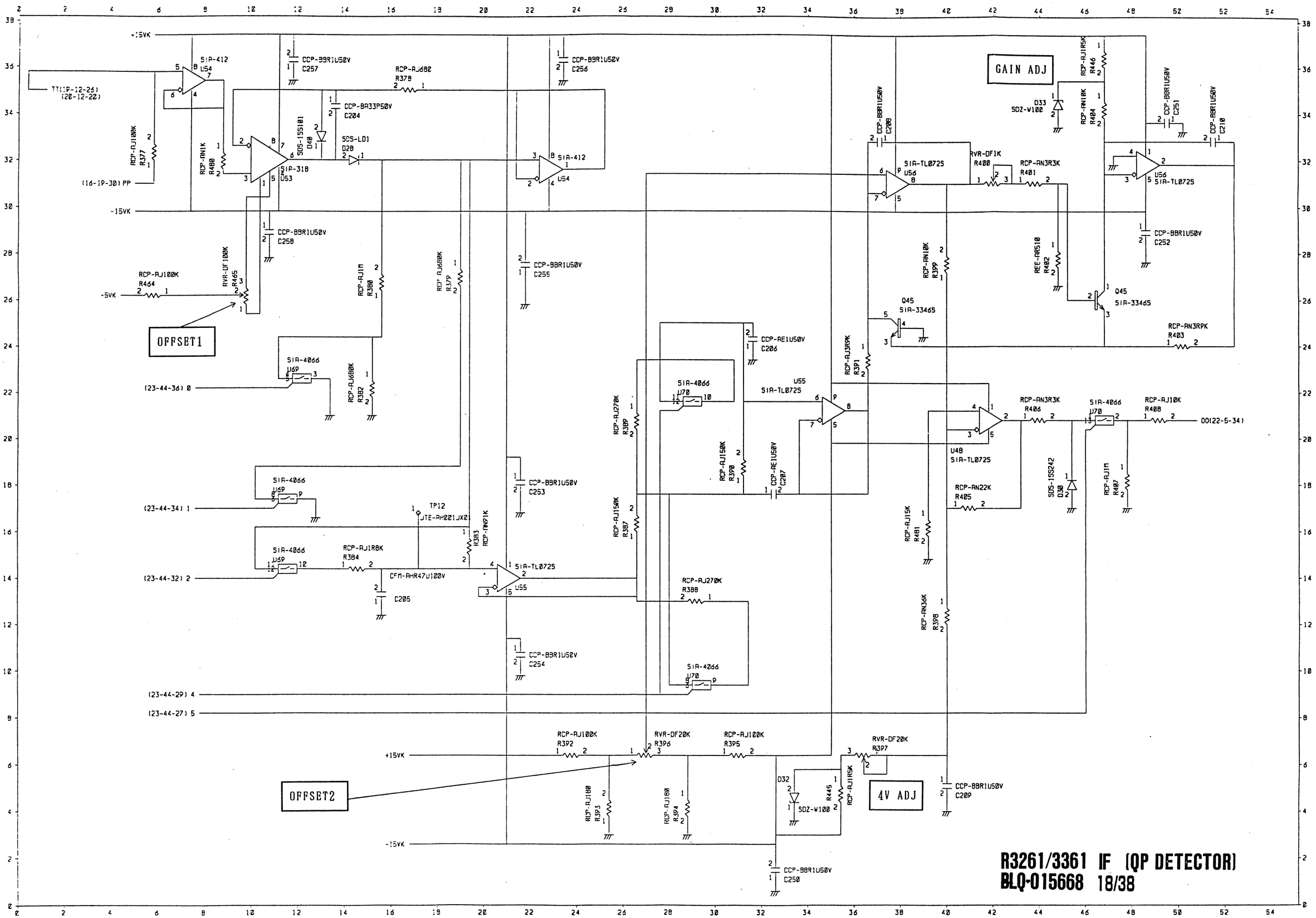
R3261/3361 IF (LOG DETECTOR)
BLQ-015668 15/38



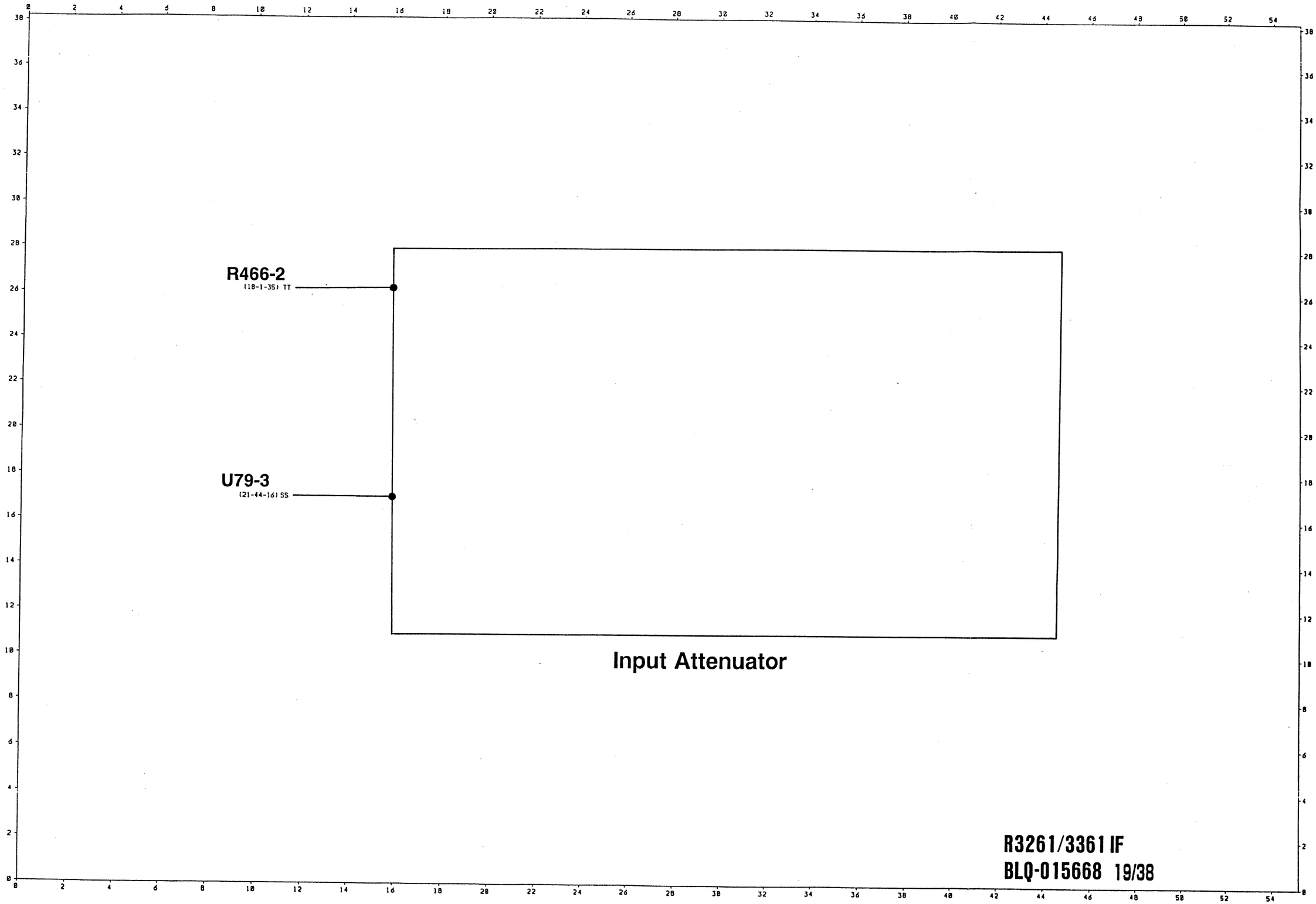
R3261/3361 IF (MAG AMP)
BLQ-015668 16/38

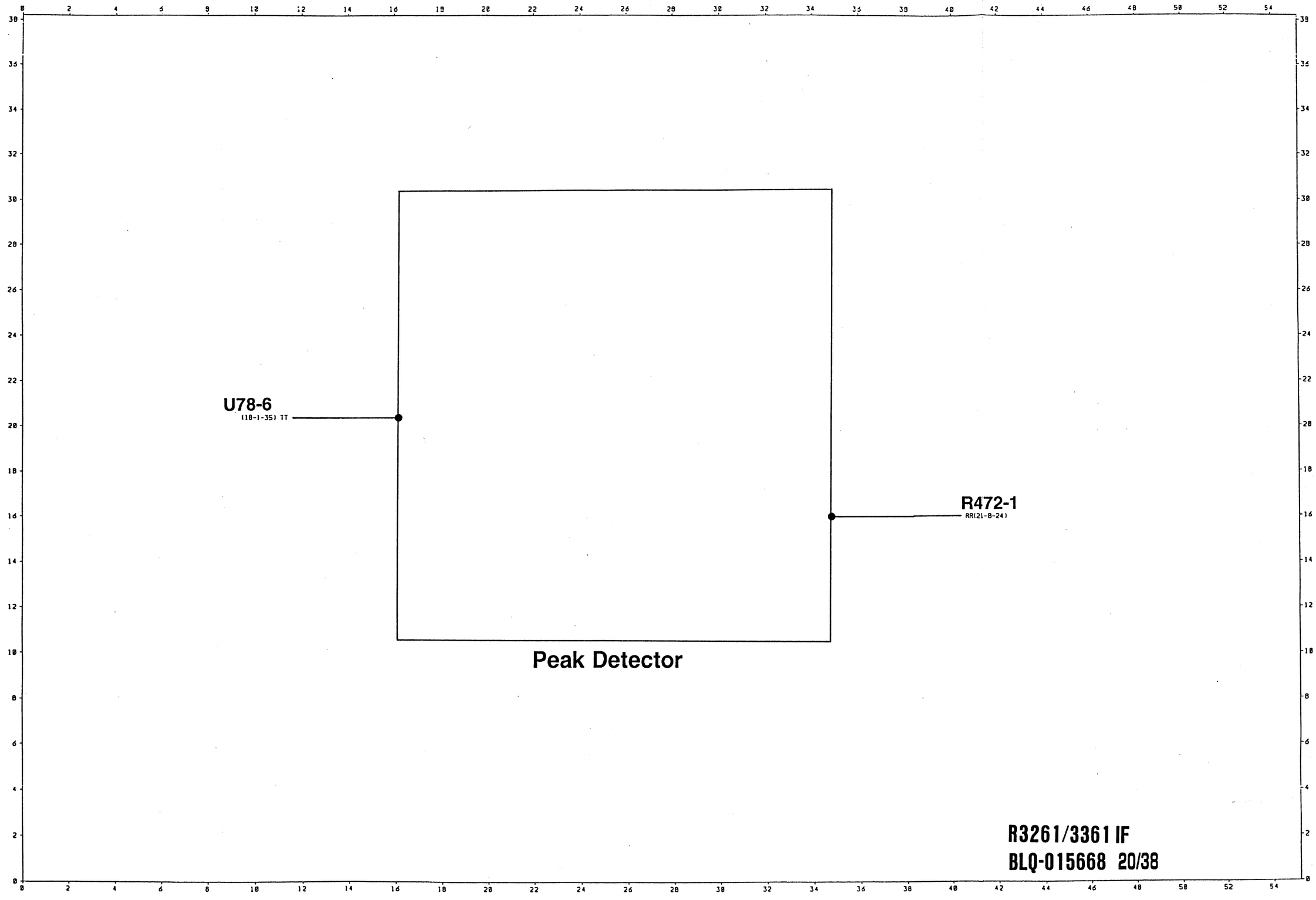


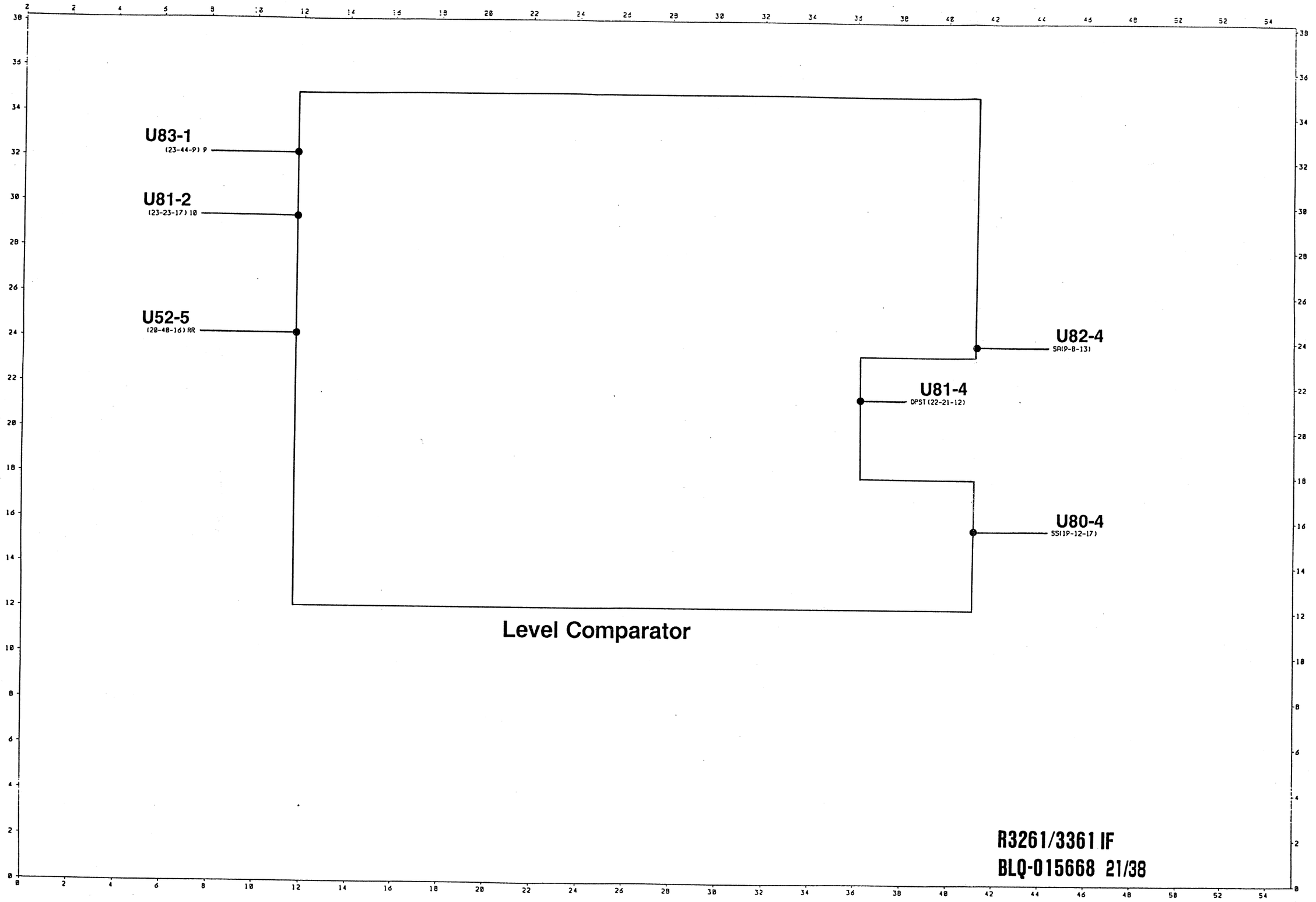
R3261/3361 IF (SOUND DETECTOR)
BLQ-015668 17/38



R3261/3361 IF (QP DETECTOR)
BLQ-015668 18/38

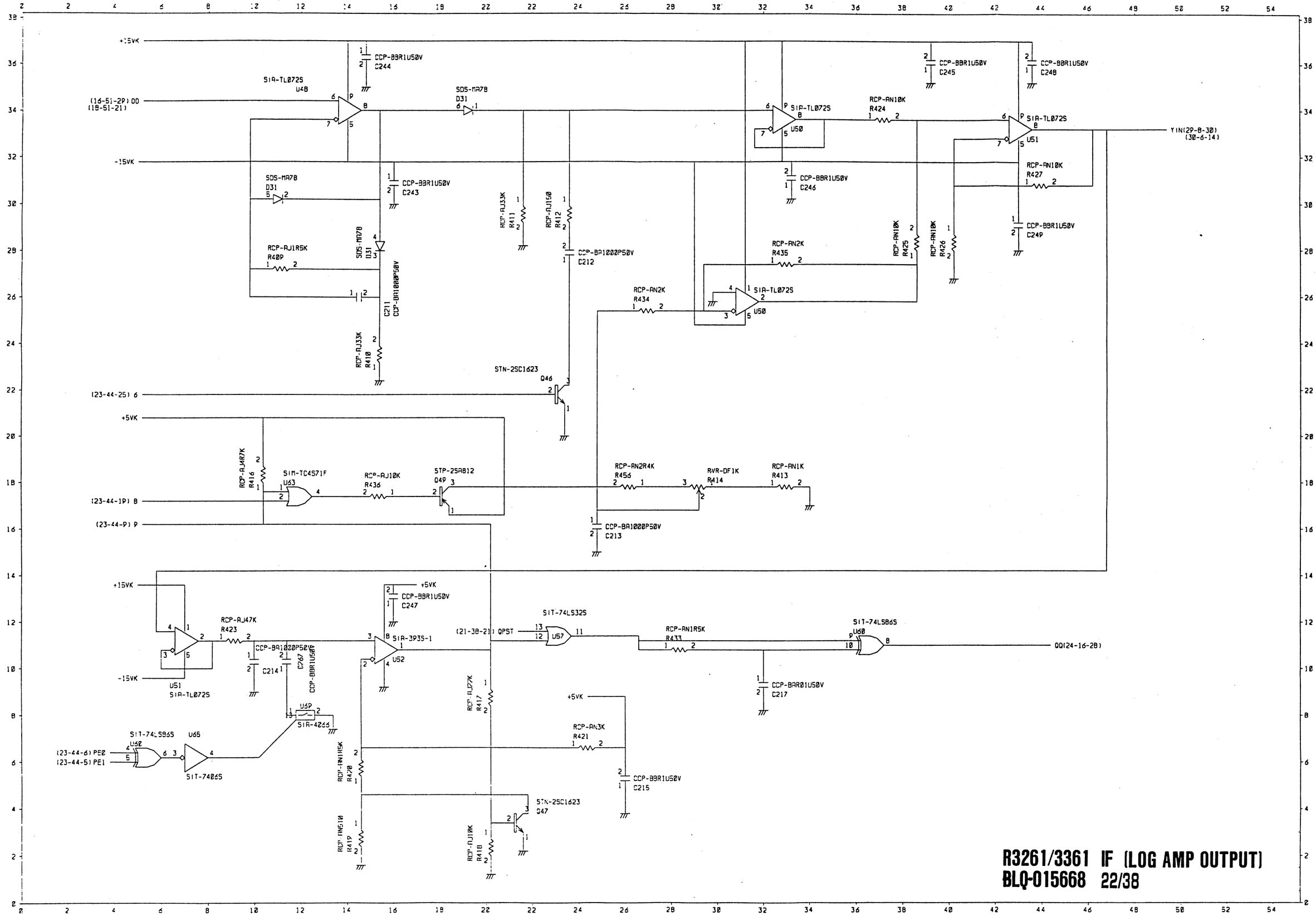

R466-2
(18-1-35) TT
U79-3
(21-44-16) SS
Input Attenuator
R3261/3361 IF
BLQ-015668 19/38


U78-6
(18-1-35) TT
R472-1
RR(21-8-24)
Peak Detector
R3261/3361 IF
BLQ-015668 20/38

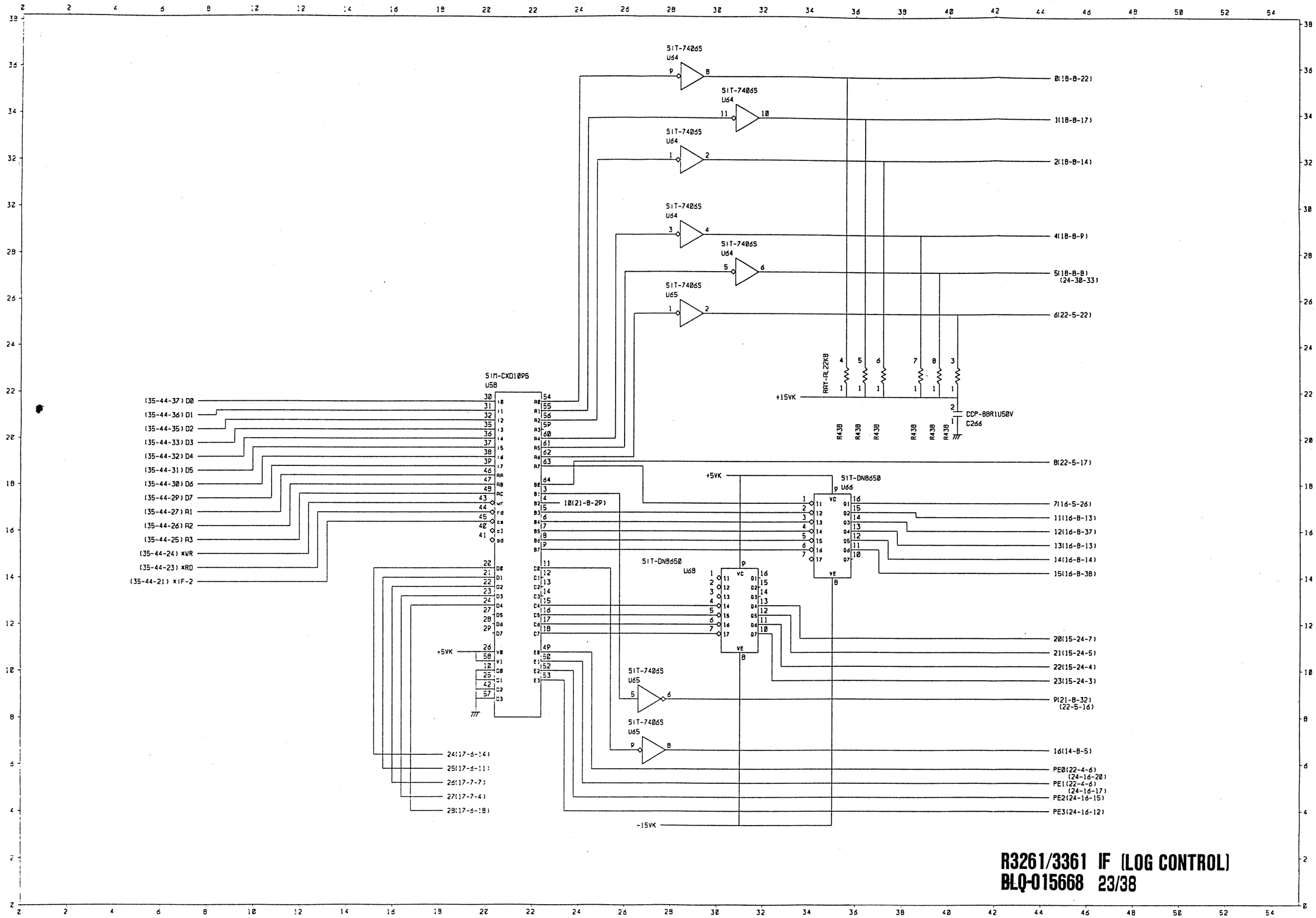


Level Comparator

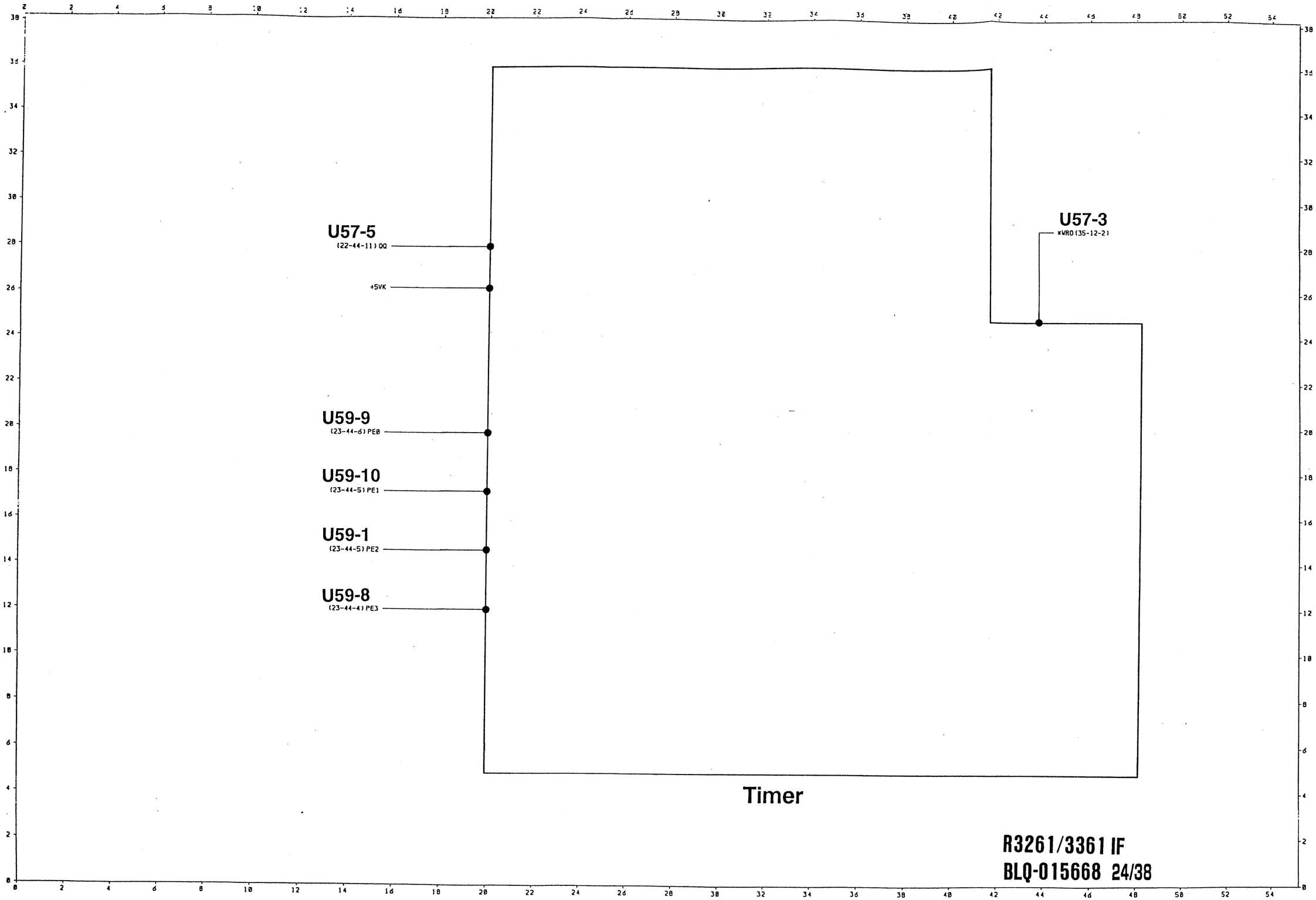
R3261/3361 IF
BLQ-015668 21/38

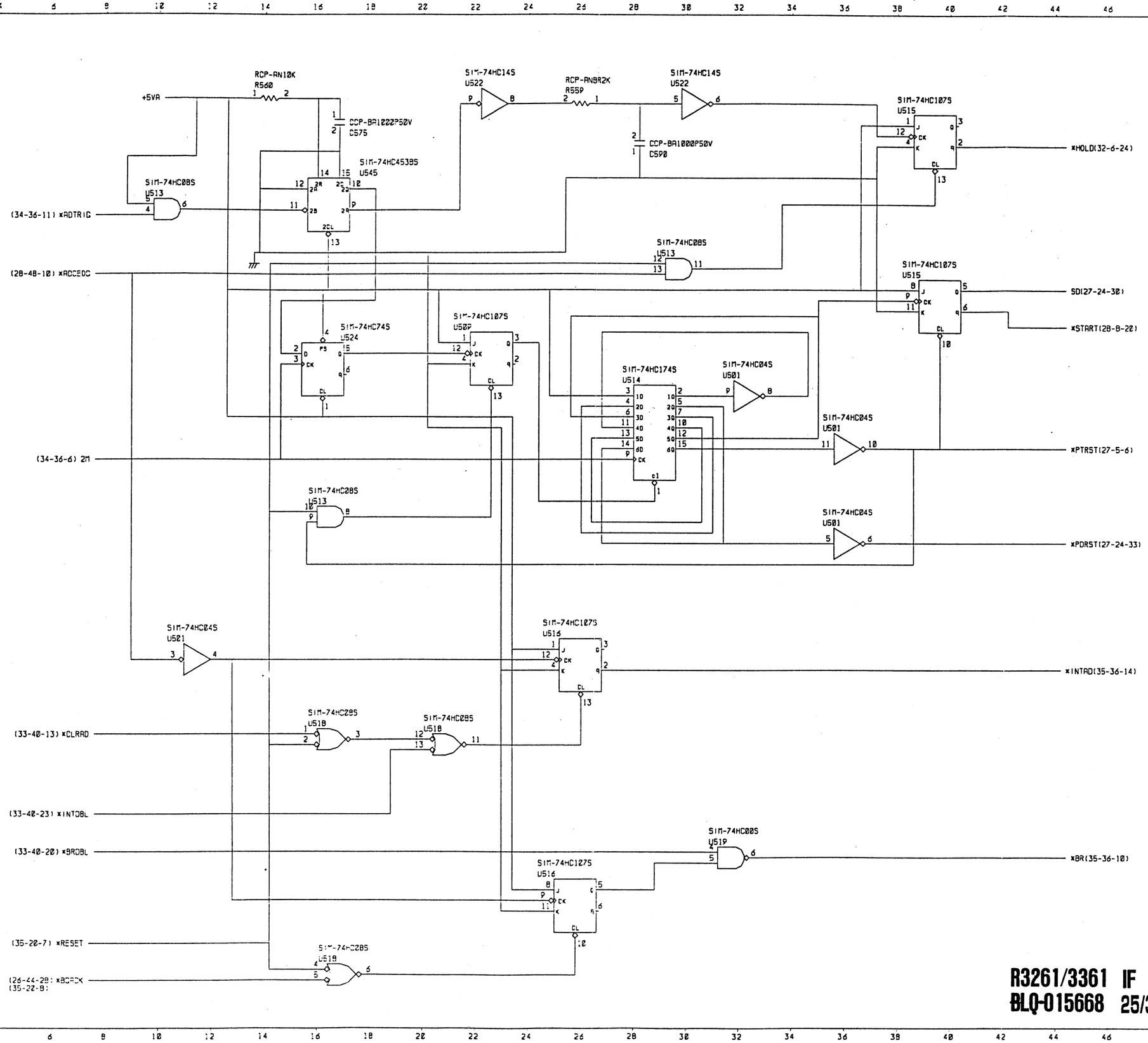


R3261/3361 IF (LOG AMP OUTPUT)
BLQ-015668 22/38

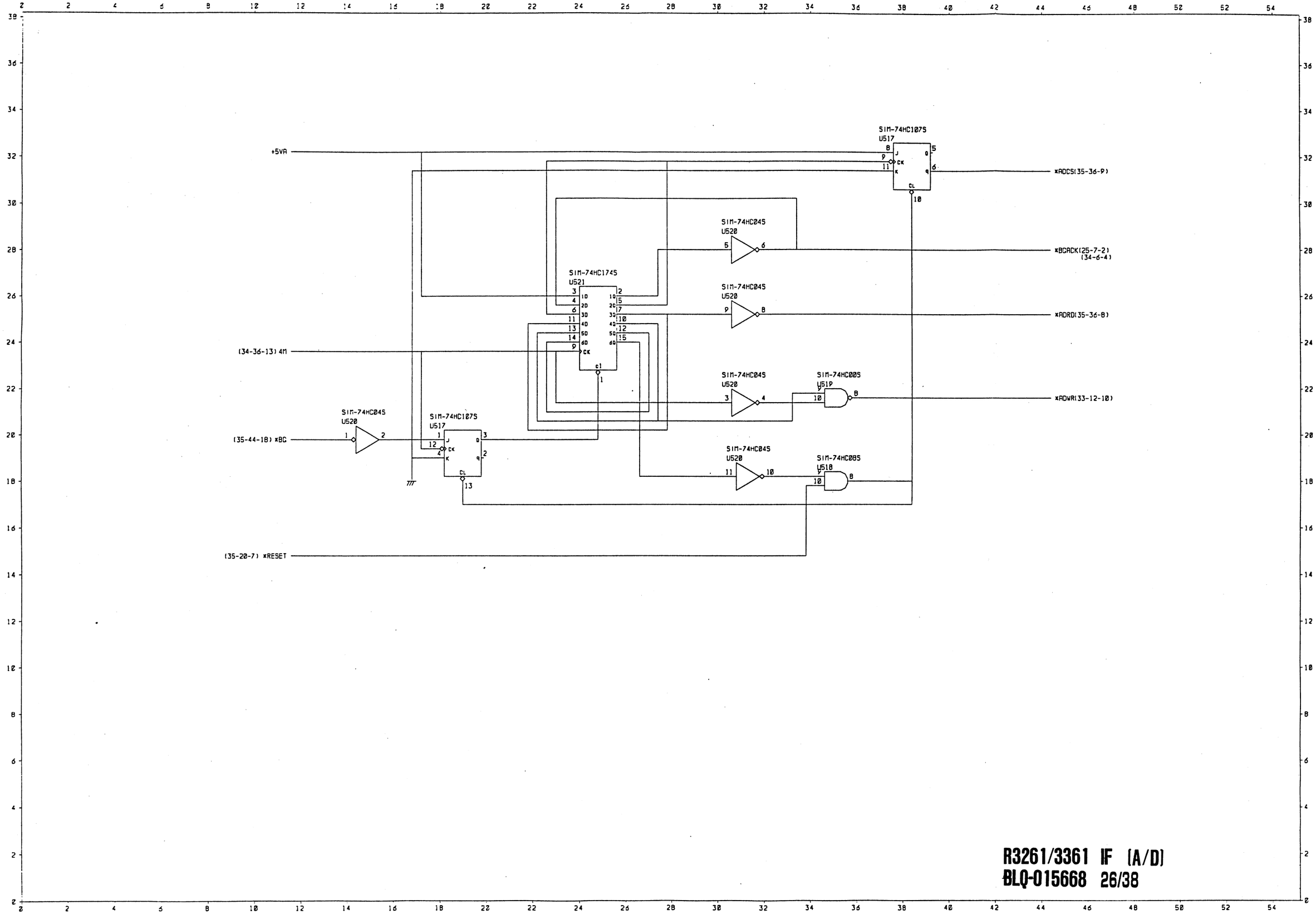


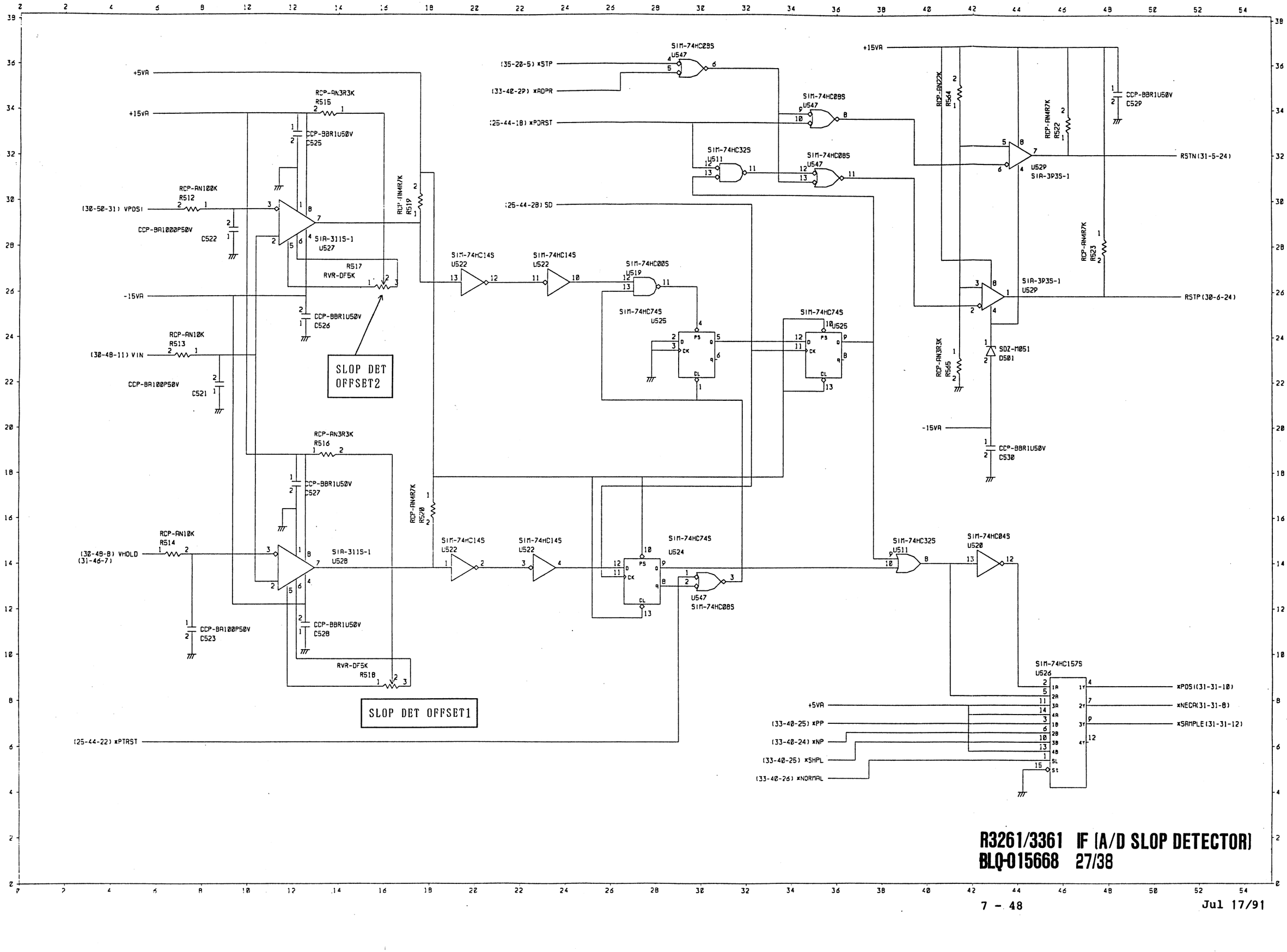
R3261/3361 IF (LOG CONTROL)
BLQ-015668 23/38


U57-5
(22-44-11) QQ
+5VK
U59-9
(23-44-6) PE0
U59-10
(23-44-5) PE1
U59-1
(23-44-5) PE2
U59-8
(23-44-4) PE3
U57-3
xWR0 (35-12-2)
Timer
R3261/3361 IF
BLQ-015668 24/38

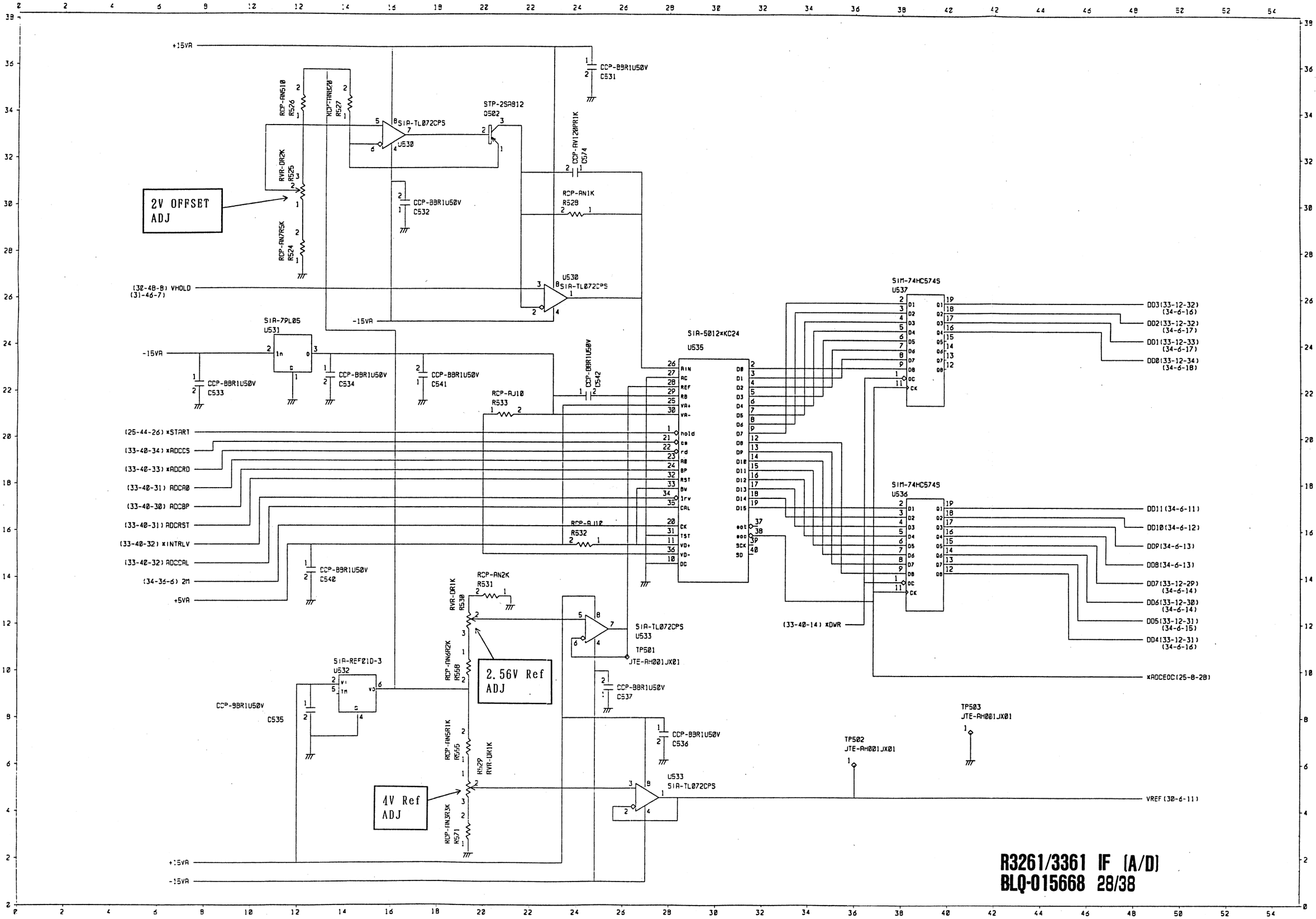


R3261/3361 IF (A/D)
BLQ-015668 25/38

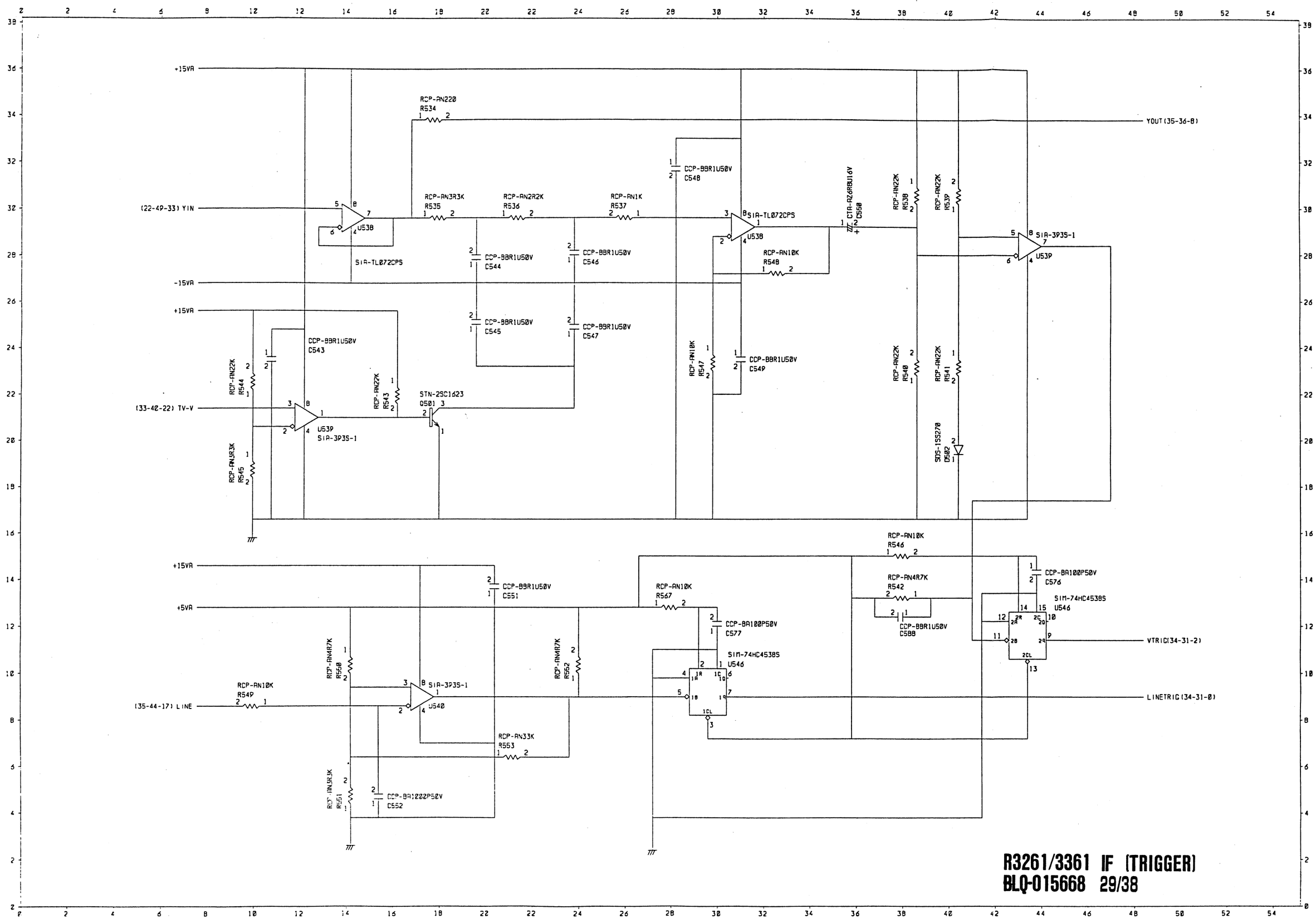

SIM-74HC107S
U517
+5VA
xADCS(35-36-9)
SIM-74HC04S
U520
xBGACK(25-7-2)
(34-6-4)
SIM-74HC174S
U521
xADRD(35-36-8)
(34-36-13) 4M
SIM-74HC00S
U519
xADWR(33-12-10)
SIM-74HC04S
U520
SIM-74HC107S
U517
(35-44-18) xBG
SIM-74HC04S
U520
SIM-74HC08S
U518
(35-20-7) xRESET



R3261/3361 IF (A/D SLOP DETECTOR)
BLQ-015668 27/38

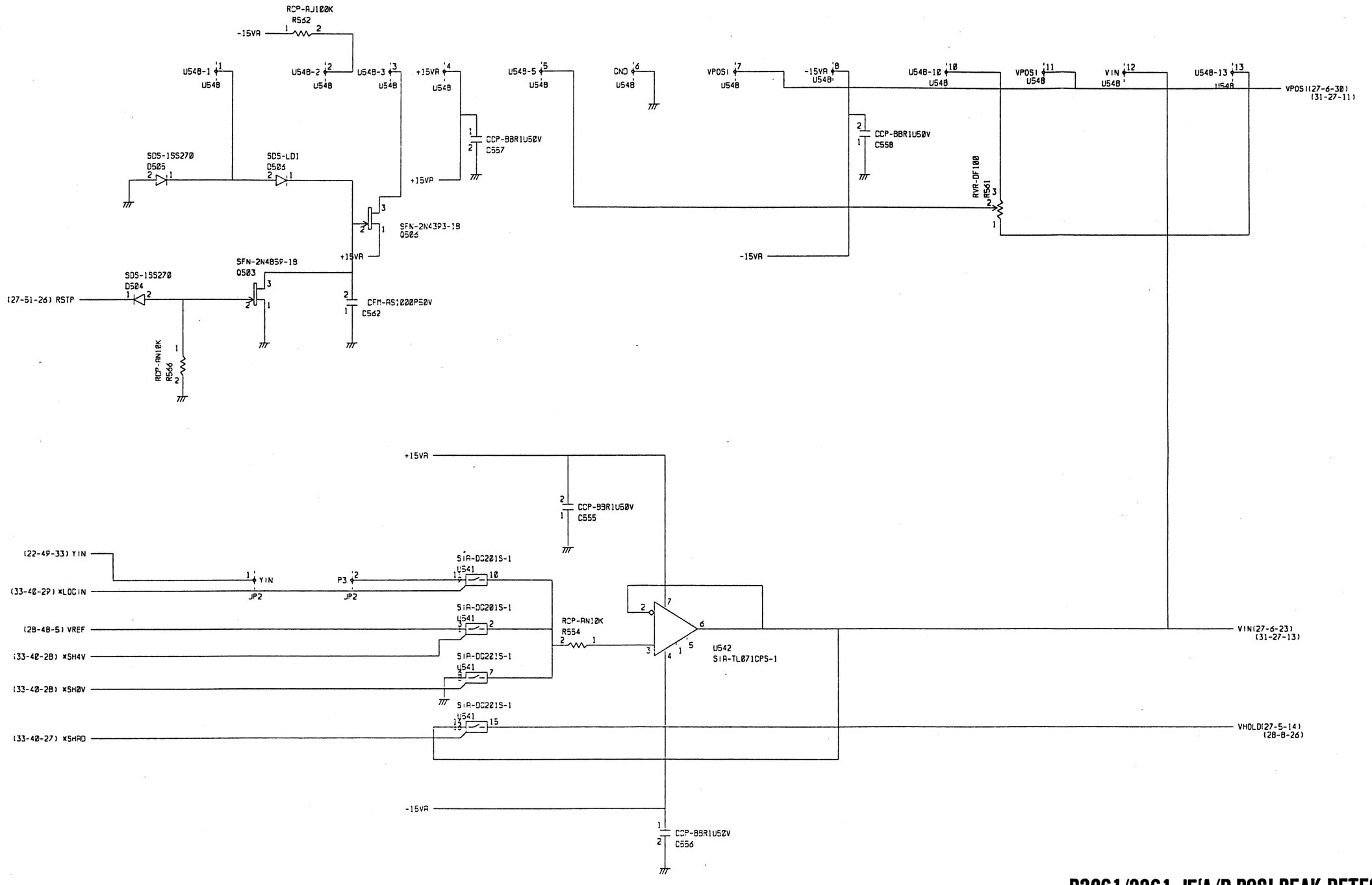


R3261/3361 IF (A/D)
BLQ-015668 28/38


+15VA
-15VA
+5VA
YOUT(35-36-8)
VTRIC(34-31-2)
LINETRIC(34-31-0)
(22-49-33) YIN
(33-40-22) TV-V
(35-44-17) LINE
RCP-AN220 R534
RCP-AN3R3K R535
RCP-AN2R2K R536
RCP-AN1K R537
RCP-AN10K R548
RCP-AN22K R538
RCP-AN22K R539
RCP-AN22K R540
RCP-AN22K R541
RCP-AN10K R547
RCP-AN22K R544
RCP-AN22K R543
RCP-AN3R3K R545
RCP-AN10K R546
RCP-AN4R7K R542
RCP-AN10K R567
RCP-AN4R7K R560
RCP-AN4R7K R552
RCP-AN10K R549
RCP-AN33K R553
RCP-AN3R3K R551
CCP-BBR1U50V C548
CCP-BBR1U50V C544
CCP-BBR1U50V C546
CCP-BBR1U50V C545
CCP-BBR1U50V C547
CCP-BBR1U50V C543
CCP-BBR1U50V C549
CCP-BBR1U50V C551
CCP-BBR1U50V C588
CCP-BA100P50V C577
CCP-BA100P50V C576
CCP-BA1000P50V C552
CTA-AZ6R8U16V C550
SIA-TL072CPS U538
SIA-3P35-1 U539
SIA-3P35-1 U540
SIA-3P35-1
SIM-74HC4538S U546
SIM-74HC4538S U546
STN-2SC1623 Q501
SDS-1SS270 D502
R3261/3361 IF (TRIGGER)
BLQ-015668 29/38

P11 - P23

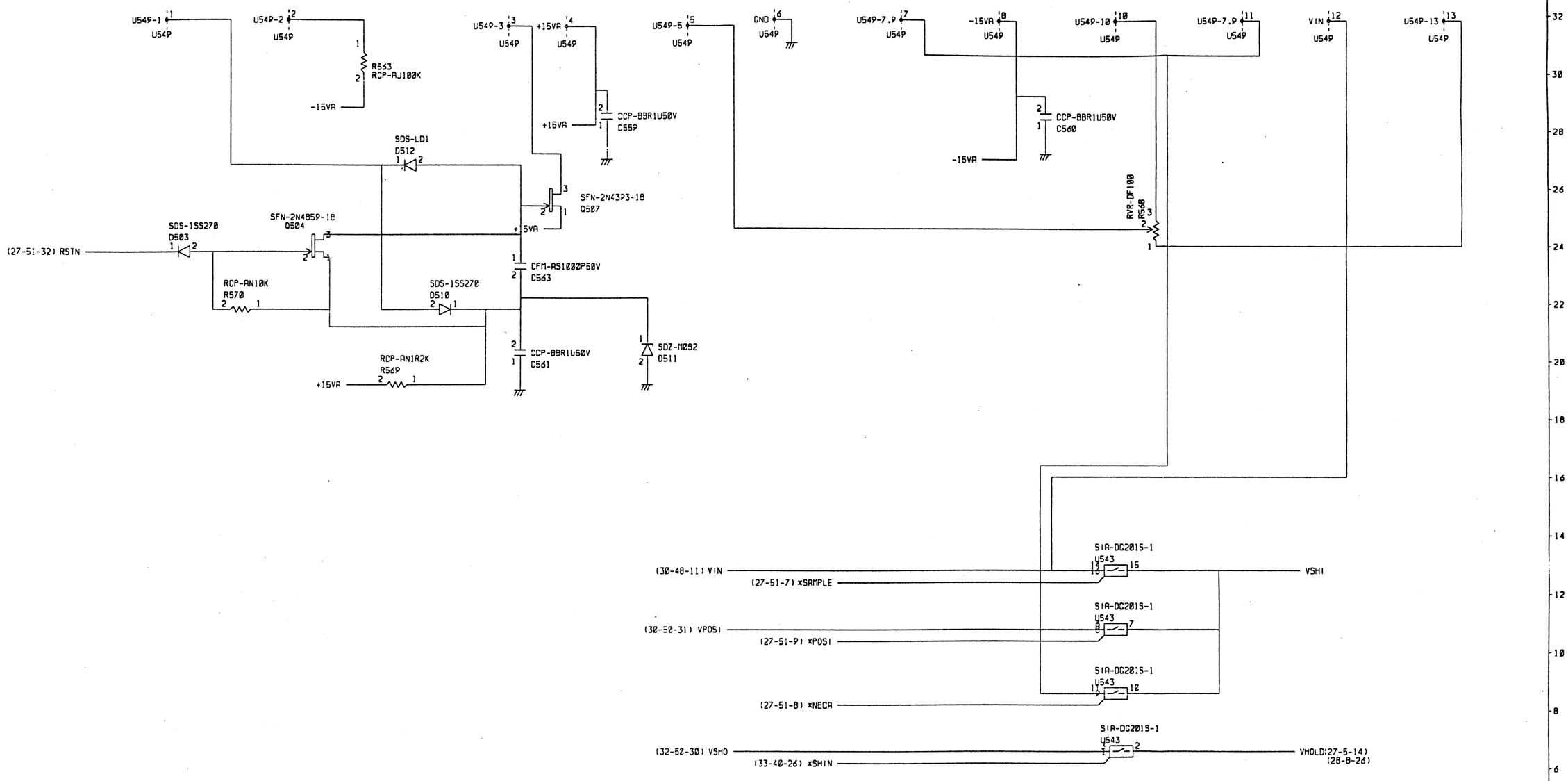
U548



R3261/3361 IF(A/D POSI PEAK DETECTOR)
BLQ-015668 30/38

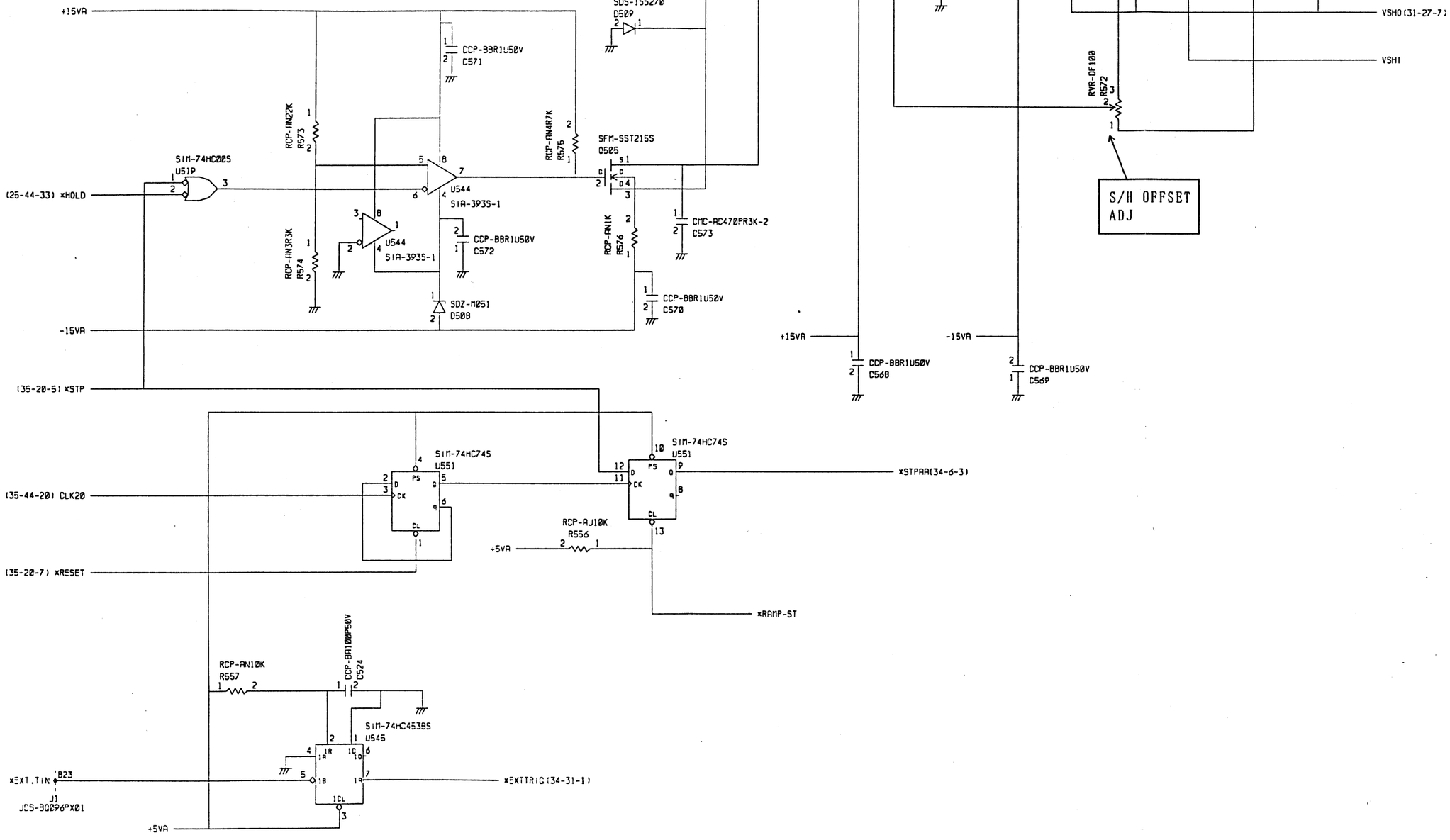
Jul 17/91

P31 - P43
U549

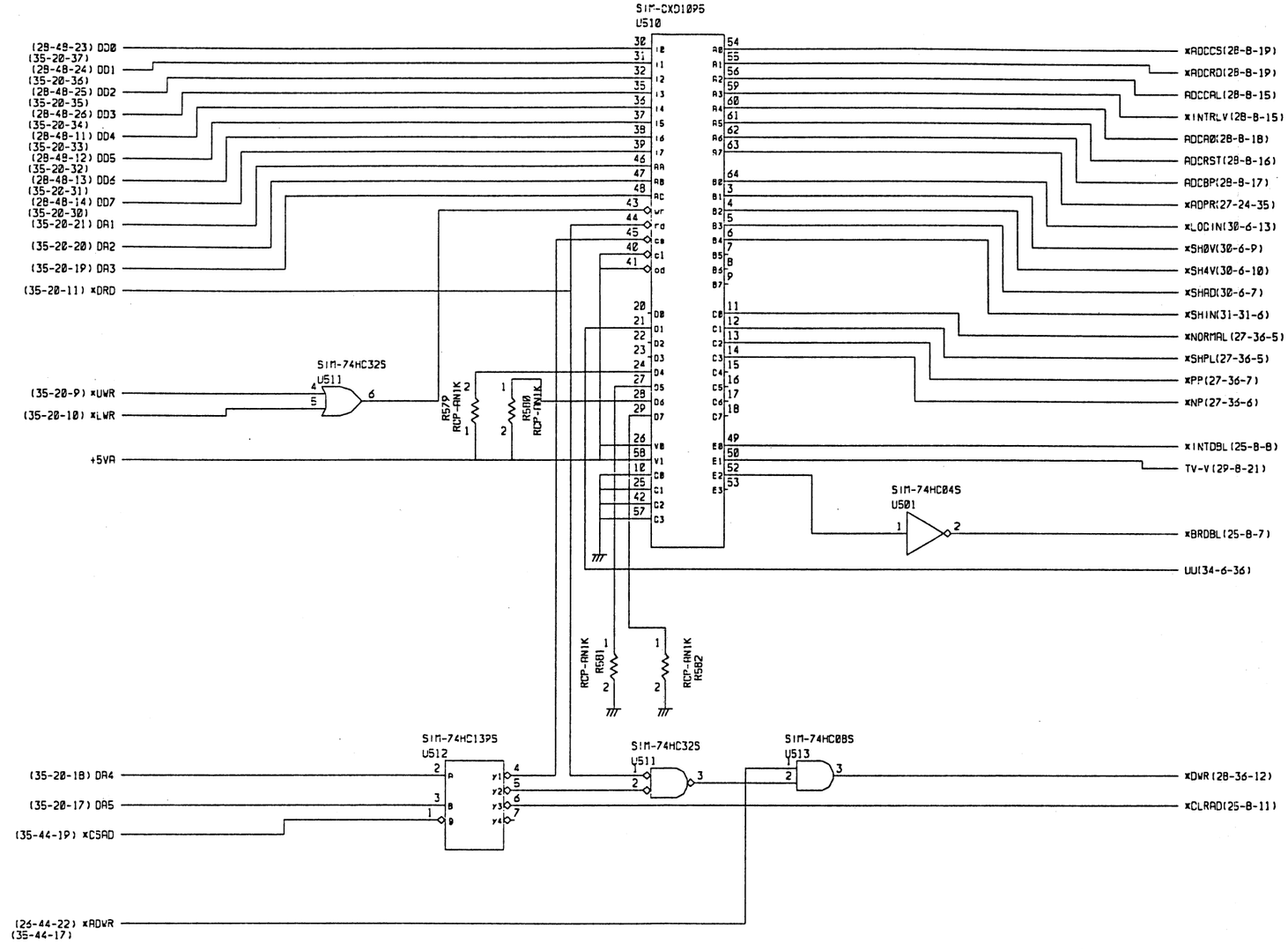


R3261/3361 IF(A/D NEGA PEAK DETECTOR)
BLQ-015668 31/38

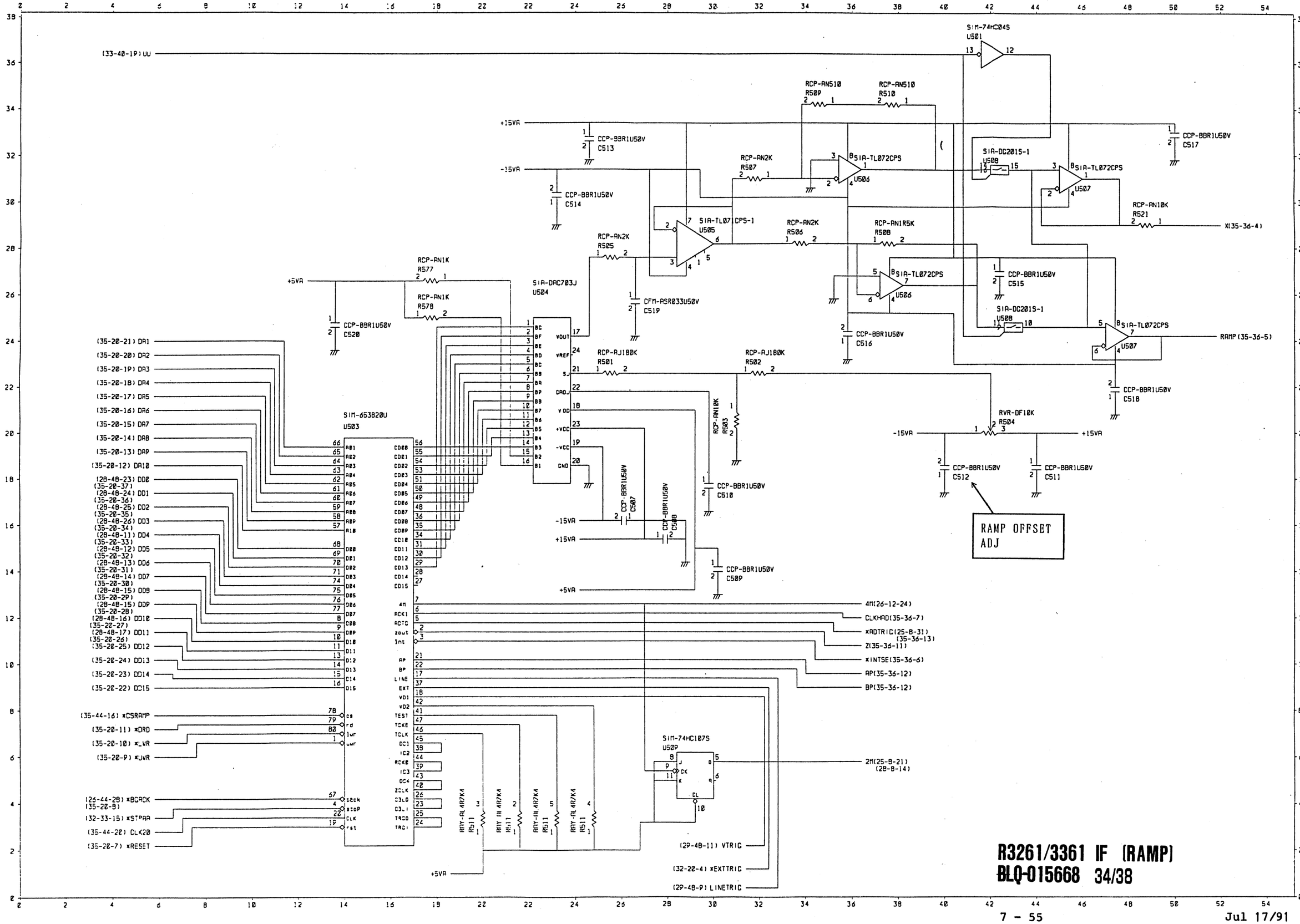
P51 - P63
U550



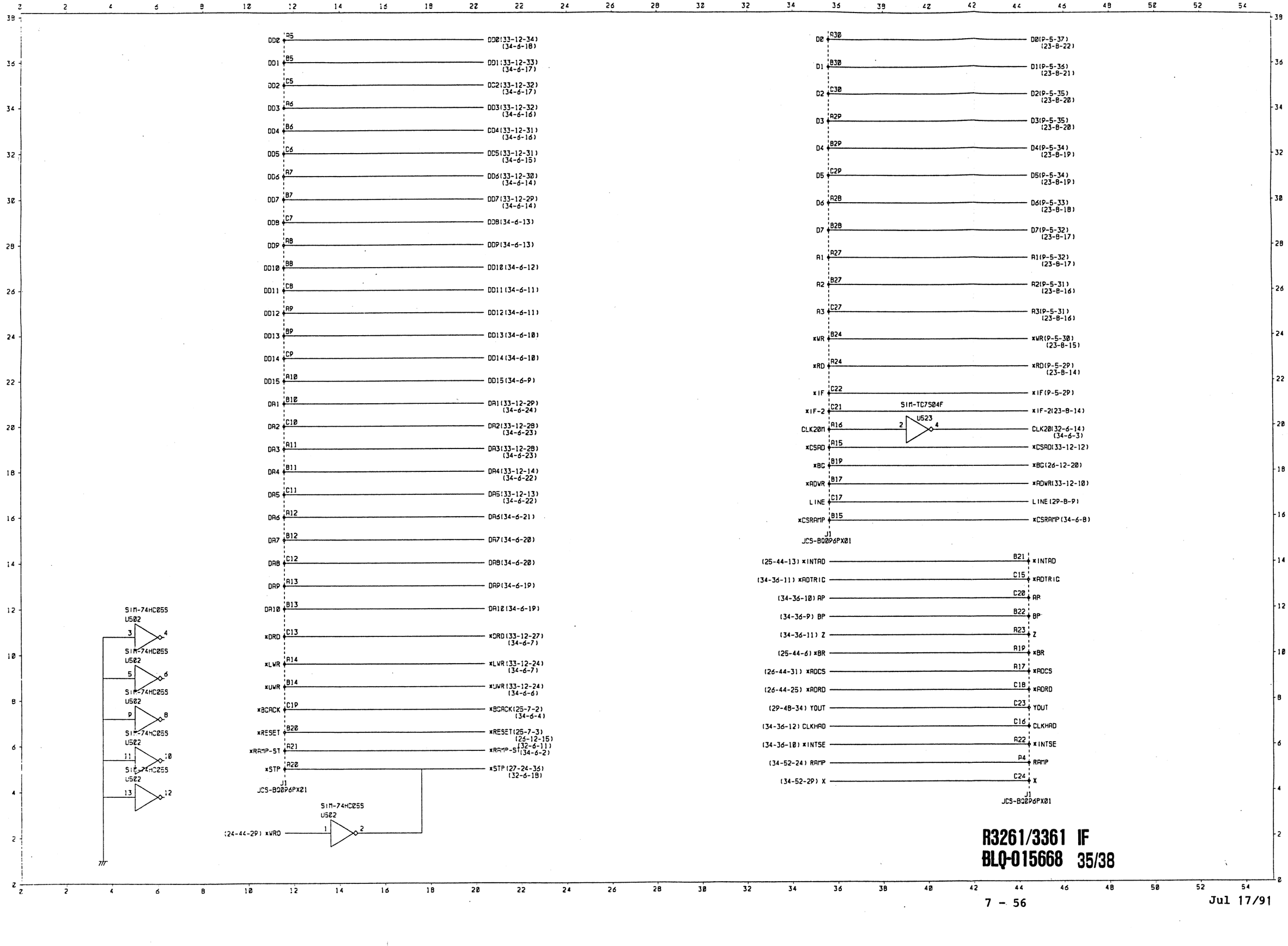
R3261/3361 IF (A/D SAMPLE HOLD DETECTOR)
BLQ-015668 32/38



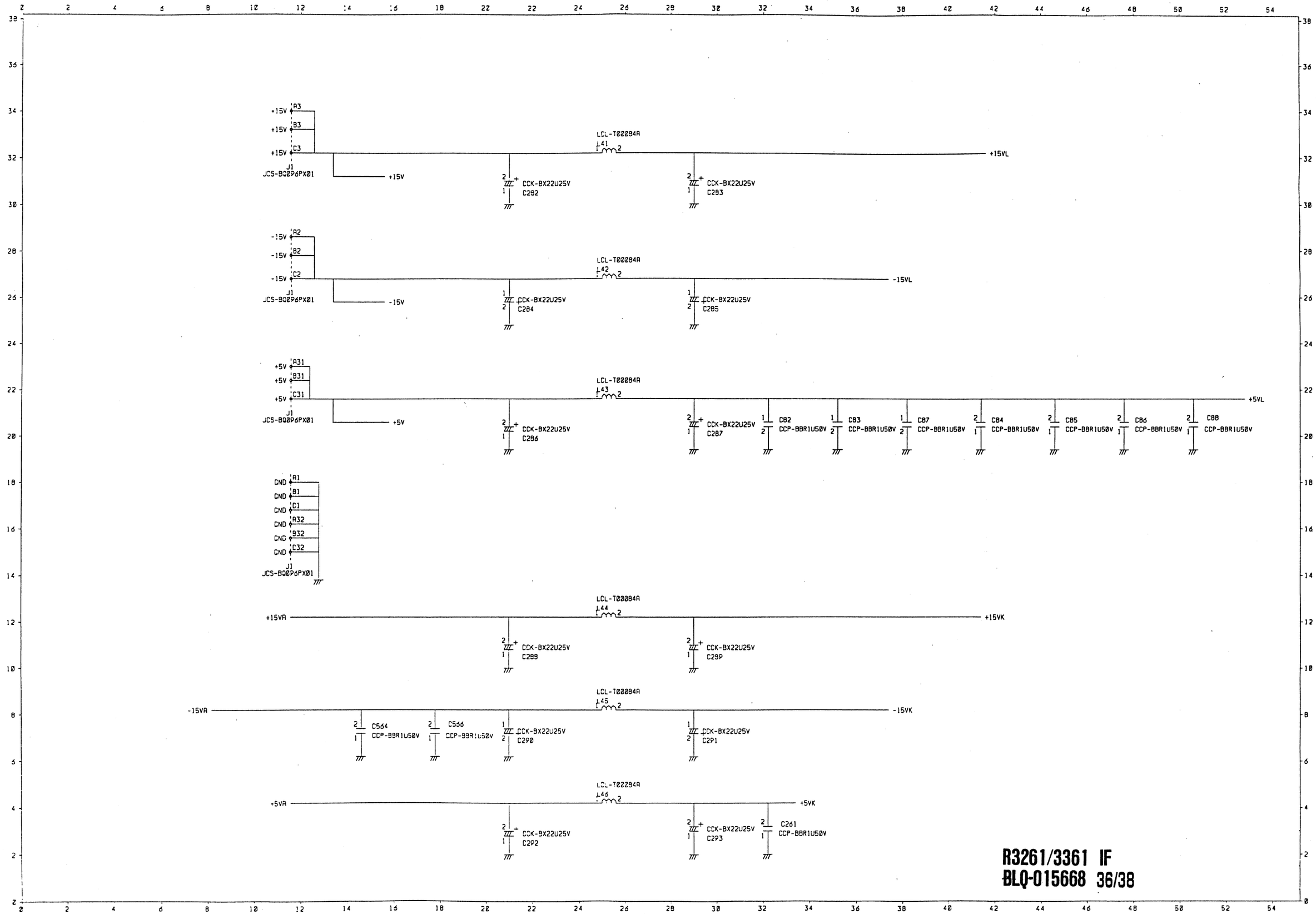
R3261/3361 IF (A/D CONTROL)
BLQ-015668 33/38



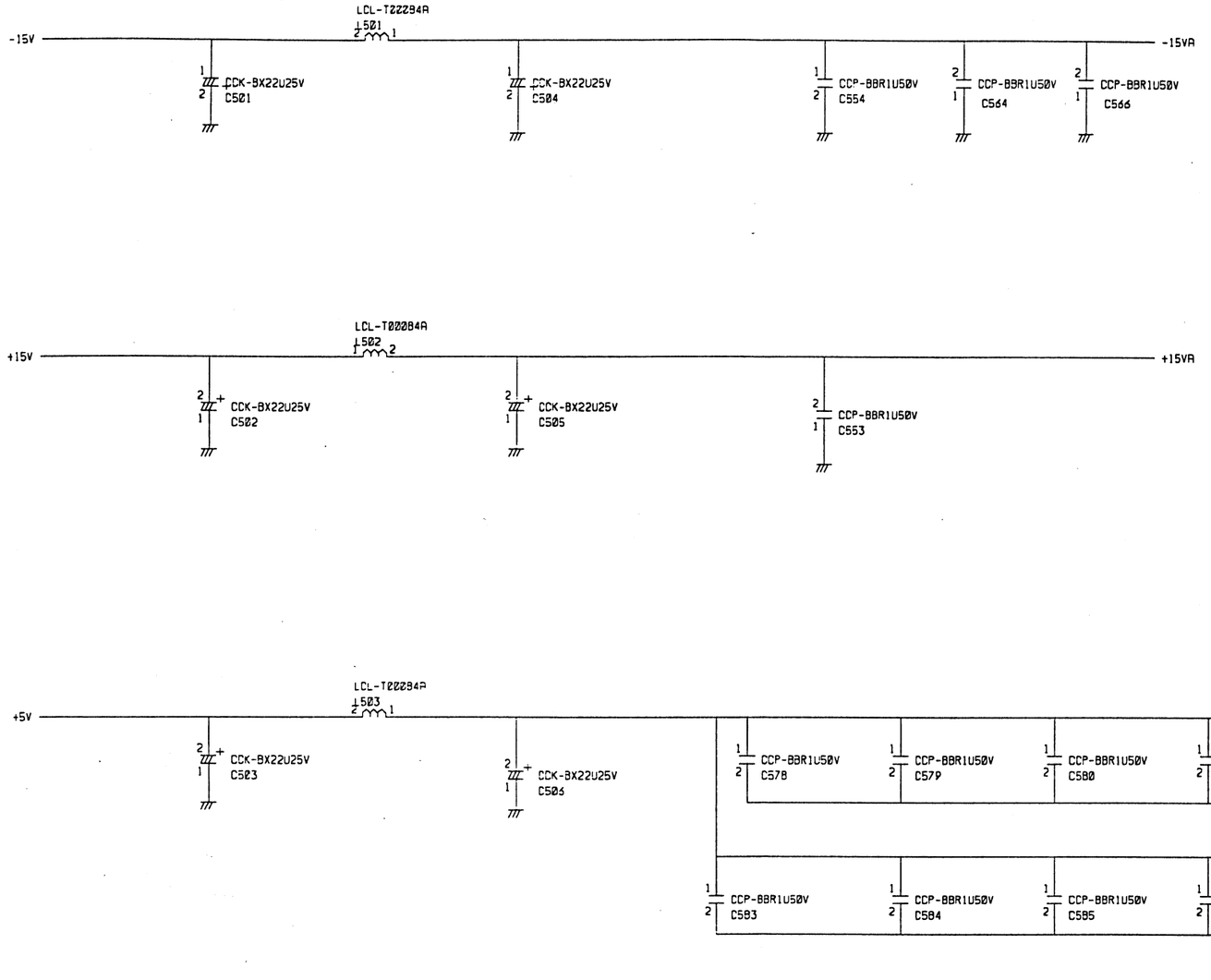
R3261/3361 IF (RAMP)
BLQ-015668 34/38



R3261/3361 IF
BLQ-015668 35/38

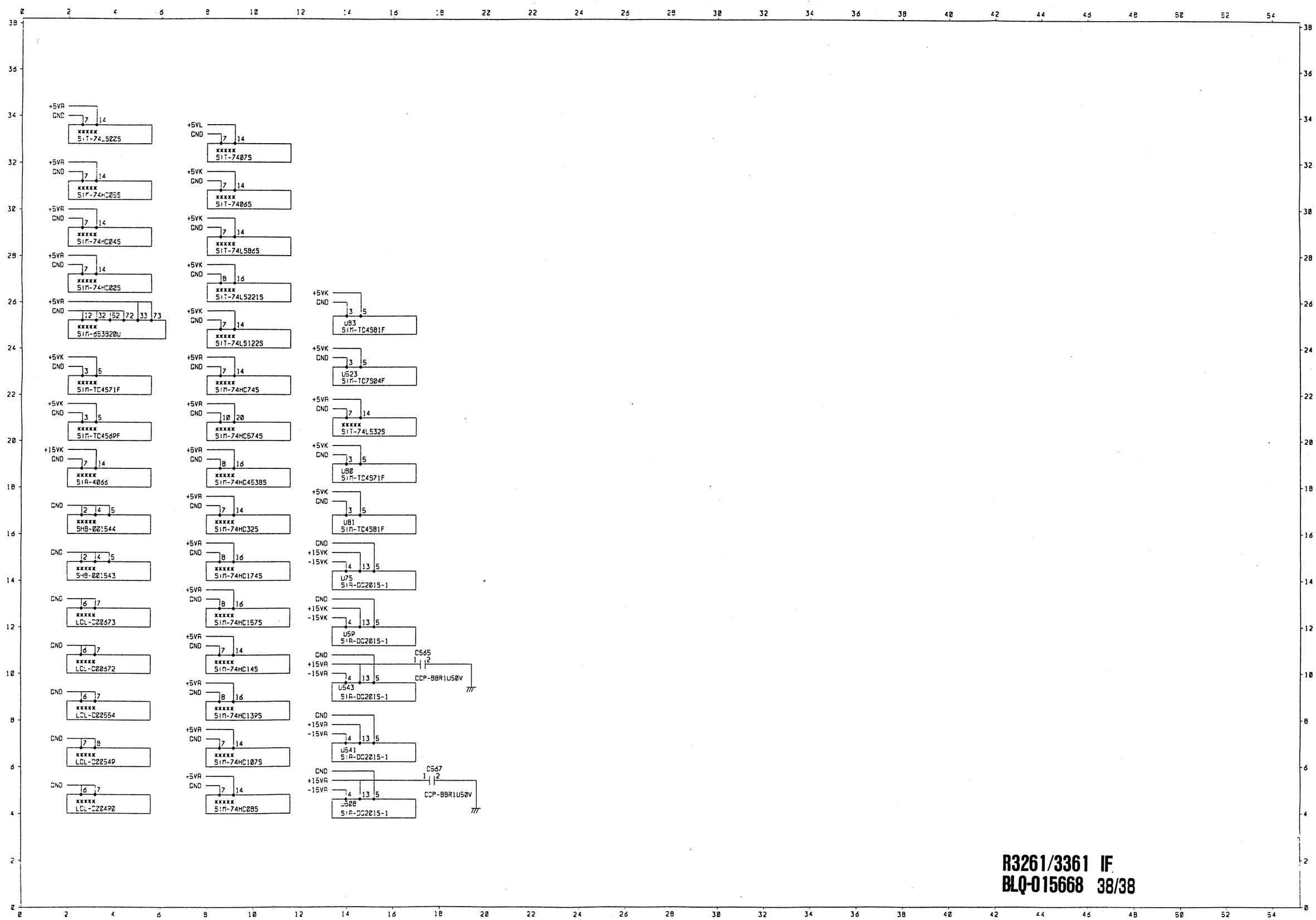


R3261/3361 IF
BLQ-015668 36/38



- PLQ-41566BFF-1
- JTT-R3201EX04
- JTT-R3201EX04
- JTT-R3201EX04

R3261/3361 IF
BLQ-015668 37/38



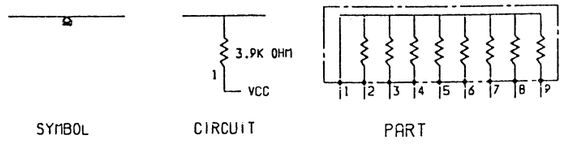
R3261/3361 IF
BLQ-015668 38/38

1. DIAGRAMS ILLUSTRATION

1-1 SYMBOLS REFERENCE DESIGNATORS

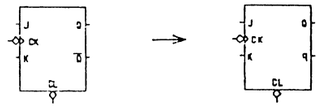
1) RESISTOR

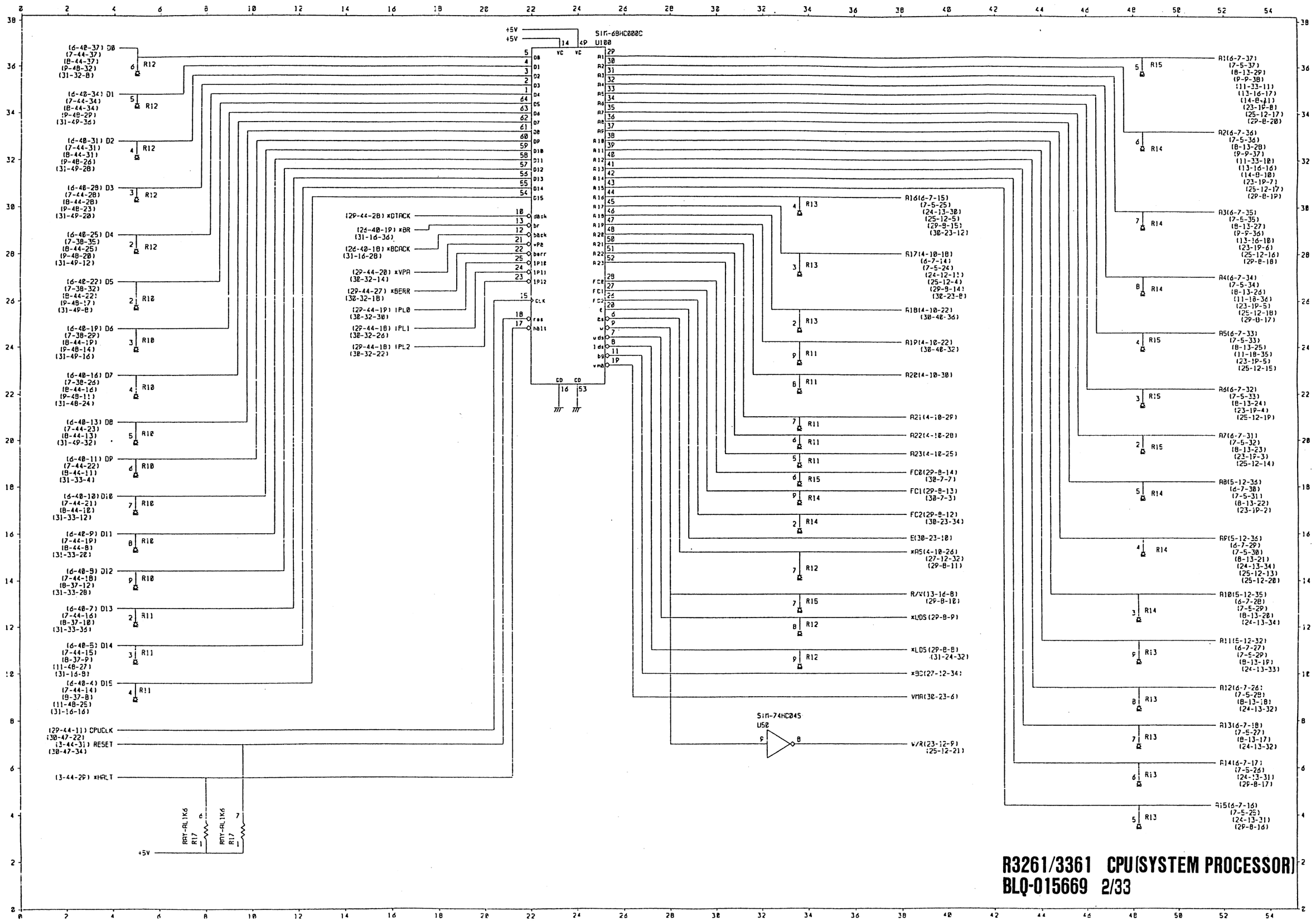
(1) RAY-AL3R9K8



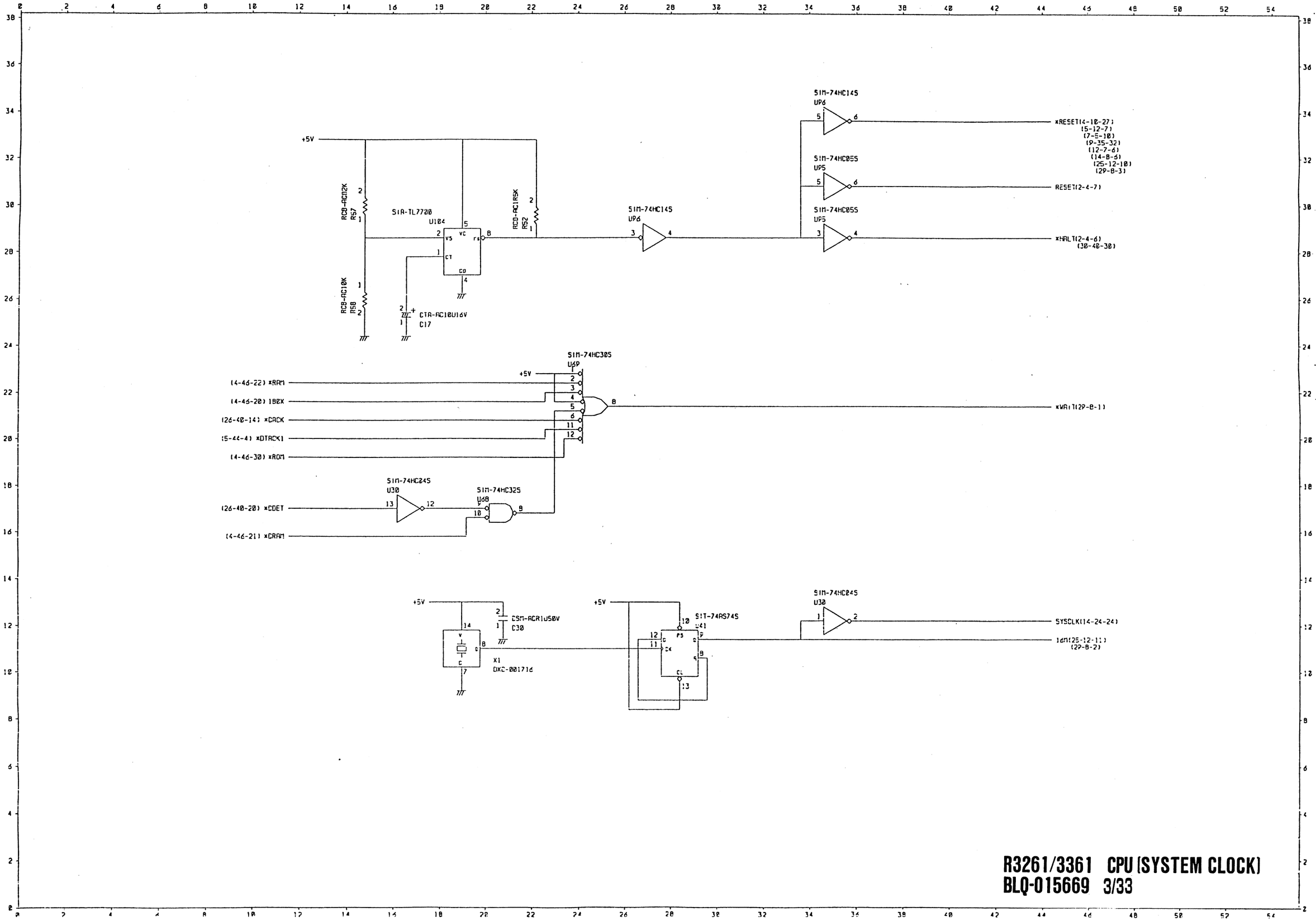
SYMBOL CIRCUIT PART

2) IC PIN NAME (PIN-NAME OF NEGATIVE LOGIC ARE DISPLAYED BY SMALL LETTER)

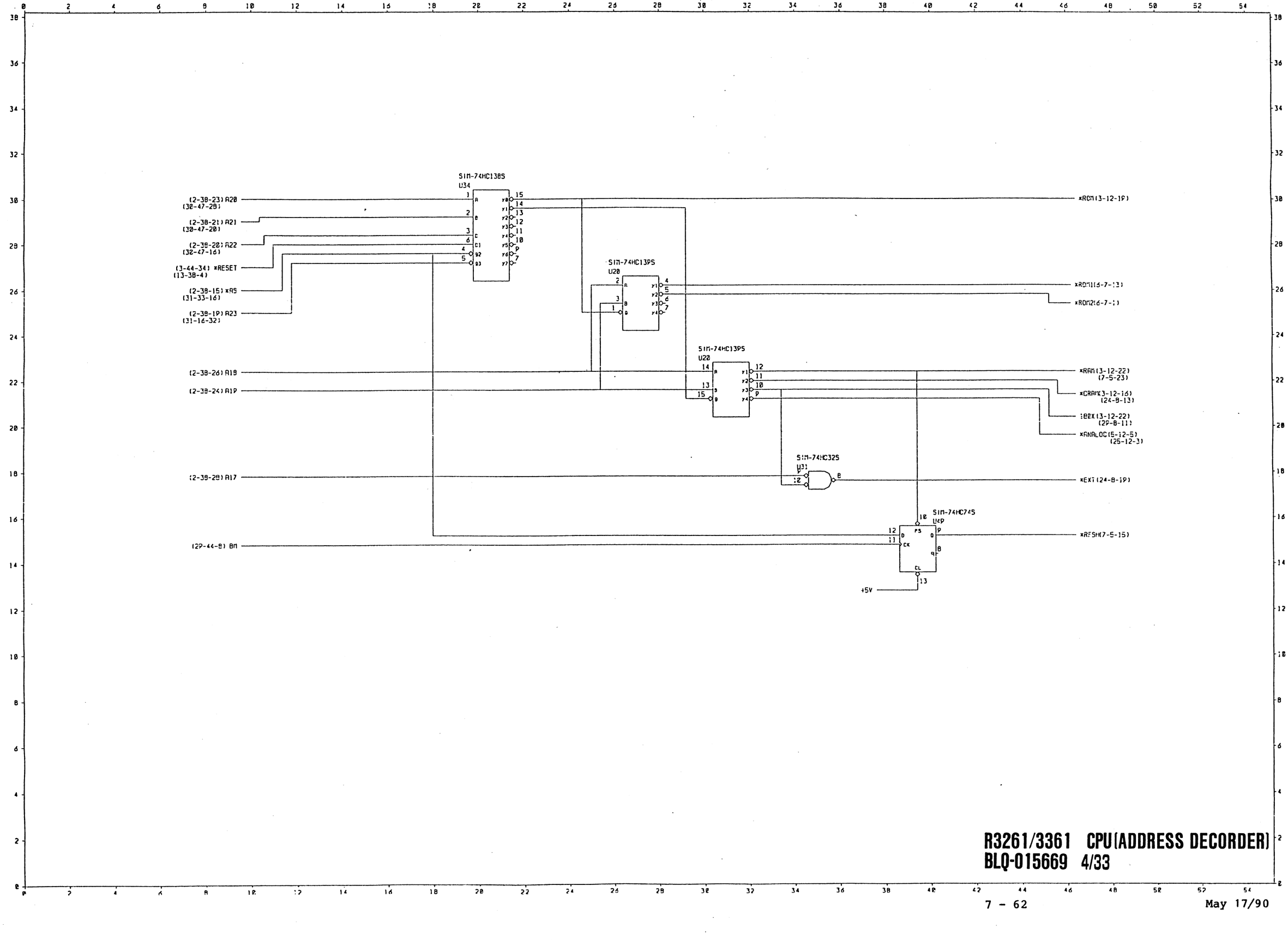




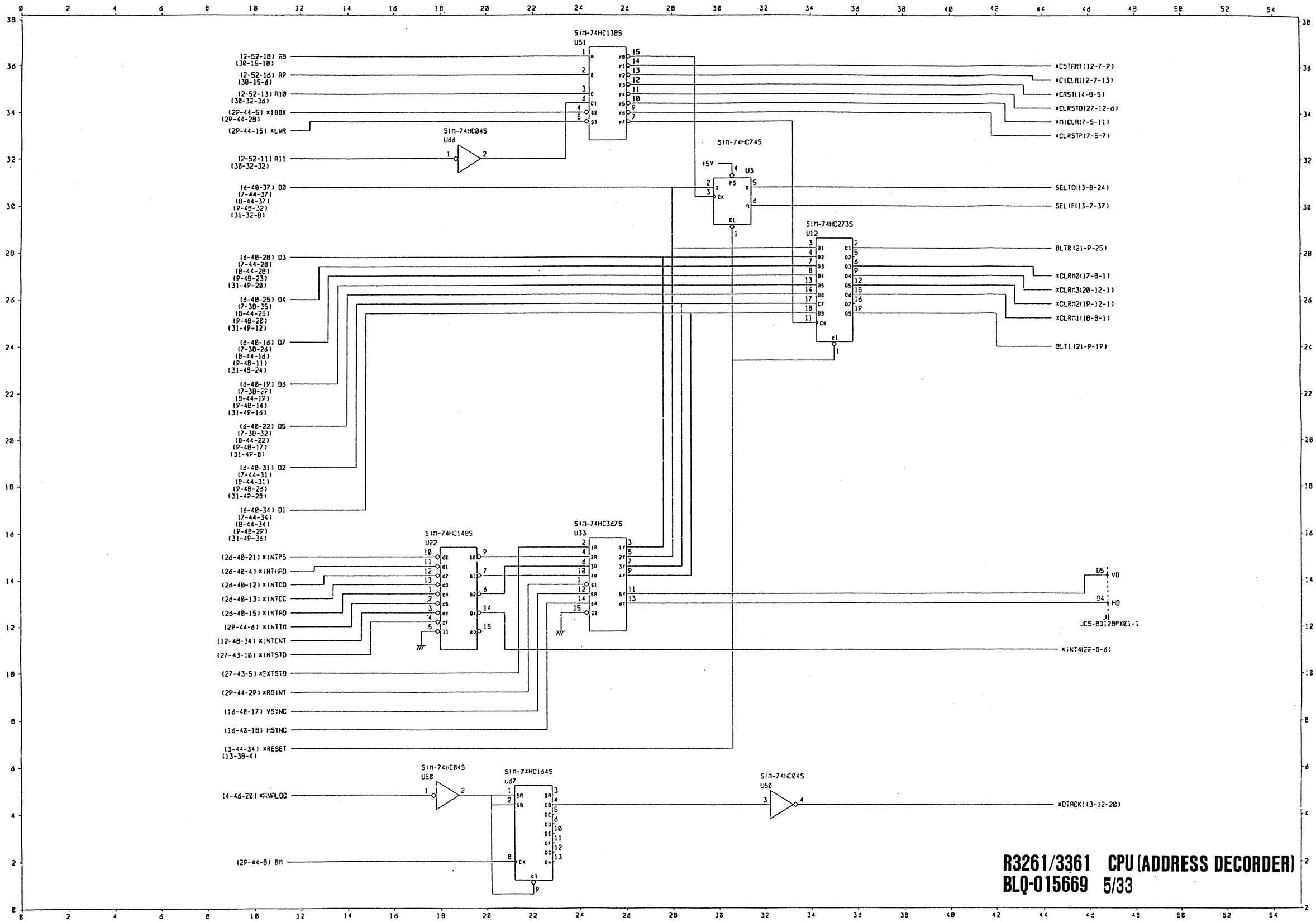
R3261/3361 CPU(SYSTEM PROCESSOR)
BLQ-015669 2/33



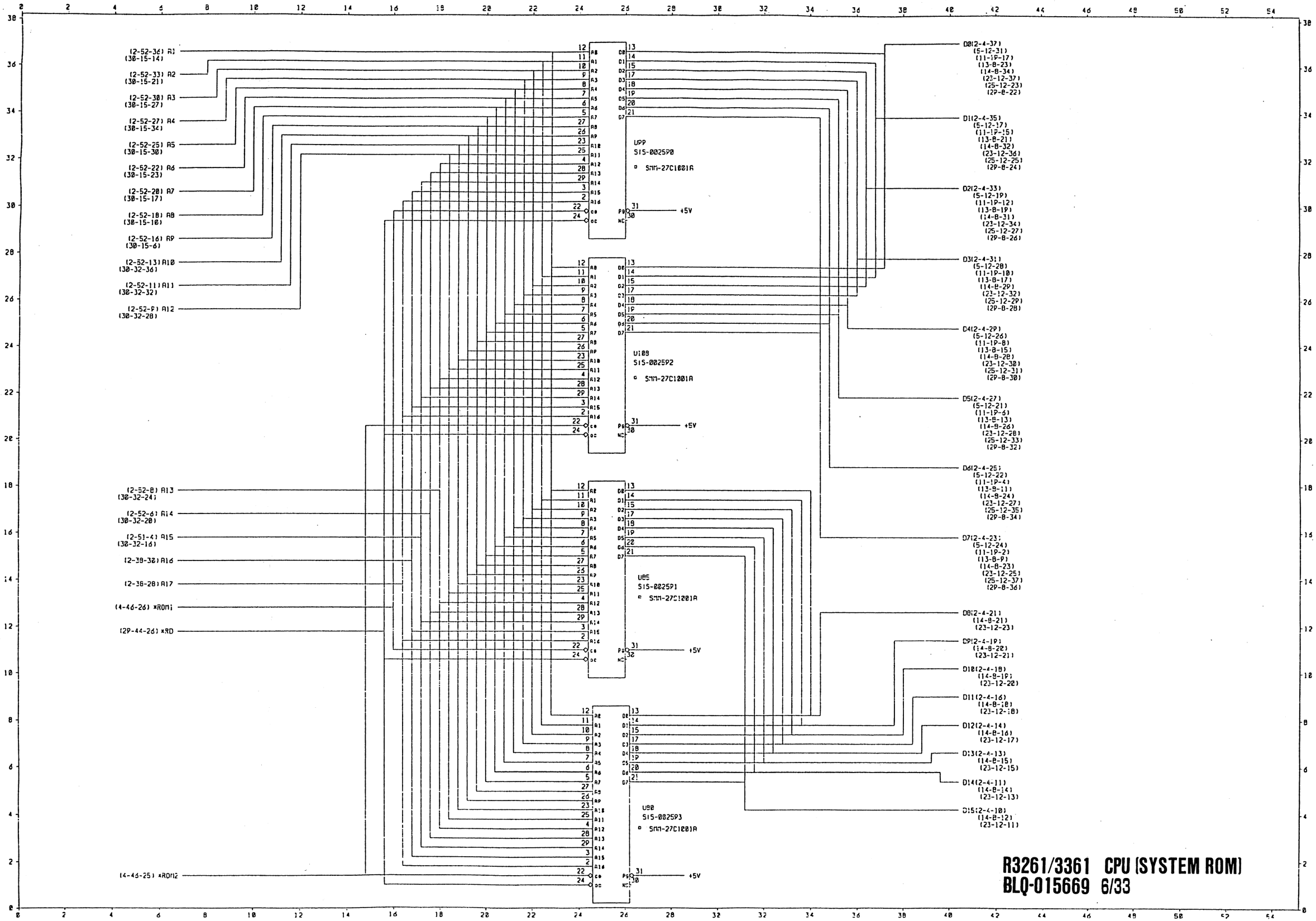
R3261/3361 CPU (SYSTEM CLOCK)
BLQ-015669 3/33



R3261/3361 CPU(ADDRESS DECORDER)
BLQ-015669 4/33



R3261/3361 CPU (ADDRESS DECORDER)
BLQ-015669 5/33



R3261/3361 CPU (SYSTEM ROM)
BLQ-015669 6/33

R3261/3361 CPU (SYSTEM RAM)
BLQ-015669 7/33

SIM-65812BDP12
U45

SIM-65812BDP12
U46

SIM-74HC08S
U45

SIM-74HC08S
U75

SIM-74HC74S

SIM-74HC05S
U95

(2-52-36) A1 (30-15-14)
(2-52-33) A2 (30-15-21)
(2-52-30) A3 (30-15-27)
(2-52-27) A4 (30-15-34)
(2-52-25) A5 (30-15-38)
(2-52-22) A6 (30-15-23)
(2-52-20) A7 (30-15-17)
(2-52-18) A8 (30-15-10)
(2-52-16) AP (30-15-6)
(2-52-13) A10 (30-32-36)
(2-52-11) A11 (30-32-32)
(2-52-9) A12 (30-32-28)
(2-52-8) A13 (30-32-24)
(2-52-6) A14 (30-32-20)
(2-51-4) A15 (30-32-16)
(2-38-38) A16
(2-38-28) A17

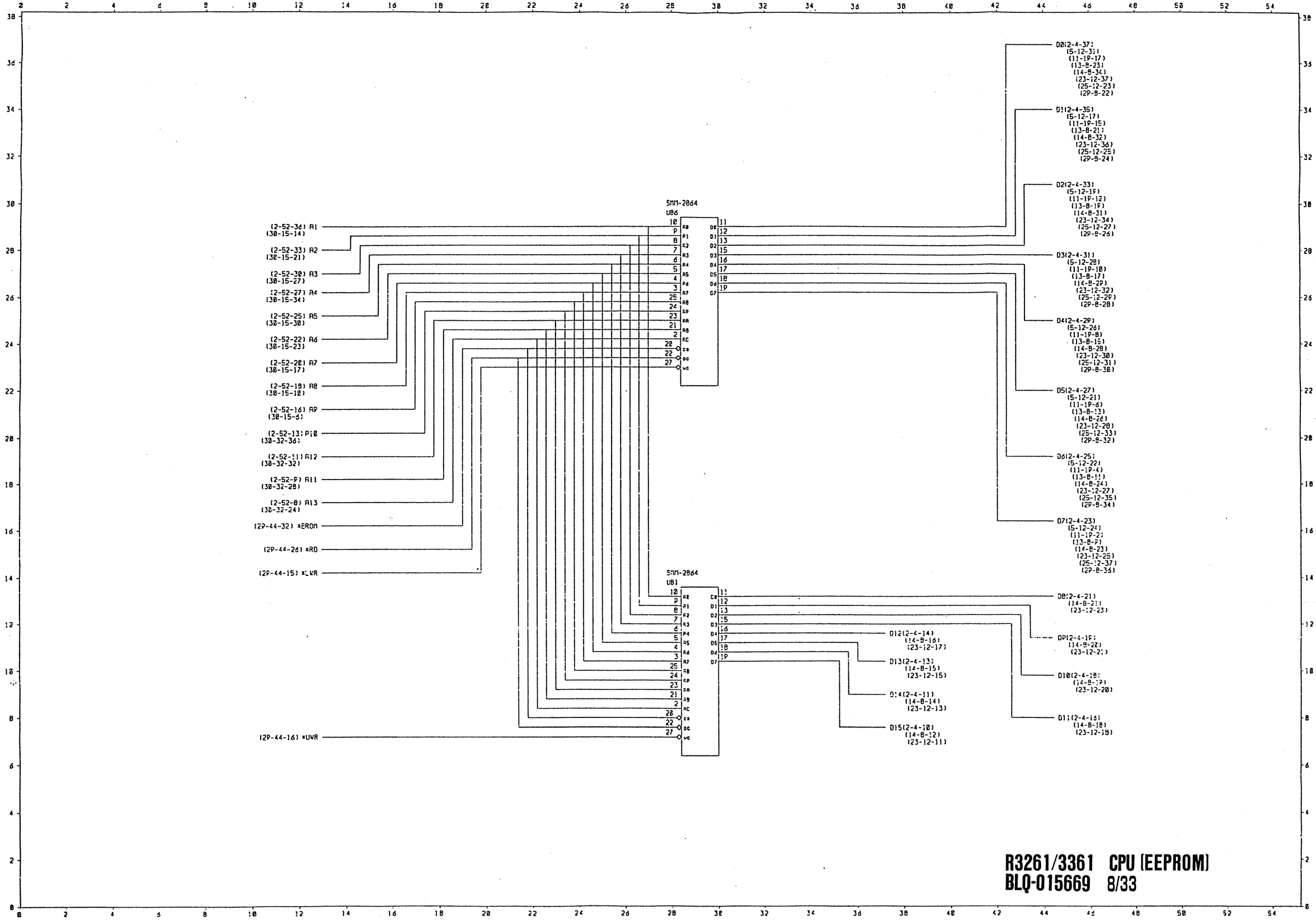
+5V

(4-46-22) *RAM
(26-40-9) *ADCS
(29-44-26) *RD
(29-44-15) *LWR
(26-40-7) *ADWR
(29-44-16) *UWR
(4-46-15) *RFSH
(26-40-8) *ADRD
(5-44-34) *RSTCLR
(3-44-34) *RESET (13-38-4)
(5-44-33) *CLRSTP

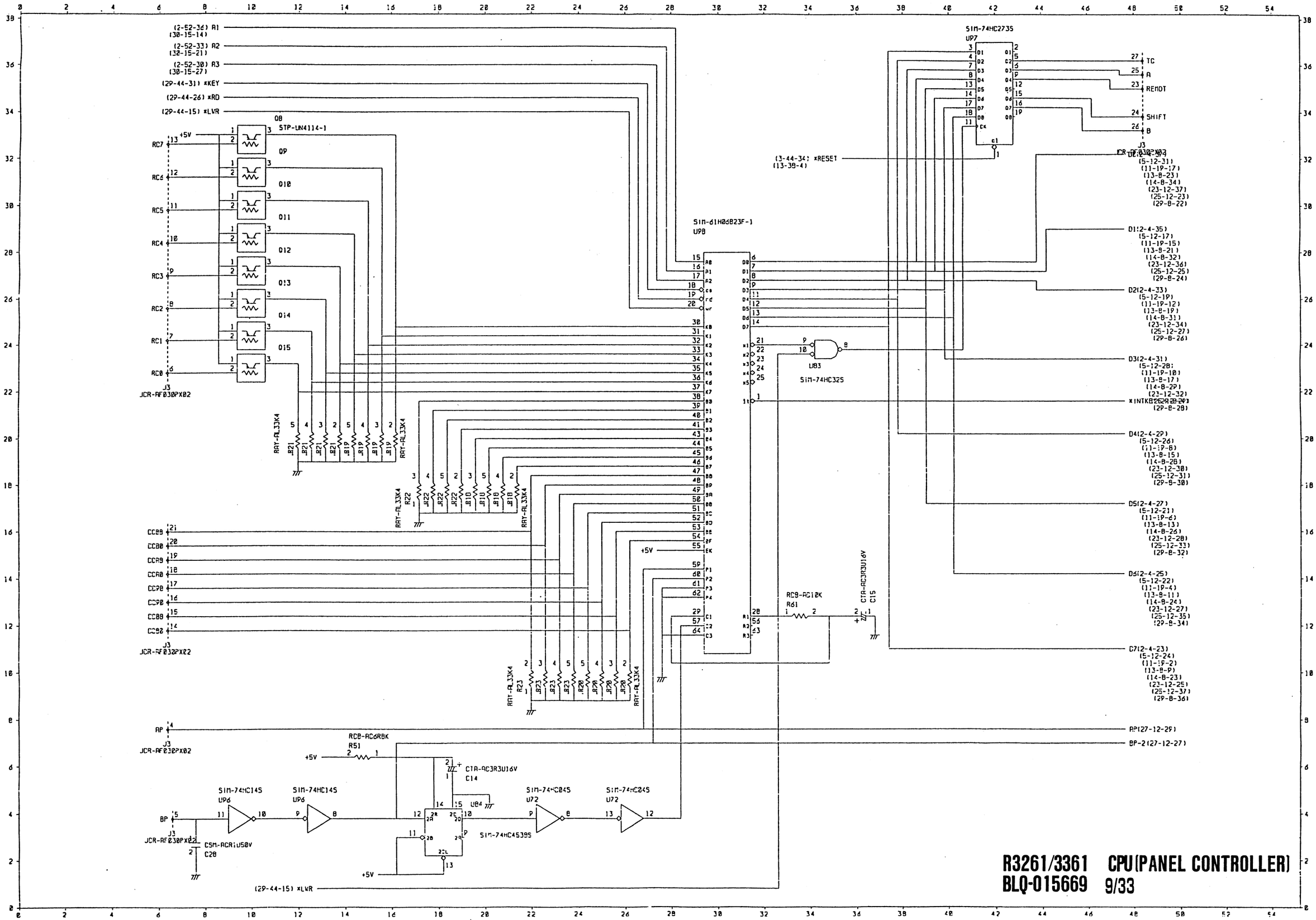
D0(2-4-37) (5-12-31) (11-19-17) (13-8-23) (14-8-34) (23-12-37) (25-12-23) (29-8-22)
D1(2-4-35) (5-12-17) (11-19-15) (13-8-21) (14-8-32) (23-12-35) (25-12-25) (29-8-24)
D2(2-4-33) (5-12-19) (11-19-12) (13-8-19) (14-8-31) (23-12-34) (25-12-27) (29-8-26)
D3(2-4-31) (5-12-28) (11-19-10) (13-8-17) (14-8-29) (23-12-32) (25-12-29) (29-8-28)
D4(2-4-29) (5-12-26) (11-19-8) (13-8-15) (14-8-28) (23-12-30) (25-12-31) (29-8-30)
D5(2-4-27) (5-12-21) (11-19-6) (13-8-13) (14-8-26) (23-12-28) (25-12-33) (29-8-32)
D6(2-4-25) (5-12-22) (11-19-4) (13-8-11) (14-8-24) (23-12-27) (25-12-35) (29-8-34)
D7(2-4-23) (5-12-24) (11-19-2) (13-8-9) (14-8-23) (23-12-25) (25-12-37) (29-8-36)

D8(2-4-21) (14-8-21) (23-12-23)
DP(2-4-19) (14-8-20) (23-12-21)
D10(2-4-18) (14-8-19) (23-12-20)
D11(2-4-16) (14-8-18) (23-12-18)
D12(2-4-14) (14-8-16) (23-12-17)
D13(2-4-13) (14-8-15) (23-12-15)
D14(2-4-11) (14-8-14) (23-12-13)
D15(2-4-10) (14-8-12) (23-12-11)

*INTMKR(29-8-5)
*SVPSTP(27-12-30)
RA(11-19-21)
BA(11-19-20)

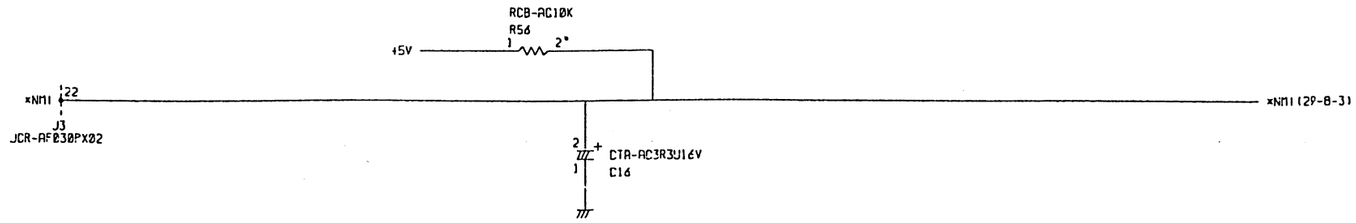


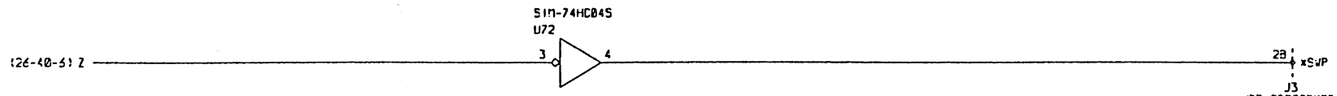
R3261/3361 CPU (EEPROM)
BLQ-015669 8/33

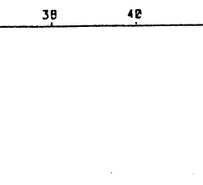


R3261/3361 CPU(PANEL CONTROLLER)
BLQ-015669 9/33

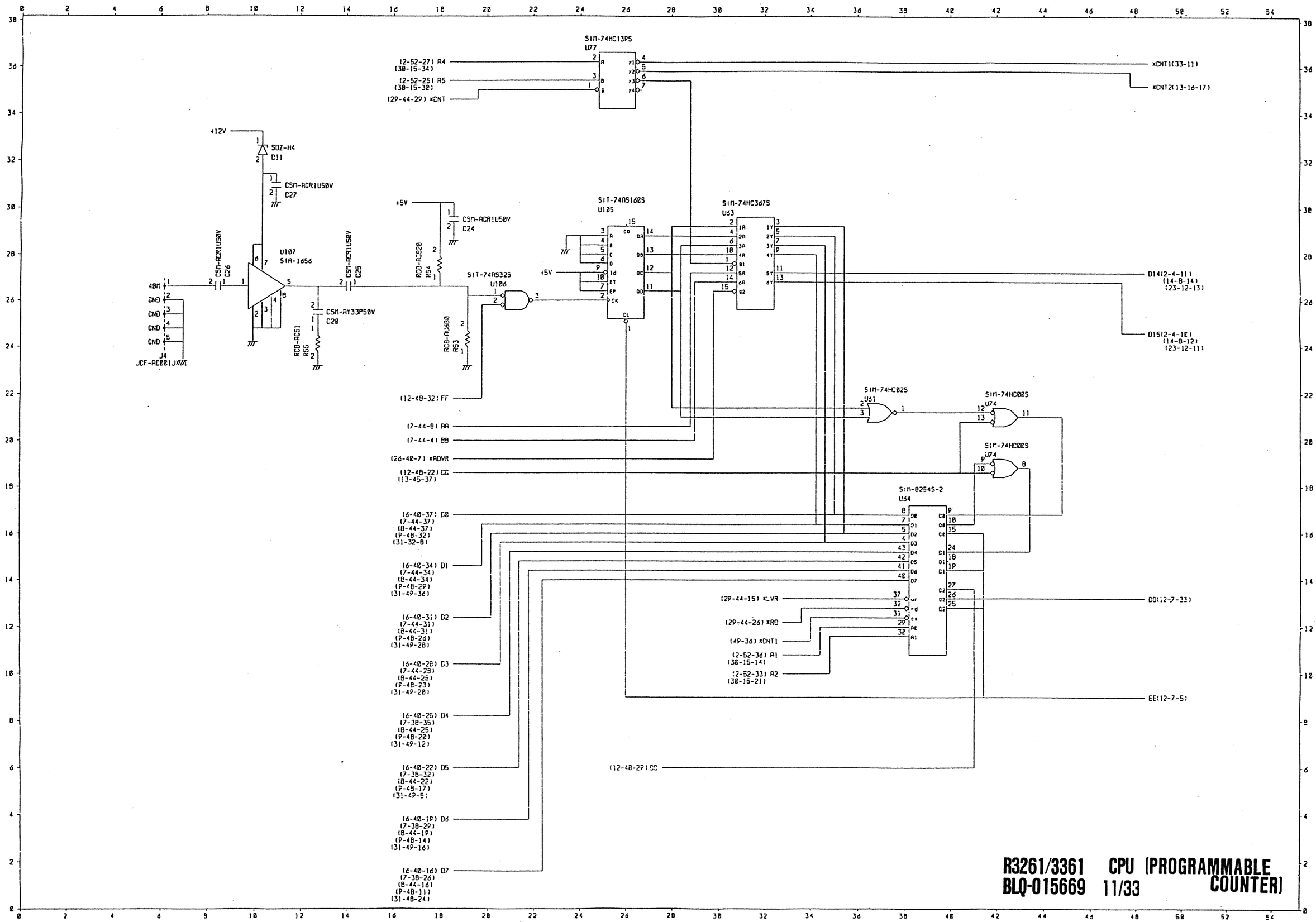
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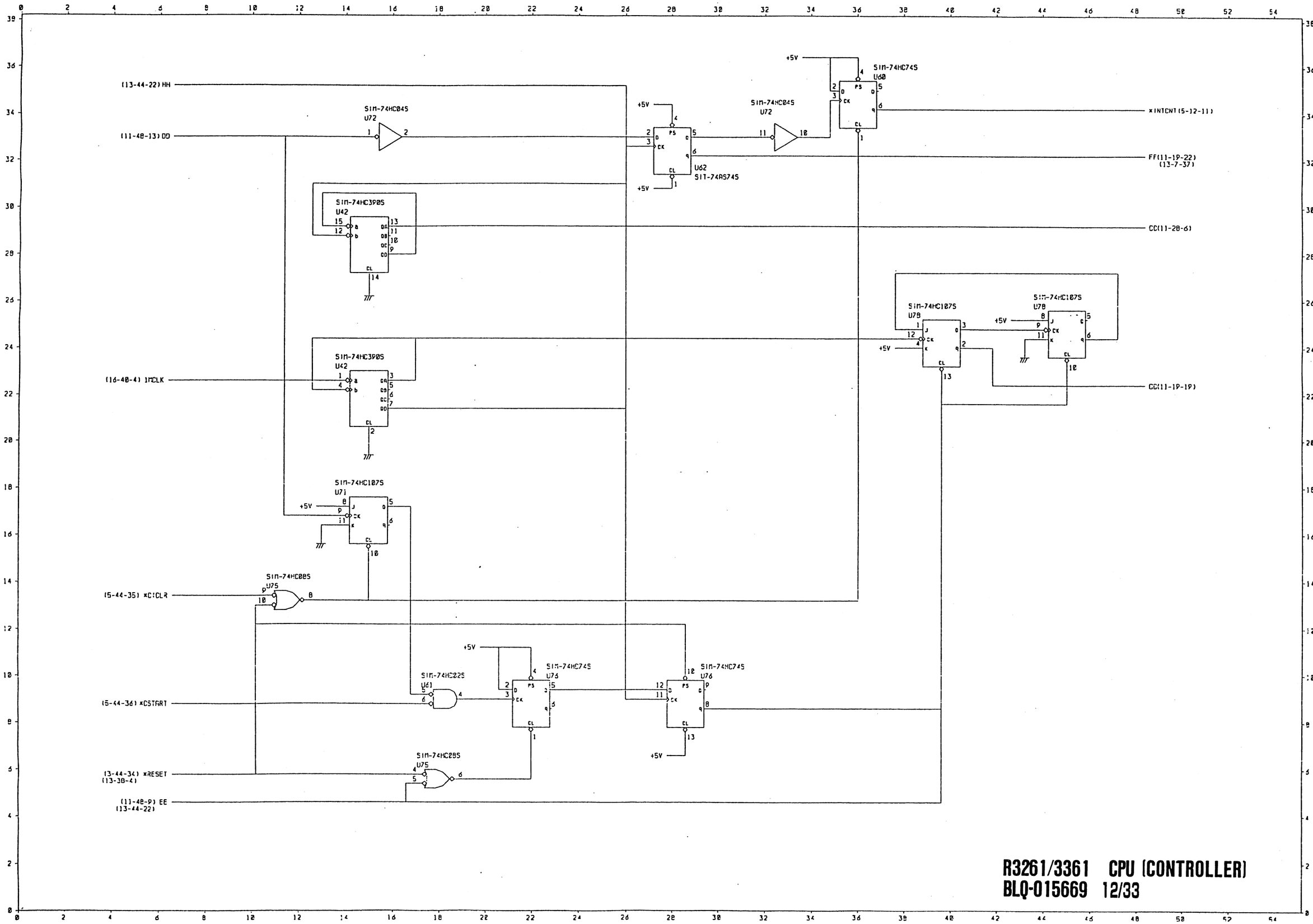


R3261/3361 CPU (TO PANEL)
BLQ-015669 10/33



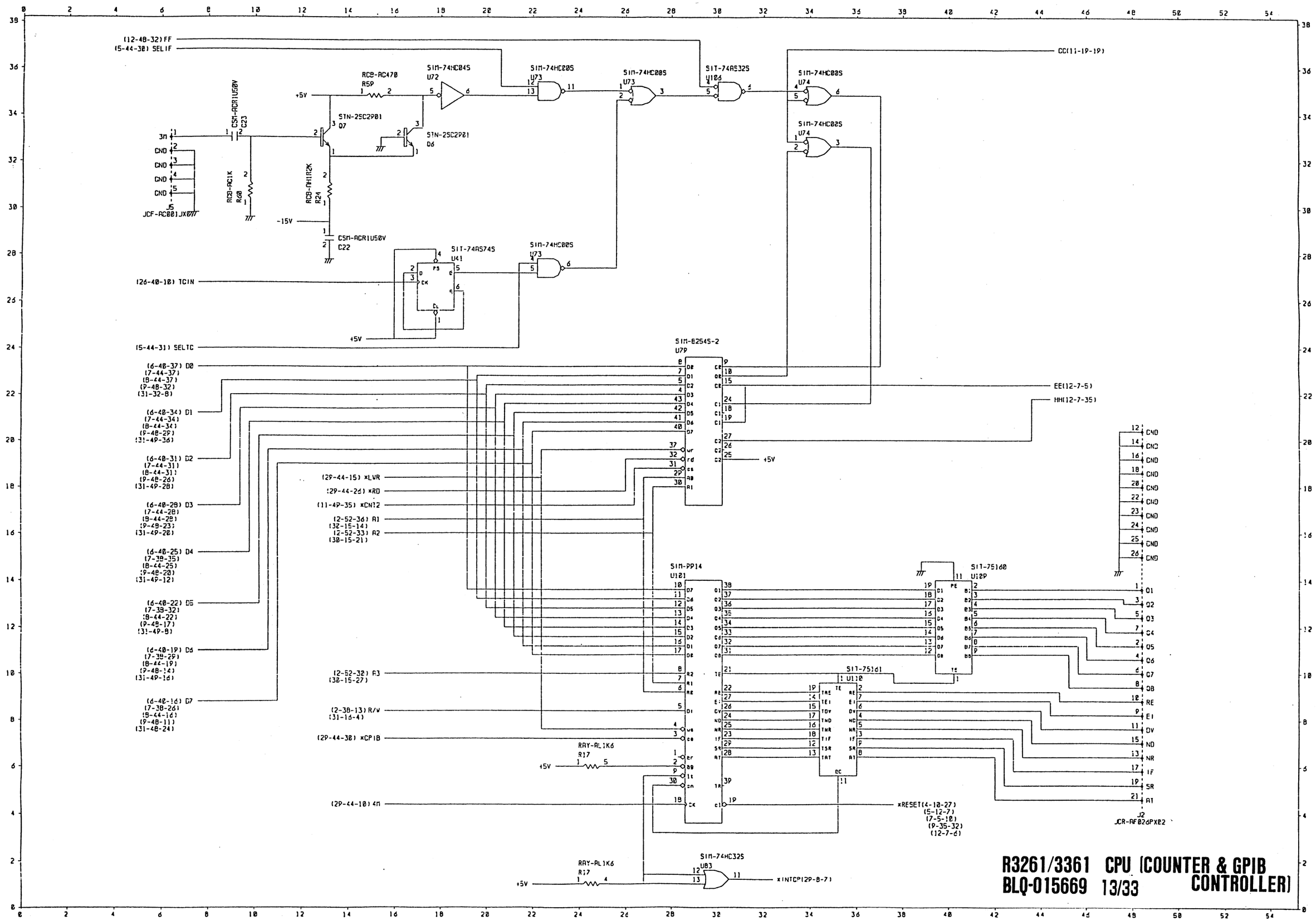
R3261/3361 CPU (PROGRAMMABLE COUNTER)
BLQ-015669 11/33

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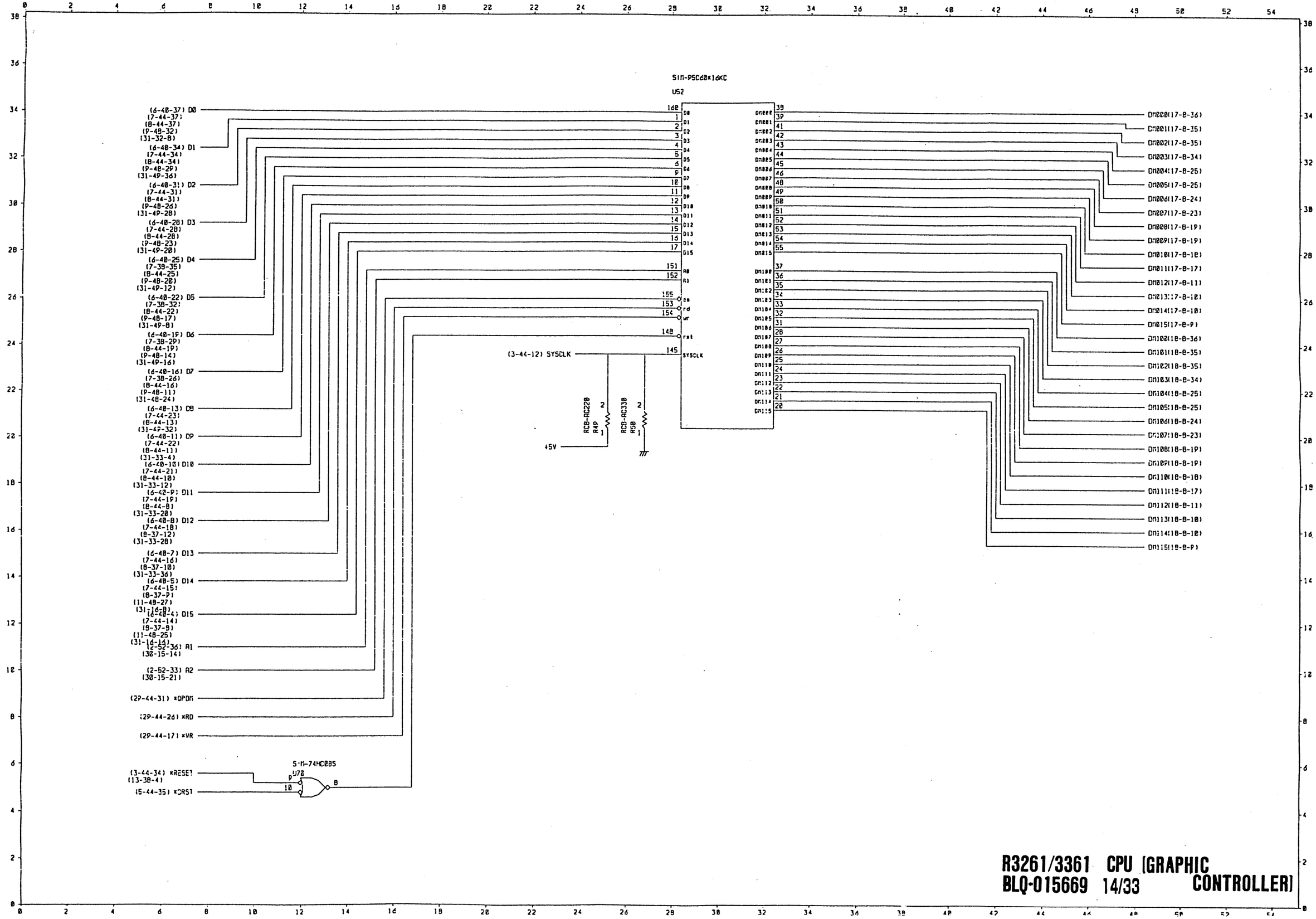
R3261/3361 CPU (CONTROLLER)
BLQ-015669 12/33

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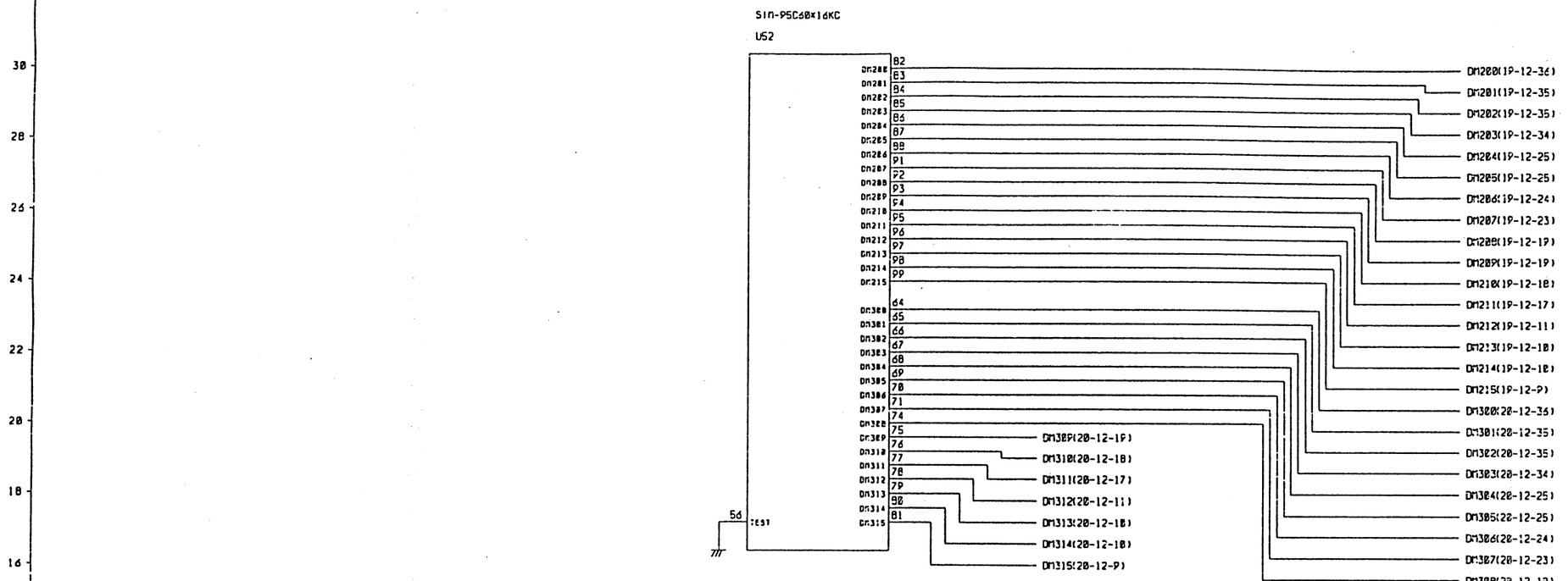
R3261/3361 CPU (COUNTER & GPIB CONTROLLER)
BLQ-015669 13/33

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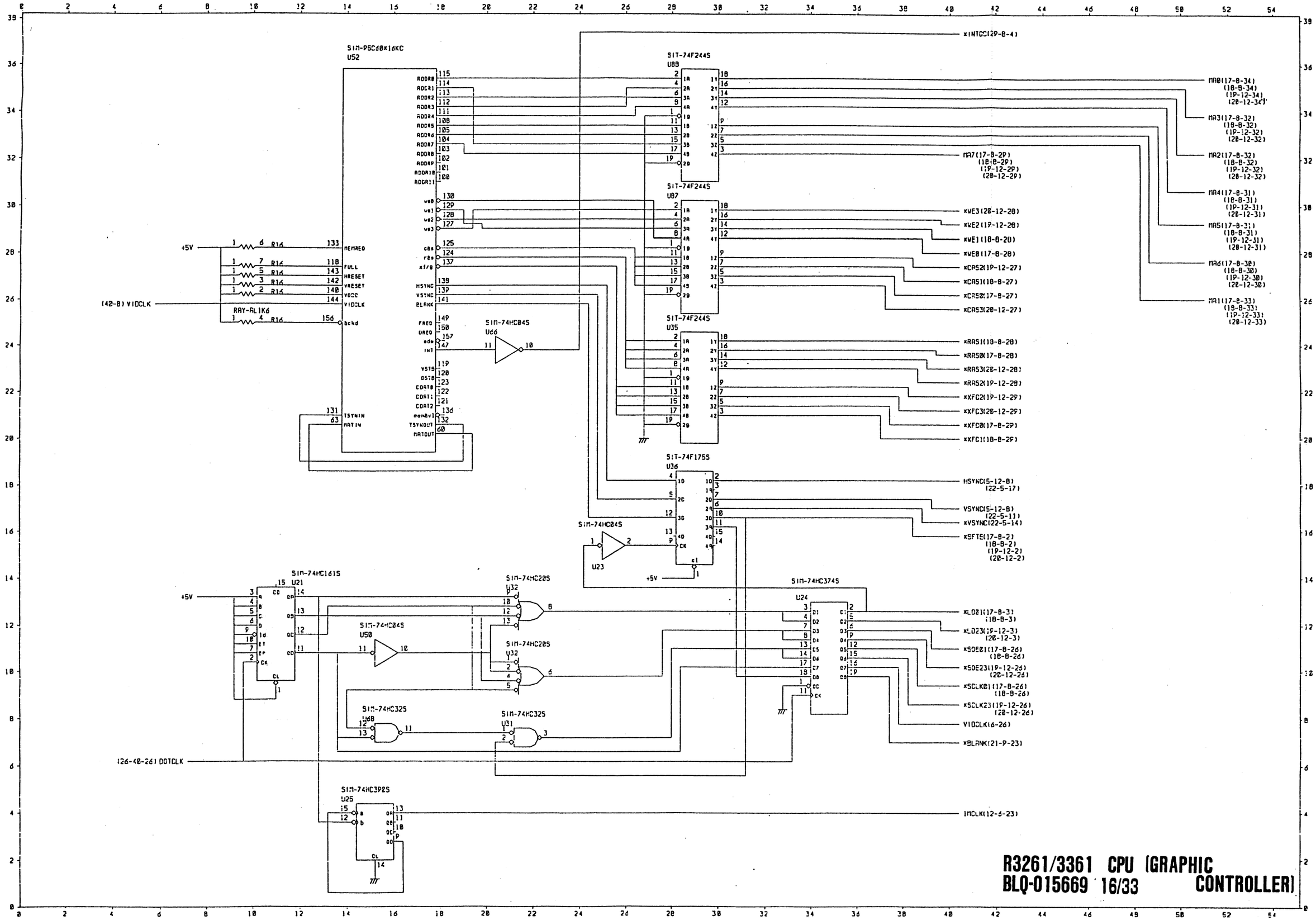
R3261/3361 CPU (GRAPHIC CONTROLLER)
BLQ-015669 14/33

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R3261/3361 CPU (GRAPHIC CONTROLLER)
BLQ-015669 15/33

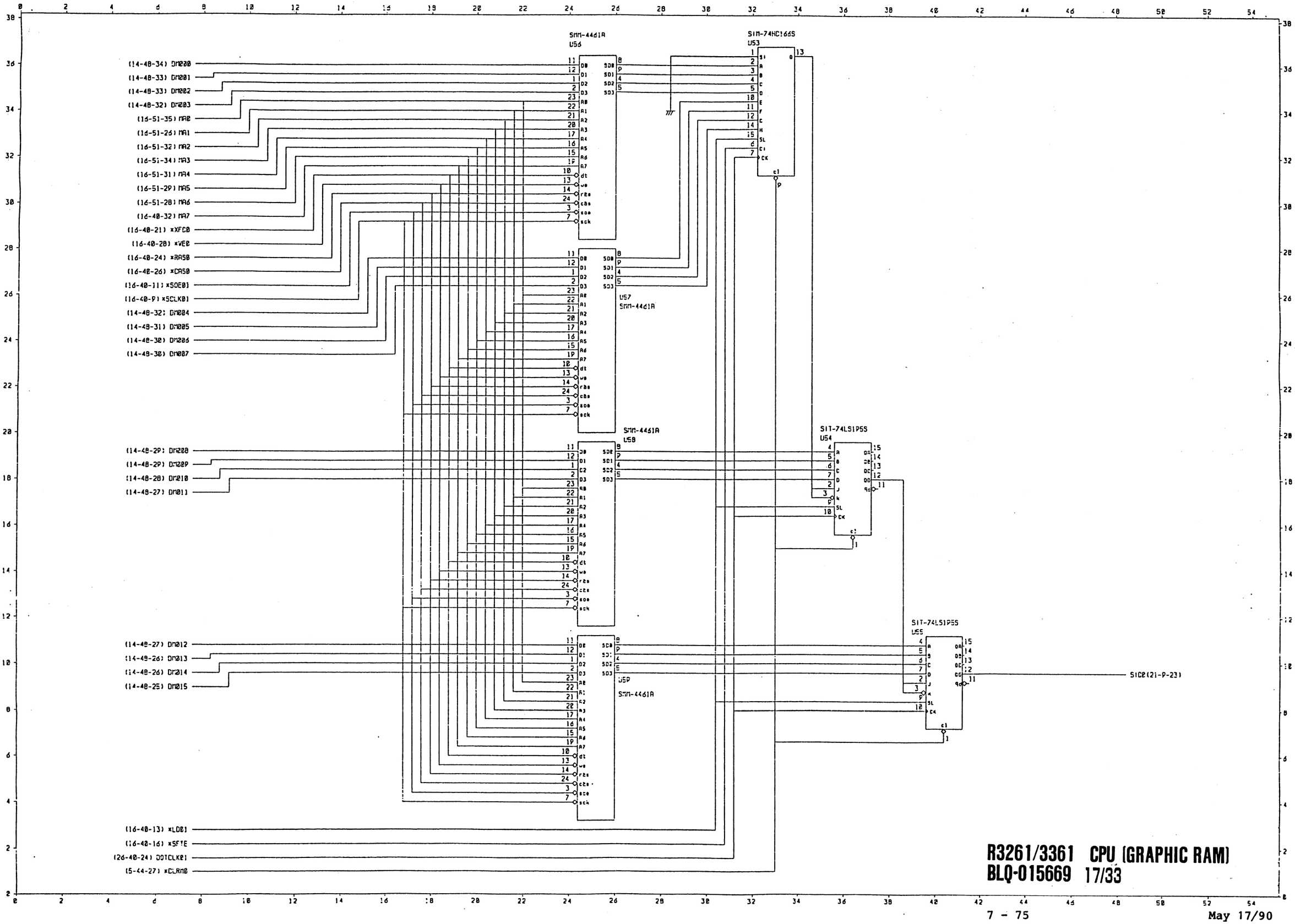
May 17/90

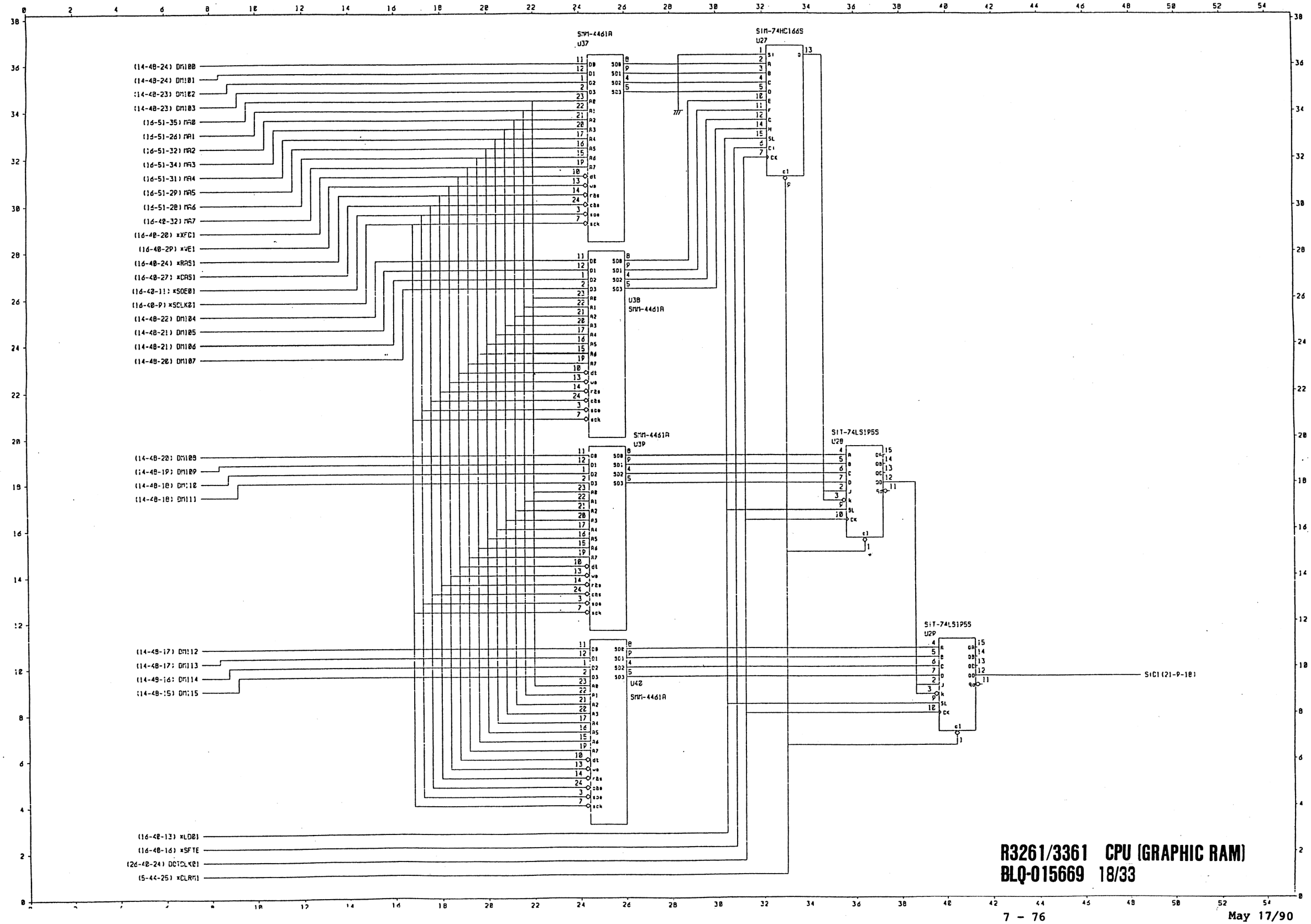


R3261/3361 CPU (GRAPHIC CONTROLLER)

BLQ-015669 16/33

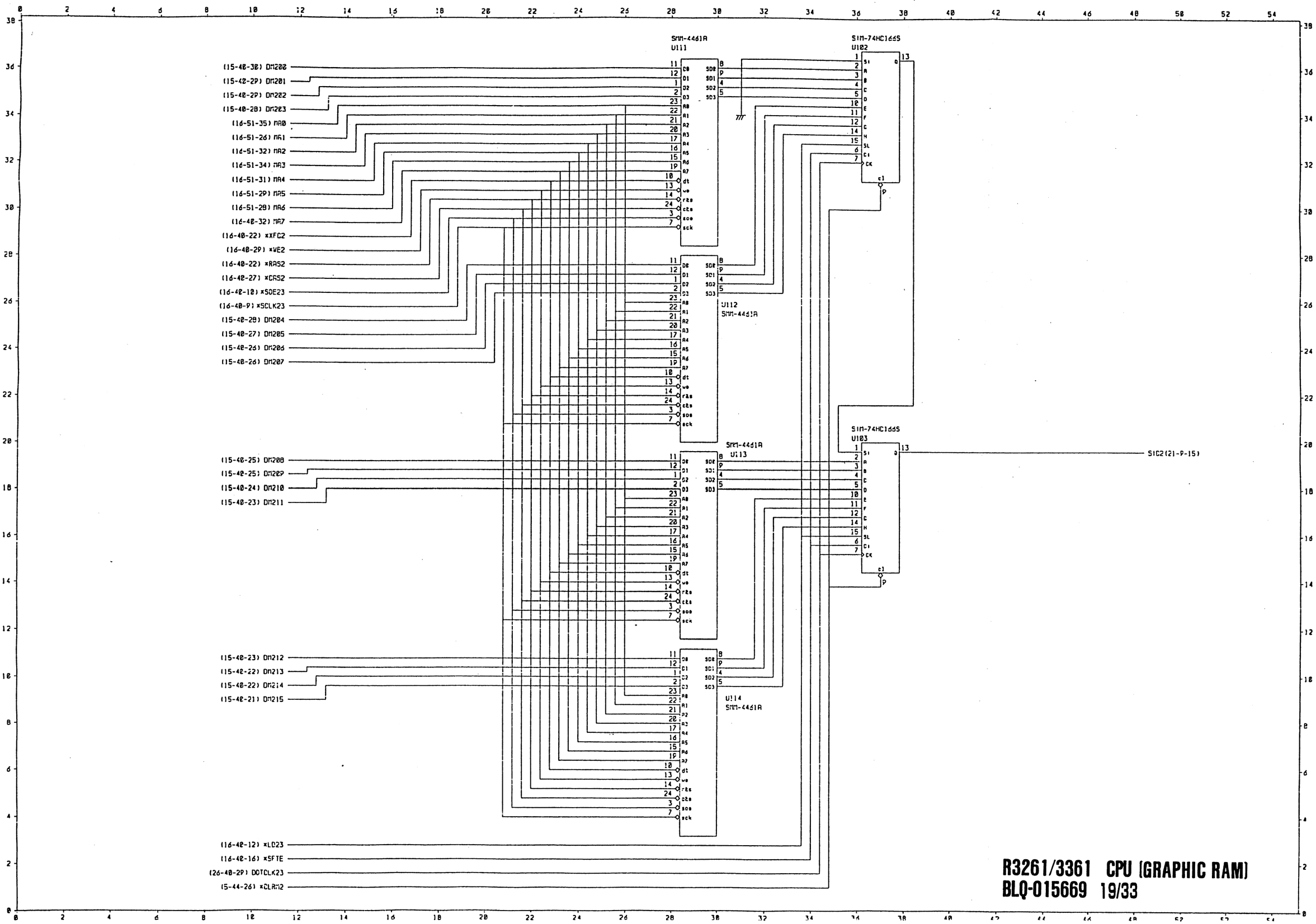
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SMM-4461A
U56
SIM-74HC166S
U53
U57
SMM-4461A
SMM-4461A
U58
SIT-74LS195S
U54
SIT-74LS195S
U55
U59
SMM-4461A
(14-48-34) DM00
(14-48-33) DM01
(14-48-33) DM02
(14-48-32) DM03
(16-51-35) MA0
(16-51-26) MA1
(16-51-32) MA2
(16-51-34) MA3
(16-51-31) MA4
(16-51-29) MA5
(16-51-28) MA6
(16-48-32) MA7
(16-48-21) *XFC0
(16-48-28) *WE0
(16-48-24) *RAS0
(16-48-26) *CAS0
(16-48-11) *SOE01
(16-48-9) *SCLK01
(14-48-32) DM04
(14-48-31) DM05
(14-48-30) DM06
(14-48-30) DM07
(14-48-29) DM08
(14-48-29) DM09
(14-48-28) DM10
(14-48-27) DM11
(14-48-27) DM12
(14-48-26) DM13
(14-48-26) DM14
(14-48-25) DM15
(16-48-13) *LD01
(16-48-16) *SFTE
(26-48-24) DOTCLK01
(5-44-27) *CLRN0
SIC01(21-9-23)
R3261/3361 CPU (GRAPHIC RAM)
BLQ-015669 17/33



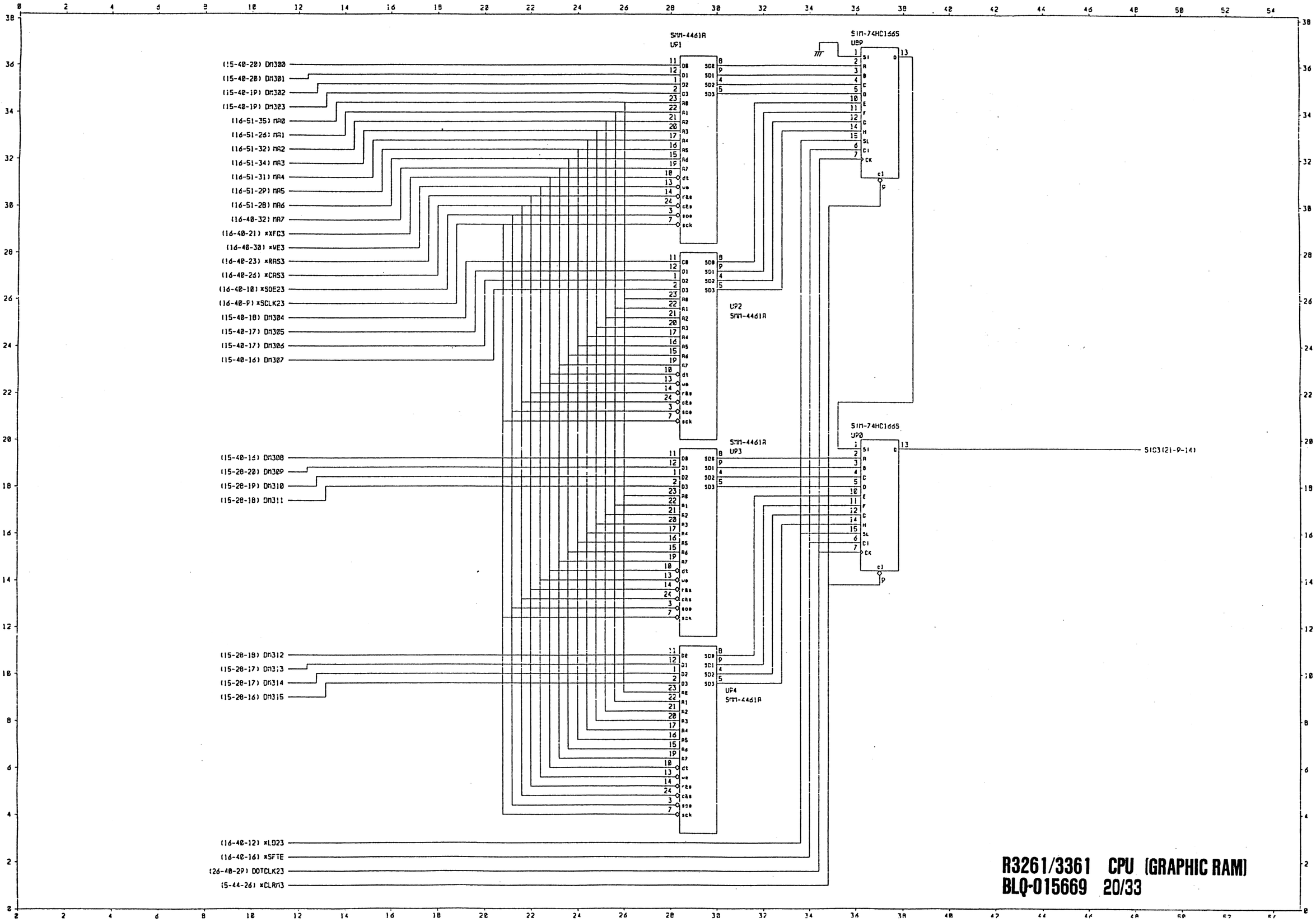
R3261/3361 CPU (GRAPHIC RAM)
BLQ-015669 18/33

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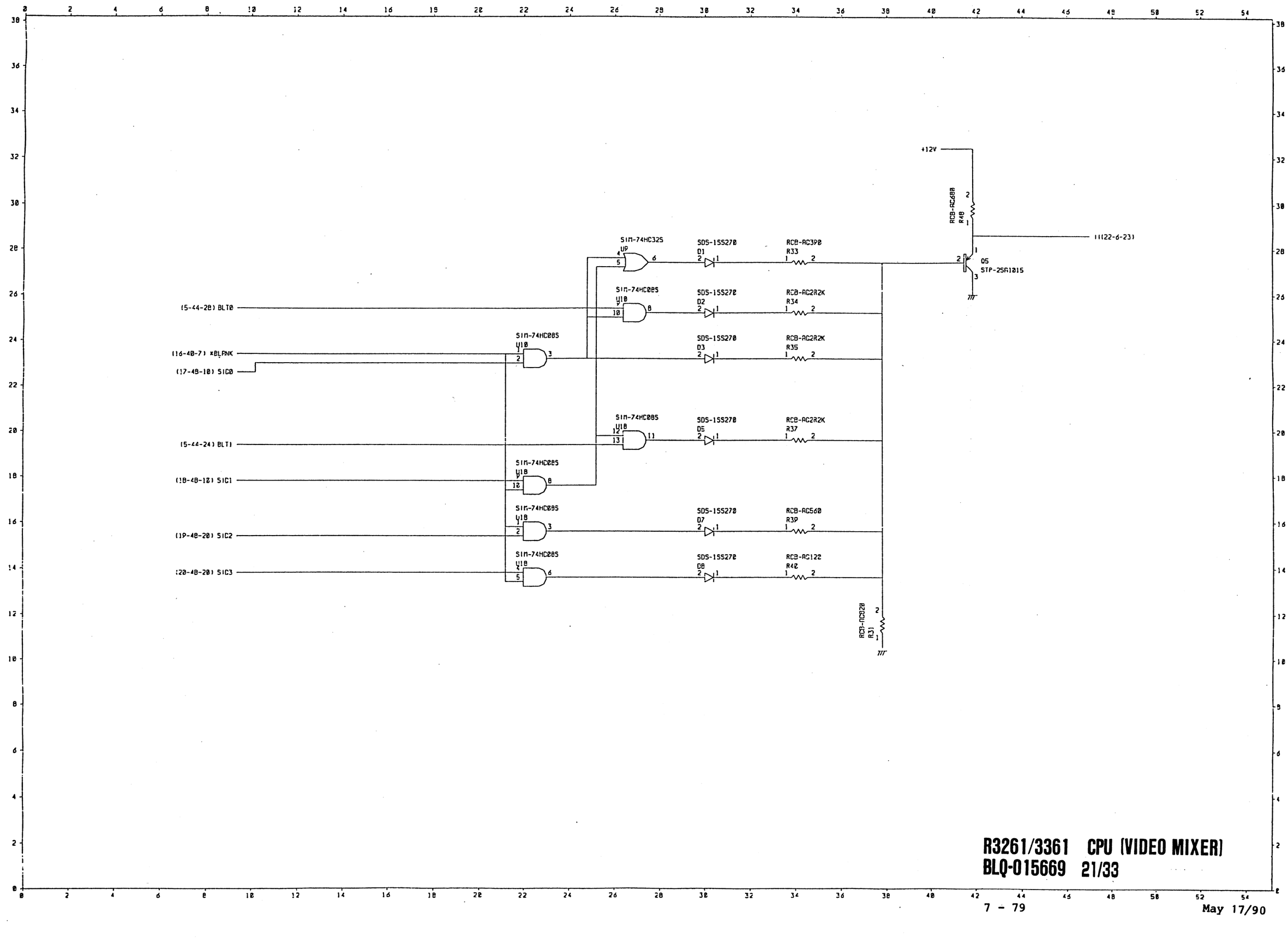


R3261/3361 CPU (GRAPHIC RAM)
BLQ-015669 19/33

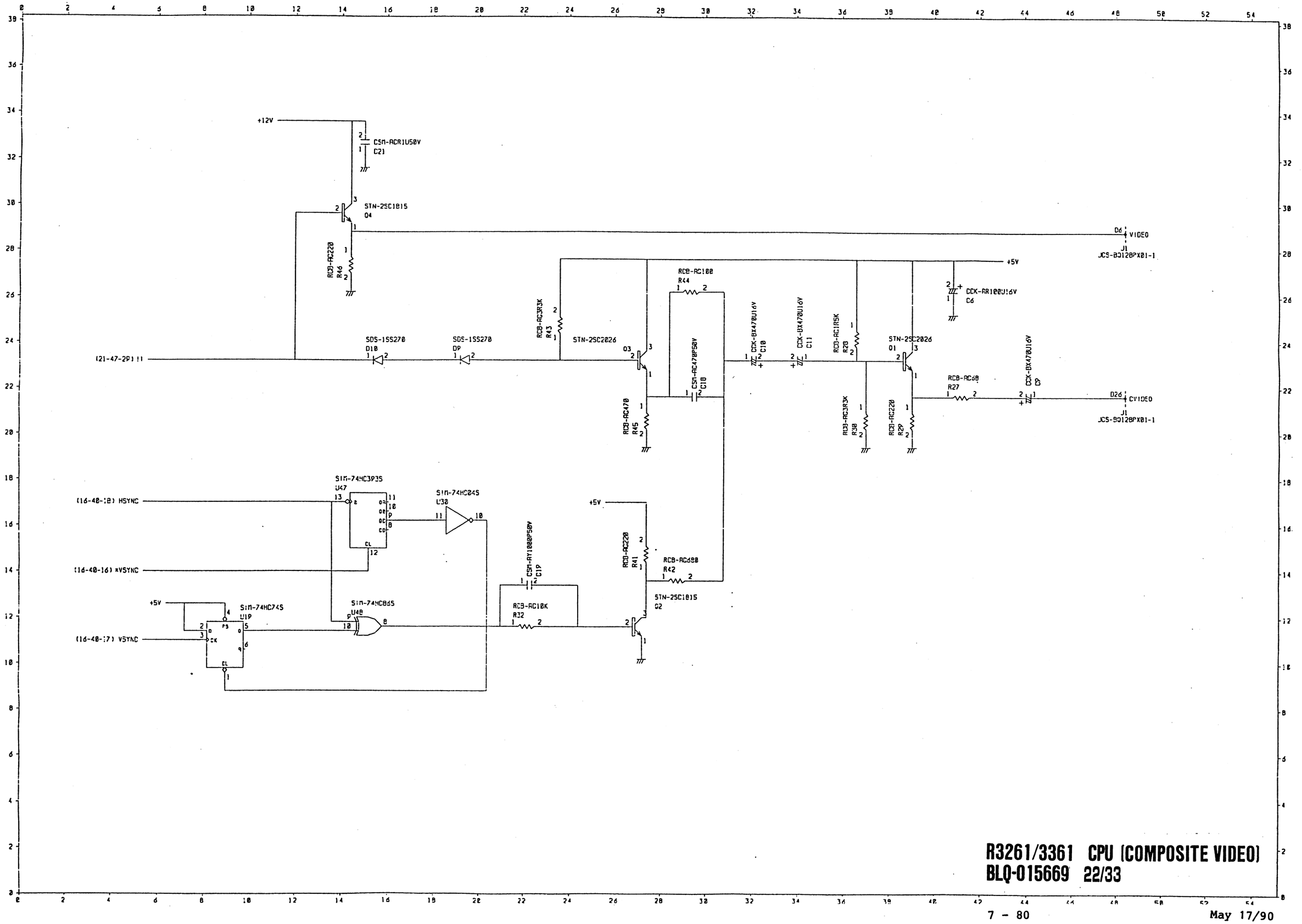
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SMM-4461A
U91
SIM-74HC166S
U89
U92
SMM-4461A
SMM-4461A
U93
SIM-74HC166S
U90
U94
SMM-4461A
(15-40-20) DM300
(15-40-20) DM301
(15-40-19) DM302
(15-40-19) DM303
(16-51-35) MA0
(16-51-26) MA1
(16-51-32) MA2
(16-51-34) MA3
(16-51-31) MA4
(16-51-29) MA5
(16-51-28) MA6
(16-40-32) MA7
(16-40-21) *XFC3
(16-40-30) *WE3
(16-40-23) *RAS3
(16-40-26) *CAS3
(16-40-18) *SOE23
(16-40-9) *SCLK23
(15-40-18) DM304
(15-40-17) DM305
(15-40-17) DM306
(15-40-16) DM307
(15-40-14) DM308
(15-28-20) DM309
(15-28-19) DM310
(15-28-18) DM311
(15-28-18) DM312
(15-28-17) DM313
(15-28-17) DM314
(15-28-16) DM315
(16-40-12) *LD23
(16-40-16) *SFTE
(26-40-29) DOTCLK23
(5-44-26) *CLRM3
SIG3(21-9-14)
R3261/3361 CPU (GRAPHIC RAM)
BLQ-015669 20/33

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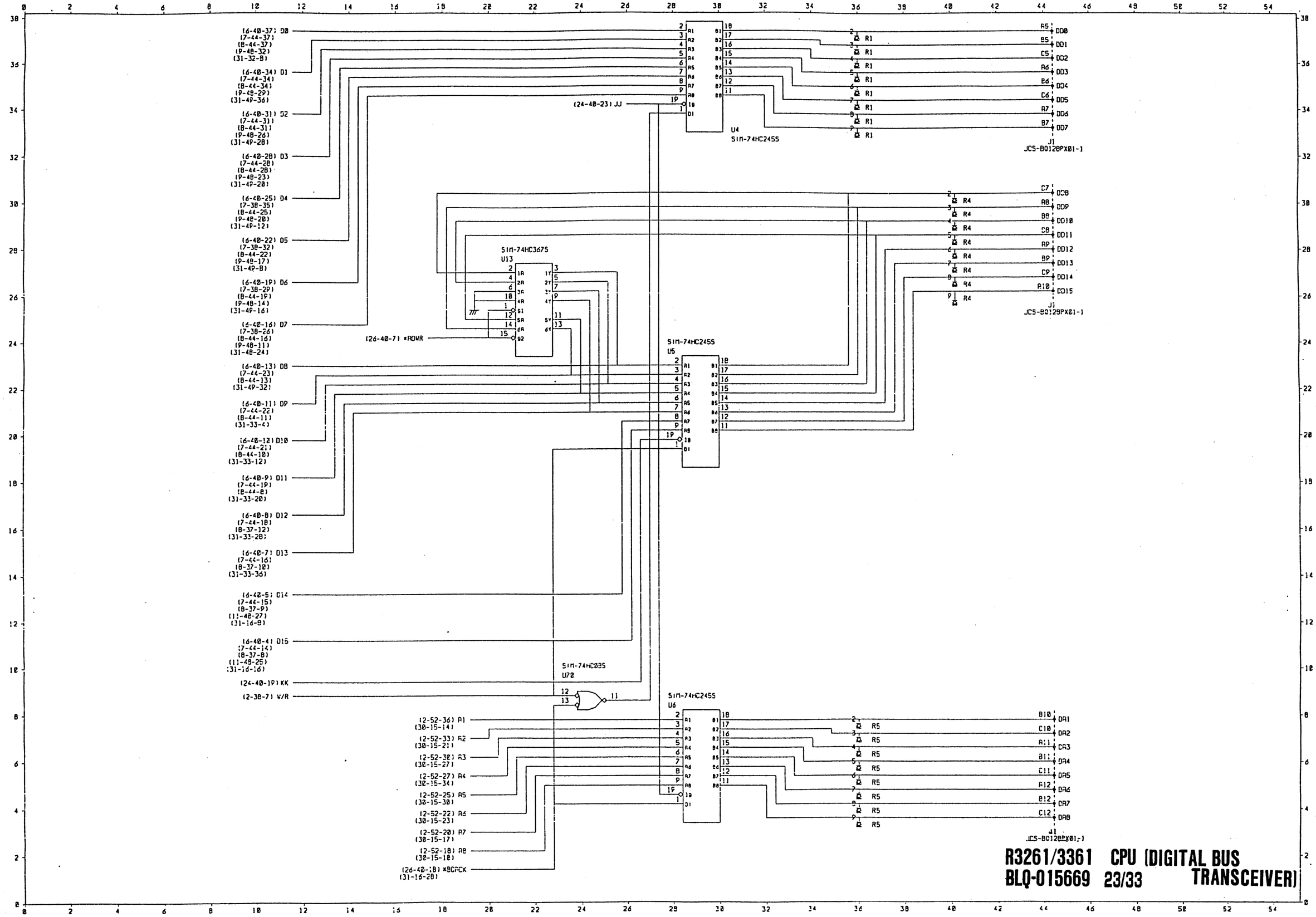


R3261/3361 CPU (VIDEO MIXER)
BLQ-015669 21/33



R3261/3361 CPU (COMPOSITE VIDEO)
BLQ-015669 22/33

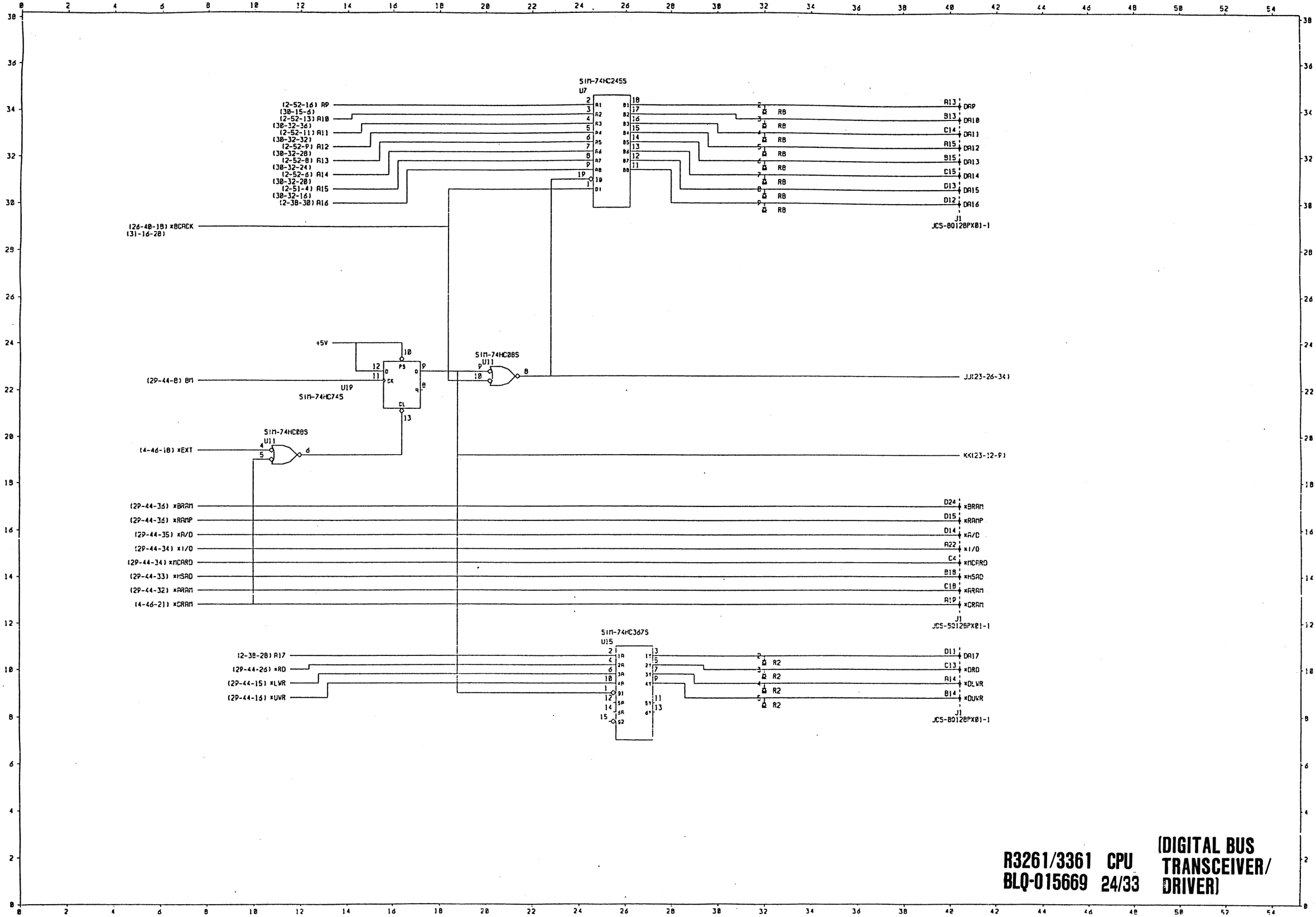
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R3261/3361 CPU (DIGITAL BUS TRANSCEIVER)

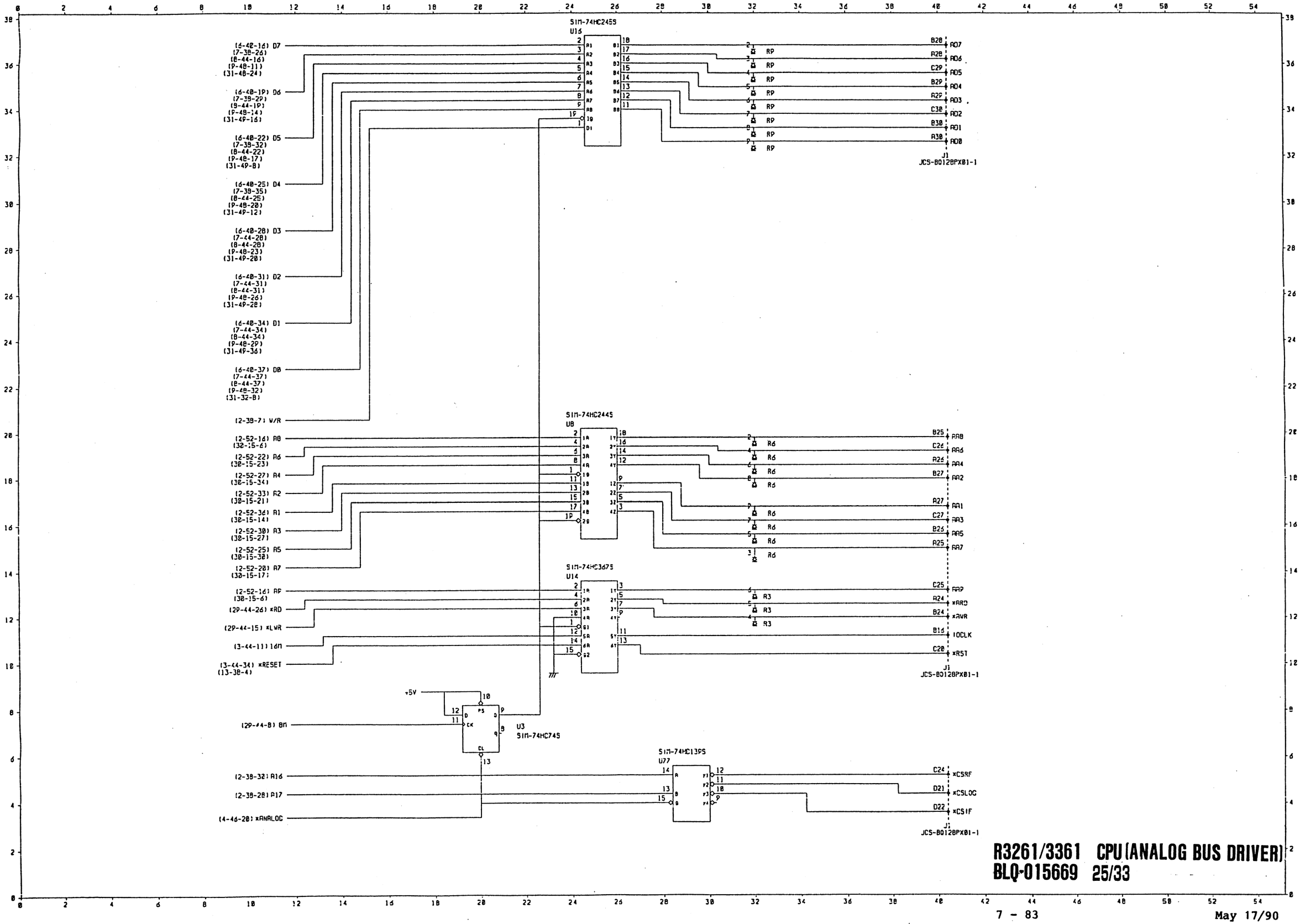
BLQ-015669 23/33

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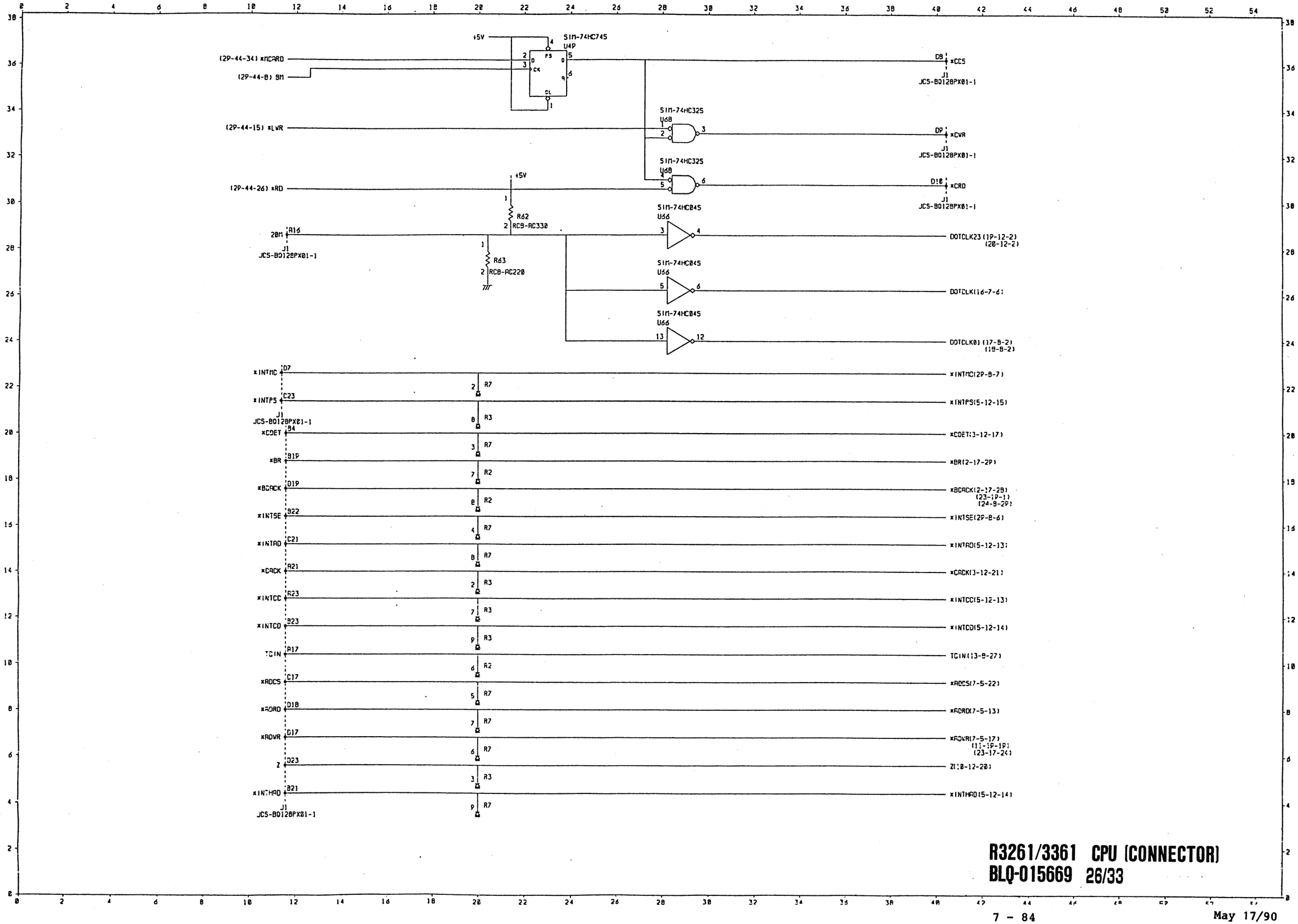
R3261/3361 CPU BLQ-015669 24/33 (DIGITAL BUS TRANSCEIVER/ DRIVER)

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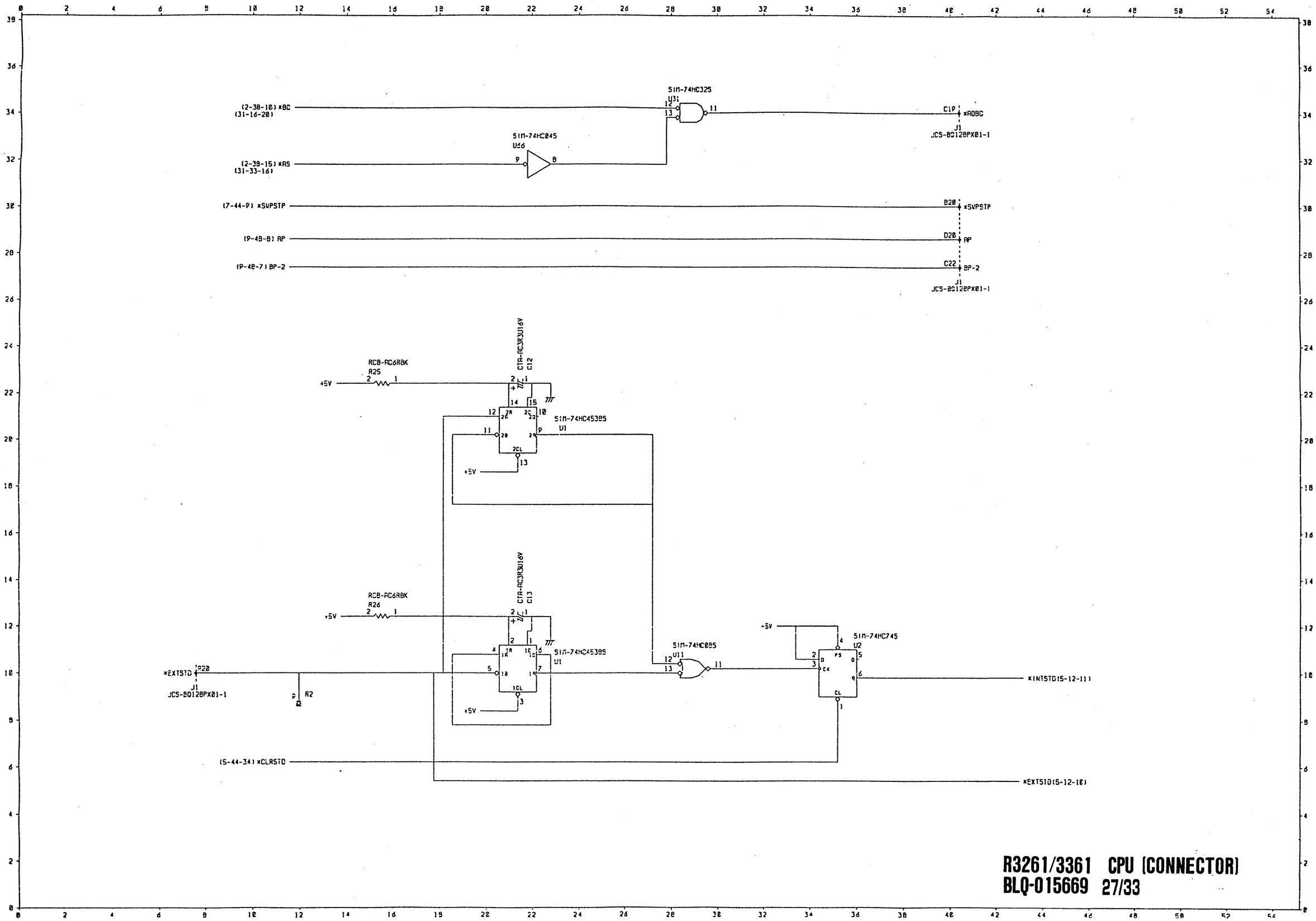
R3261/3361 CPU (ANALOG BUS DRIVER)
BLQ-015669 25/33

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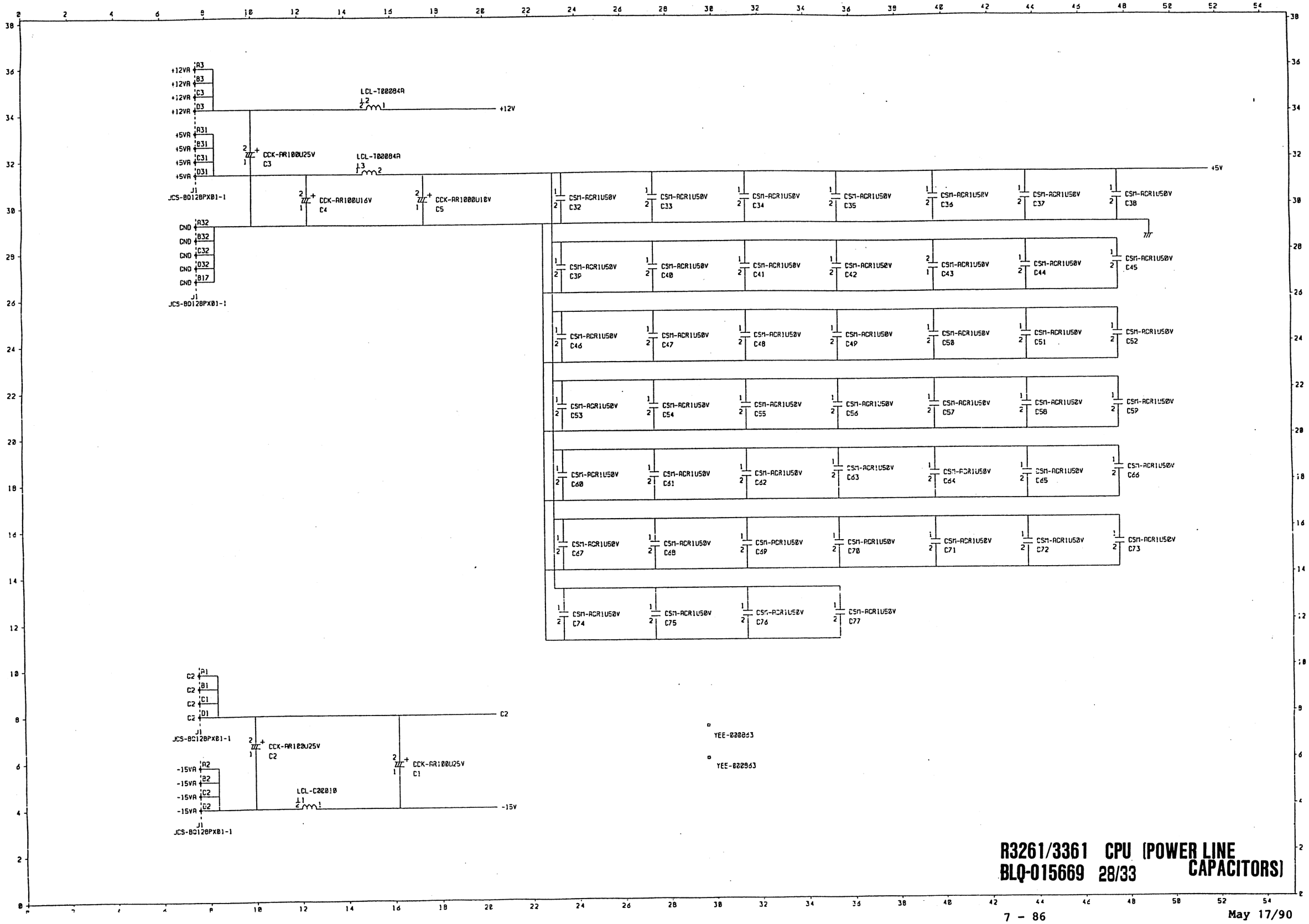
R3261/3361 CPU (CONNECTOR)
BLQ-015669 26/33

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R3261/3361 CPU (CONNECTOR)
BLQ-015669 27/33

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R3261/3361 CPU (POWER LINE CAPACITORS)

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SIM-654815
U82

SIM-74HC08S
U11

U70

SIM-74HC08S

SIM-74HC24S
U30

SIM-74HC24S
U30

+5V

(6-48-16) D7
(7-38-26)
(8-44-16)
(9-48-11)
(31-48-24)

(6-48-19) D6
(7-39-29)
(8-44-19)
(9-48-14)
(31-49-16)

(6-48-22) D5
(7-39-32)
(8-44-22)
(9-48-17)
(31-49-8)

(6-48-25) D4
(7-38-35)
(8-44-25)
(9-48-20)
(31-49-12)

(6-48-28) D3
(7-44-28)
(8-44-28)
(9-48-23)
(31-49-20)

(6-48-31) D2
(7-44-31)
(8-44-31)
(9-48-26)
(31-49-28)

(6-48-34) D1
(7-44-34)
(8-44-34)
(9-48-29)
(31-49-36)

(6-48-37) D0
(7-44-37)
(8-44-37)
(9-48-32)
(31-32-8)

(2-52-36) A1
(32-15-14)

(2-52-33) A2
(30-15-21)

(2-52-30) A3
(32-15-27)

(2-52-27) A4
(32-15-34)

(2-52-6) A14
(30-32-28)

(2-51-4) A15
(30-32-16)

(2-38-30) A16

(2-38-28) A17

(2-38-19) FC0

(2-38-18) FC1

(2-38-17) FC2

(4-46-20) 180X

(2-38-15) xAS
(31-33-16)

(2-38-13) R/W
(31-16-4)

(2-38-12) xUDS
(31-33-24)

(2-38-11) xLDS

(26-40-23) xINTMC

(13-32-1) xINTGP

(25-42-16) xINTSE

(5-45-11) xINT4

(7-44-13) xINTKR

(16-42-37) xINTCC

(9-48-22) xINTKEY

(12-42-28) xNMI

(3-44-34) xRESET
(13-38-4)

(3-44-11) 16M

(3-44-21) xWAIT

xBRAM(24-8-17)

xRAMP(24-8-16)

xR/D(24-8-16)

x1/O(24-8-15)

xMCARD(24-8-15)
(26-12-36)

xHSAD(24-8-14)

xARAM(24-8-13)

xEROM(8-13-16)

xKEY(9-9-35)

xOPDN(14-8-9)

xCPIB(13-16-7)

xCNT(11-18-35)

xRDINT(5-12-9)

x1BBX(5-12-34)

xDTACK(2-17-30)
(31-8-12)

xBERR(2-17-26)

xRD(6-7-12)
(7-5-21)
(8-13-15)
(9-9-35)
(11-33-12)
(13-16-18)
(14-8-8)
(24-12-18)
(25-12-12)
(26-12-31)

xVPA(2-17-27)

IPL0(2-17-26)

IPL1(2-17-25)

IPL2(2-17-24)

xWR(14-8-7)

xUWR(7-5-16)
(8-13-7)
(24-12-9)

xLWR(5-12-33)
(7-5-19)
(8-13-14)
(9-9-34)
(9-13-1)
(11-33-13)
(13-16-18)
(24-12-9)
(25-12-12)
(26-12-33)

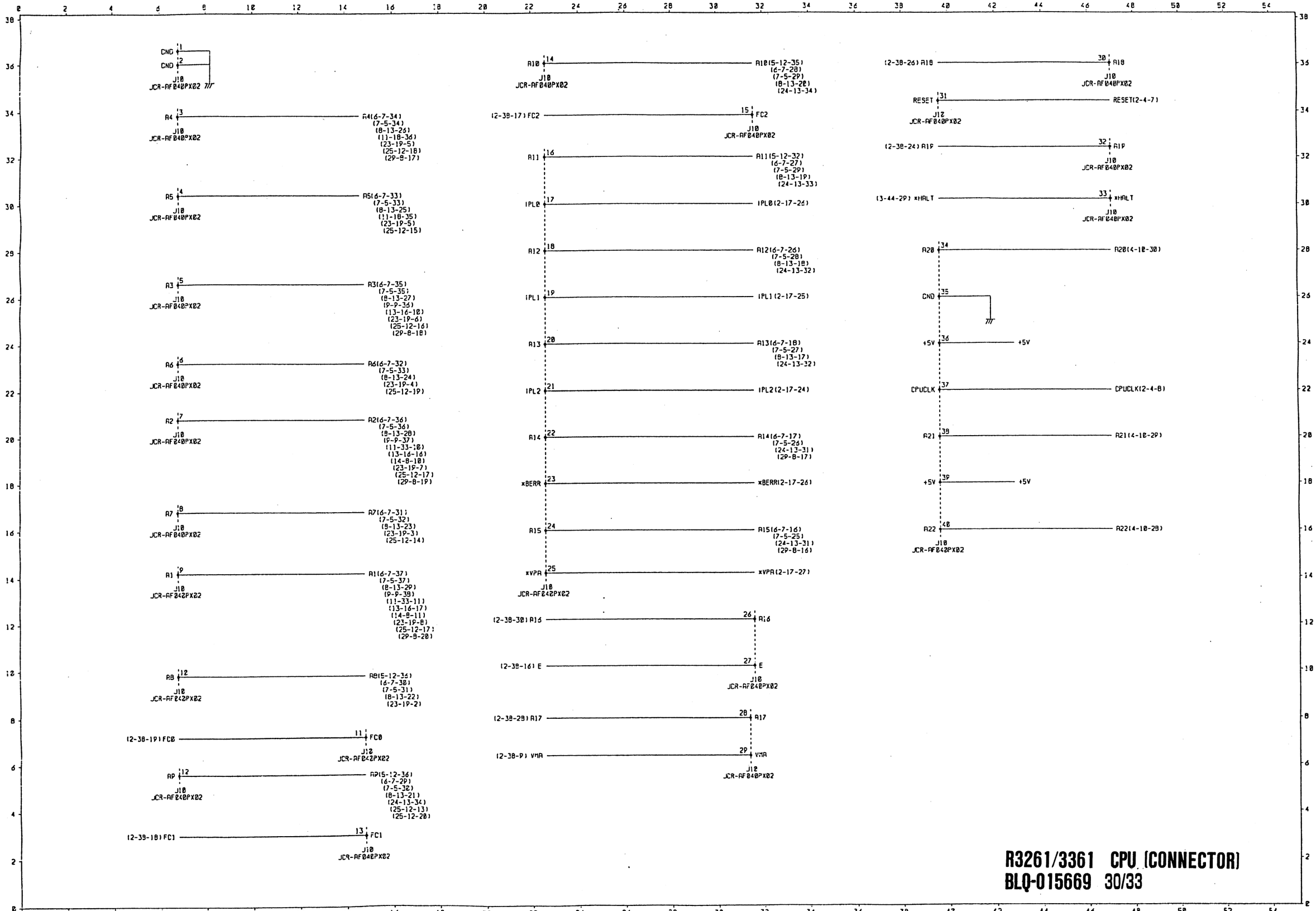
CPUCLK(2-4-8)

4M(13-16-4)

8M(4-18-15)
(5-11-2)
(24-8-22)
(25-12-7)
(26-12-35)

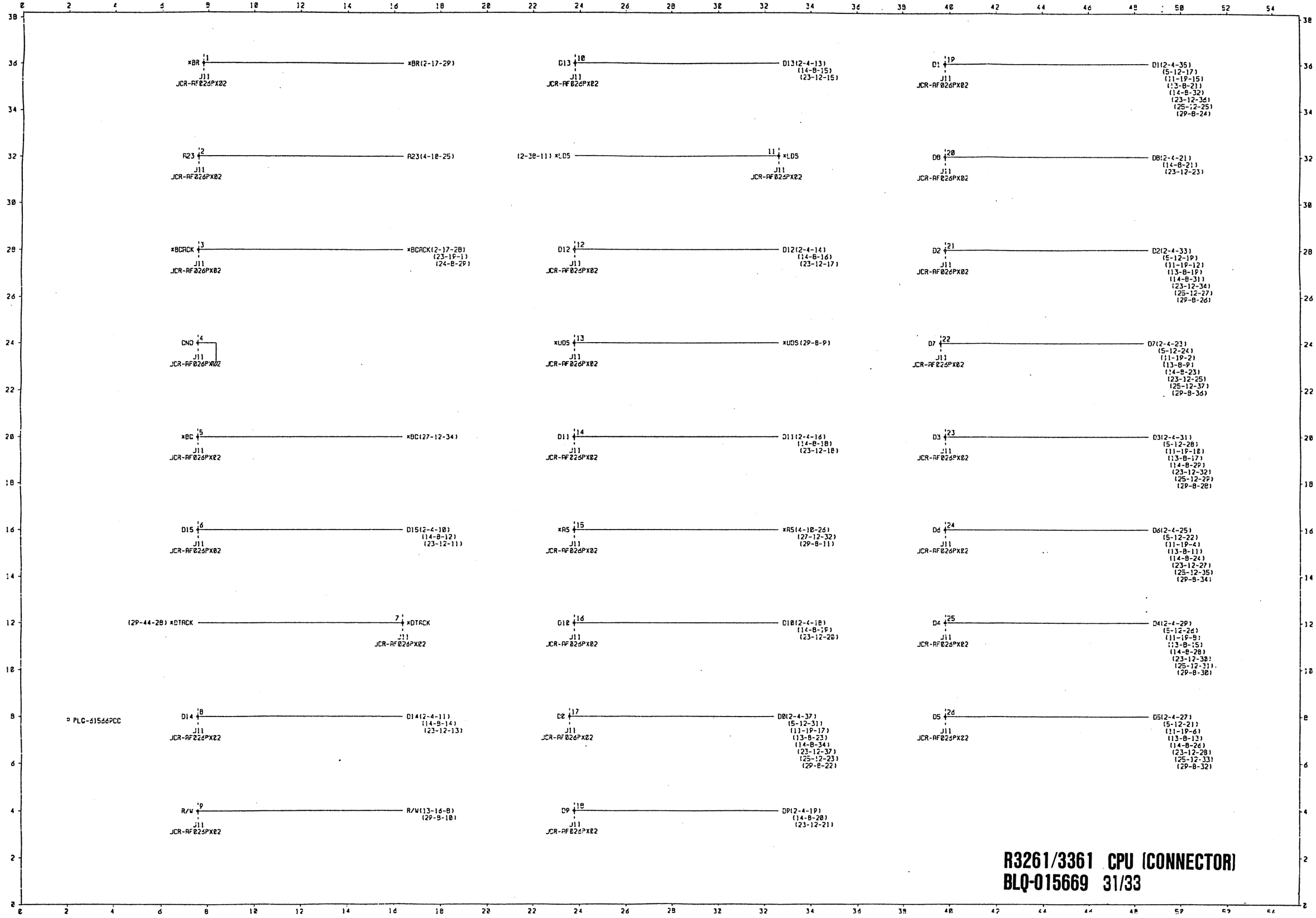
xINTTM(5-12-12)

R3261/3361 CPU (DECORDER & CLOCK GENERATOR)
BLQ-015669 29/33



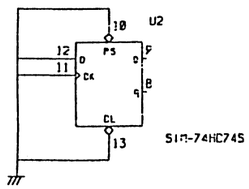
R3261/3361 CPU (CONNECTOR)
BLQ-015669 30/33

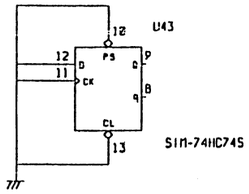
May 17/90

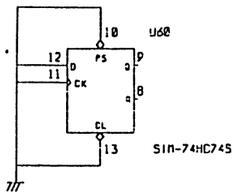


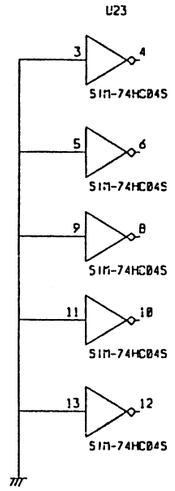
R3261/3361 CPU (CONNECTOR)
BLQ-015669 31/33

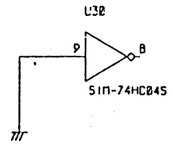
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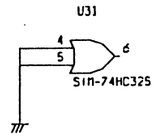


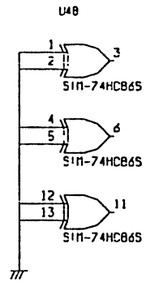


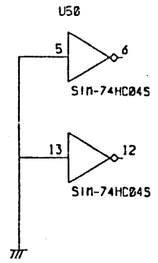


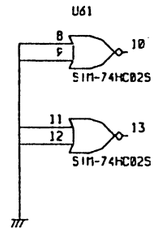


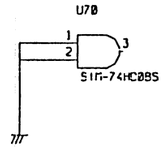


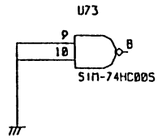


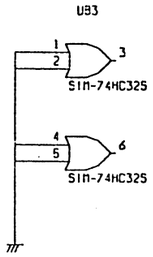


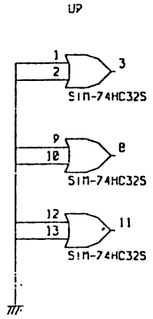


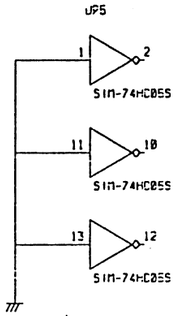


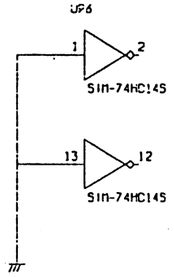






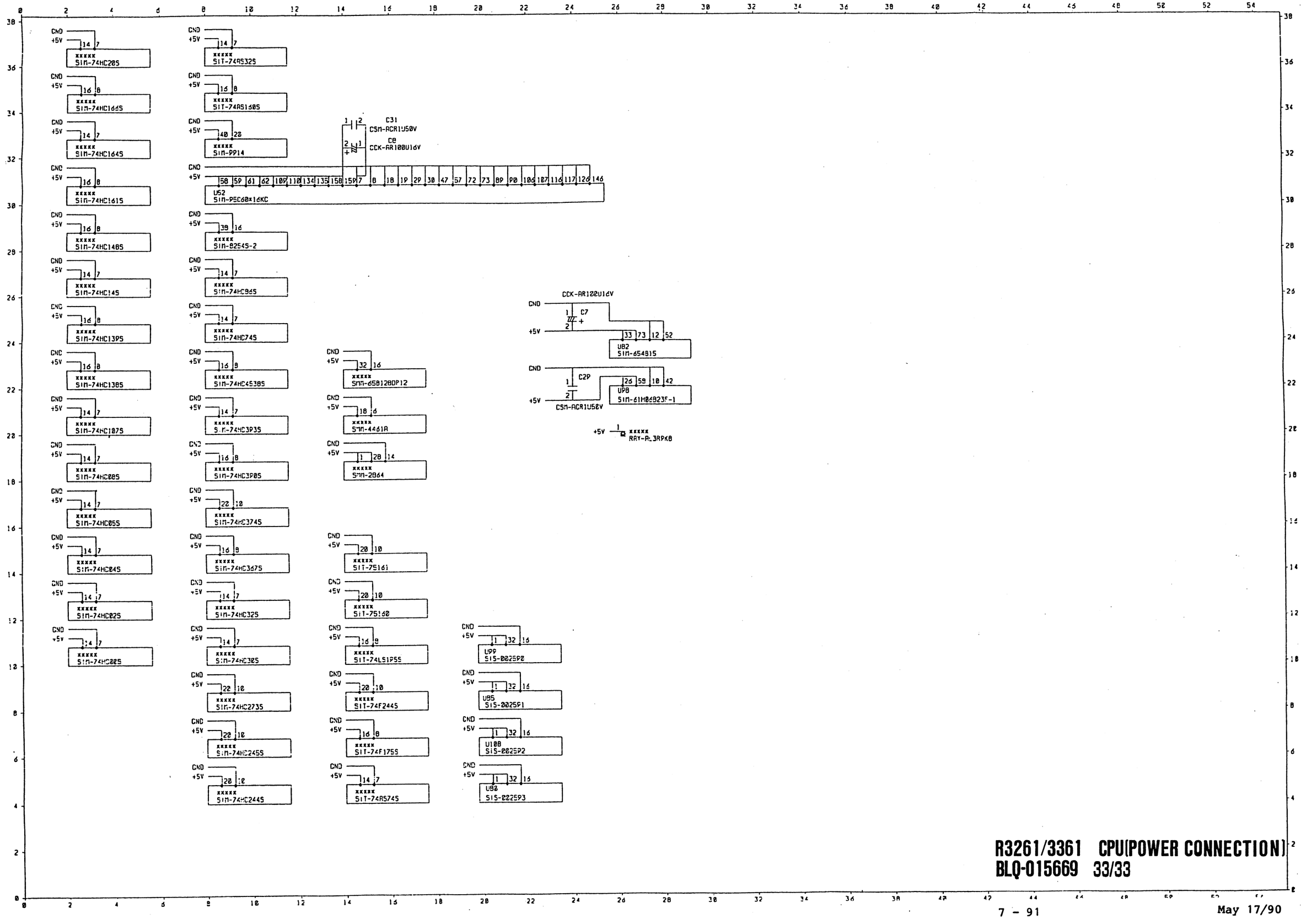






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R3261/3361 CPU(POWER CONNECTION)
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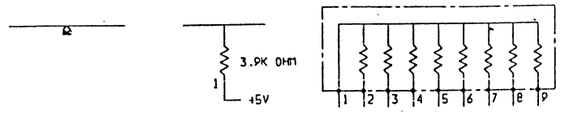
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1. DIAGRAMS ILLUSTRATION

1-1 SYMBOLS REFERENCE DESIGNATORS

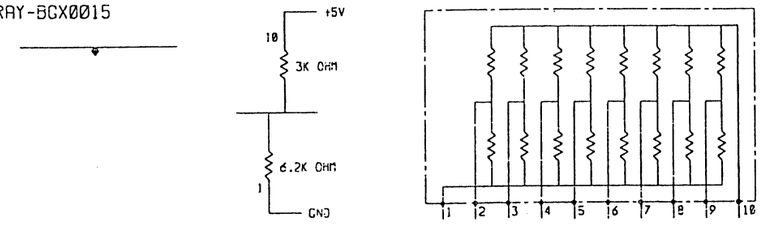
1) RESISTOR

(1)RAY-AL3R9K8



SYMBOL CIRCUIT PART

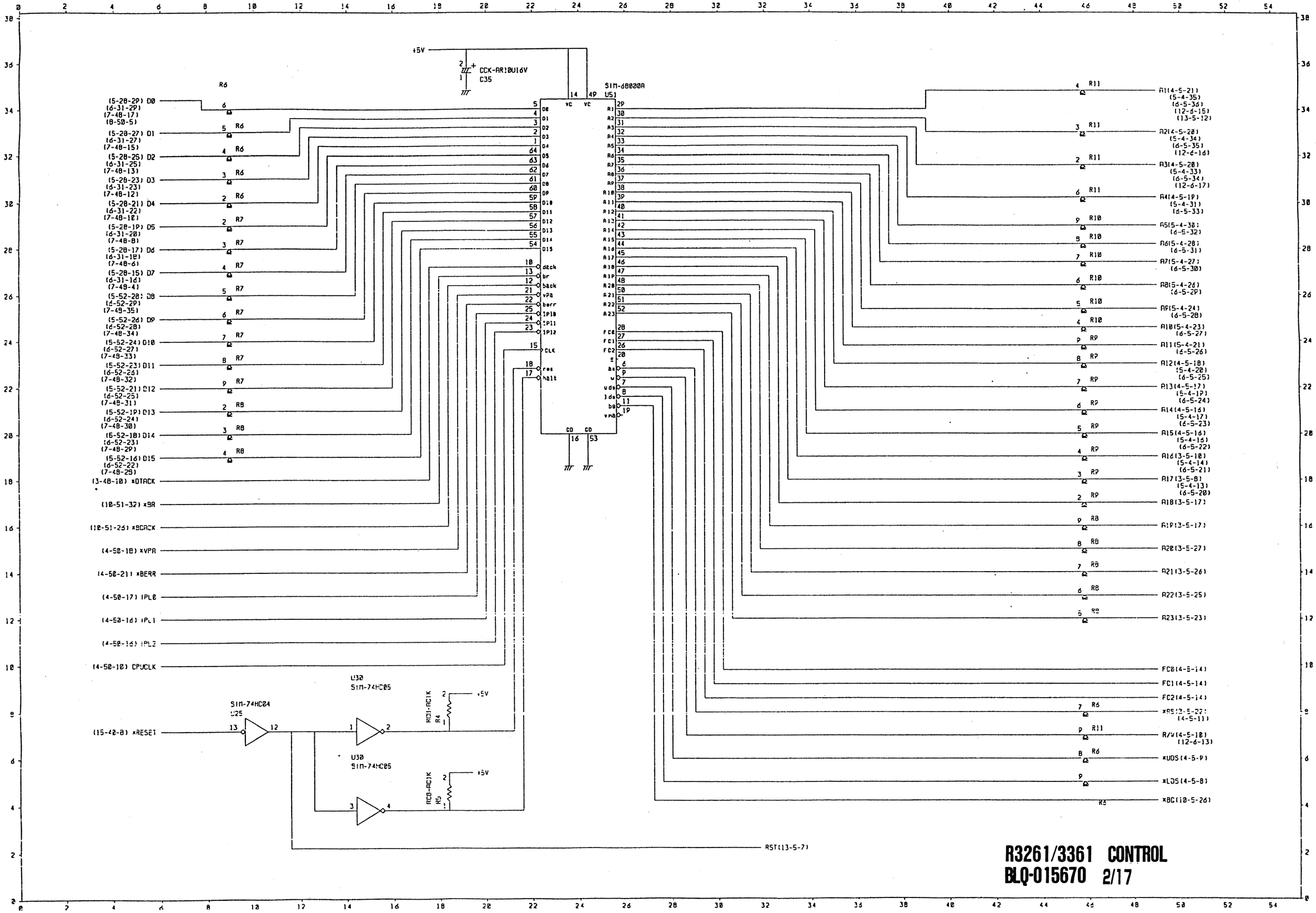
(2) RAY-BGX0015



SYMBOL CIRCUIT PART

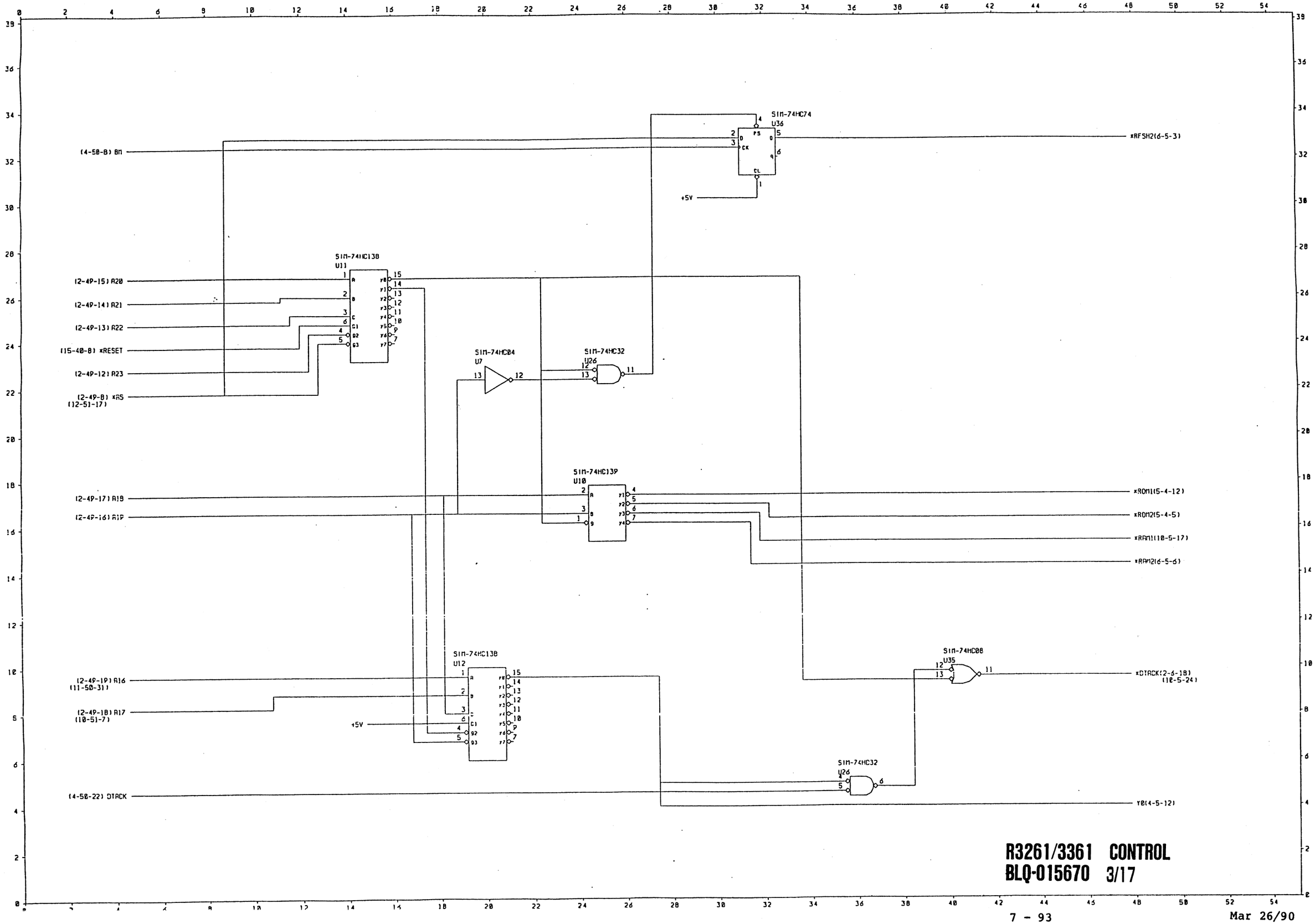
2) IC PIN NAME (PIN-NAME OF NEGATIVE LOGIC ARE DISPLAYED BY SMALL LETTER)





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+5V

RAY-RL1K4 R1 1 2 D2
RAY-RL1K4 R1 1 3 D3
RAY-RL1K4 R1 1 4 D4
RAY-RL1K4 R1 1 5 D5
NLD-R00016

(5-28-15) D7
(6-31-16)
(7-48-4)
(5-28-17) D6
(6-31-18)
(7-48-6)
(5-28-19) D5
(6-31-20)
(7-48-8)
(5-28-21) D4
(6-31-22)
(7-48-10)
(5-28-23) D3
(6-31-23)
(7-48-12)
(5-28-25) D2
(6-31-25)
(7-48-13)
(5-28-27) D1
(6-31-27)
(7-48-15)
(5-28-29) D0
(6-31-29)
(7-48-17)
(8-50-5)

(2-49-35) A1
(11-50-22)
(2-49-33) A2
(11-50-14)
(2-49-32) A3
(11-50-20)
(2-49-30) A4
(11-50-15)
(2-49-23) A12
(11-50-28)
(2-49-22) A13
(11-50-33)
(2-49-21) A14
(11-50-30)
(2-49-20) A15
(11-50-32)

(2-49-10) FC0
(2-49-9) FC1
(2-49-9) FC2

(3-48-4) Y8
(2-49-8) xAS
(12-51-17)
(2-49-7) R/W
(2-49-6) xUDS
(2-49-5) xLDS
(12-51-4) xINTC
(13-52-14) xINTSIO
(8-50-25) xINTD
(8-50-33) xINTC
(10-32-3) xNMI
(15-40-8) xRESET
(15-40-4) CLK16

SIM-65C815
U31

29	D7	cs0	79
28	D6	cs1	78
27	D5	cs2	77
26	D4	cs3	76
25	D3	cs4	75
24	D2	cs5	74
23	D1	cs6	72
22	D0	cs7	71
8	A1	cs8	70
7	A2	cs9	69
6	A3	csA	68
5	A4	csB	67
4	A13	csC	66
3	A14	csD	65
2	A15	csE	64
1	A16	dtAck	18
34	FC0	berr	21
35	FC1	halt	20
36	FC2	ecs	17
80	ade	rd	16
9	as	vPA	37
13	v	iPl0	50
10	uds	iPl1	49
11	lds	iPl2	48
32	ml	int	47
31	iorq	uwr	14
38	intB	lwr	15
39	int1		
40	int2		
41	int3		
42	int4		
43	int5		
44	int6		
45	int7		
46	intB		
30	Z80	8mhz	55
51	reset	4mhz	56
53	xin	2mhz	57
54	xout	dout1	58
60	tack	dout2	59
61	tick	tbout	62
19	vclk	tlout	63

+5V
+5V
+5V

xCSCPIB(12-6-10)
xCSBZR(12-6-1)
xCSSIO(13-5-13)
xCSPIO(14-4-5)
xRGTRC(7-5-2)
xIPENDC(8-6-1)
xBUZZER
C/DC(8-4-10)
xSC(12-6-6)
DTACK(3-5-5)
xBERR(2-6-14)
xRD(5-4-10)
(6-5-1)
(7-5-3)
(10-5-16)
(12-6-2)
(13-5-14)
(14-4-3)
xVPA(2-6-15)
IPL0(2-6-13)
IPL1(2-6-12)
IPL2(2-6-11)
xUVR(6-5-2)
(7-5-25)
(10-5-13)
(14-4-7)
xLVR(6-5-5)
(7-5-4)
(8-4-12)
(10-5-15)
(12-6-3)
(12-6-12)
(13-5-16)
(14-4-22)

SIM-74HC04
U25
3 4
CPUCLK(12-6-10)

SIM-74HC04
U25
11 10
UNI3-5-32)
(10-5-23)

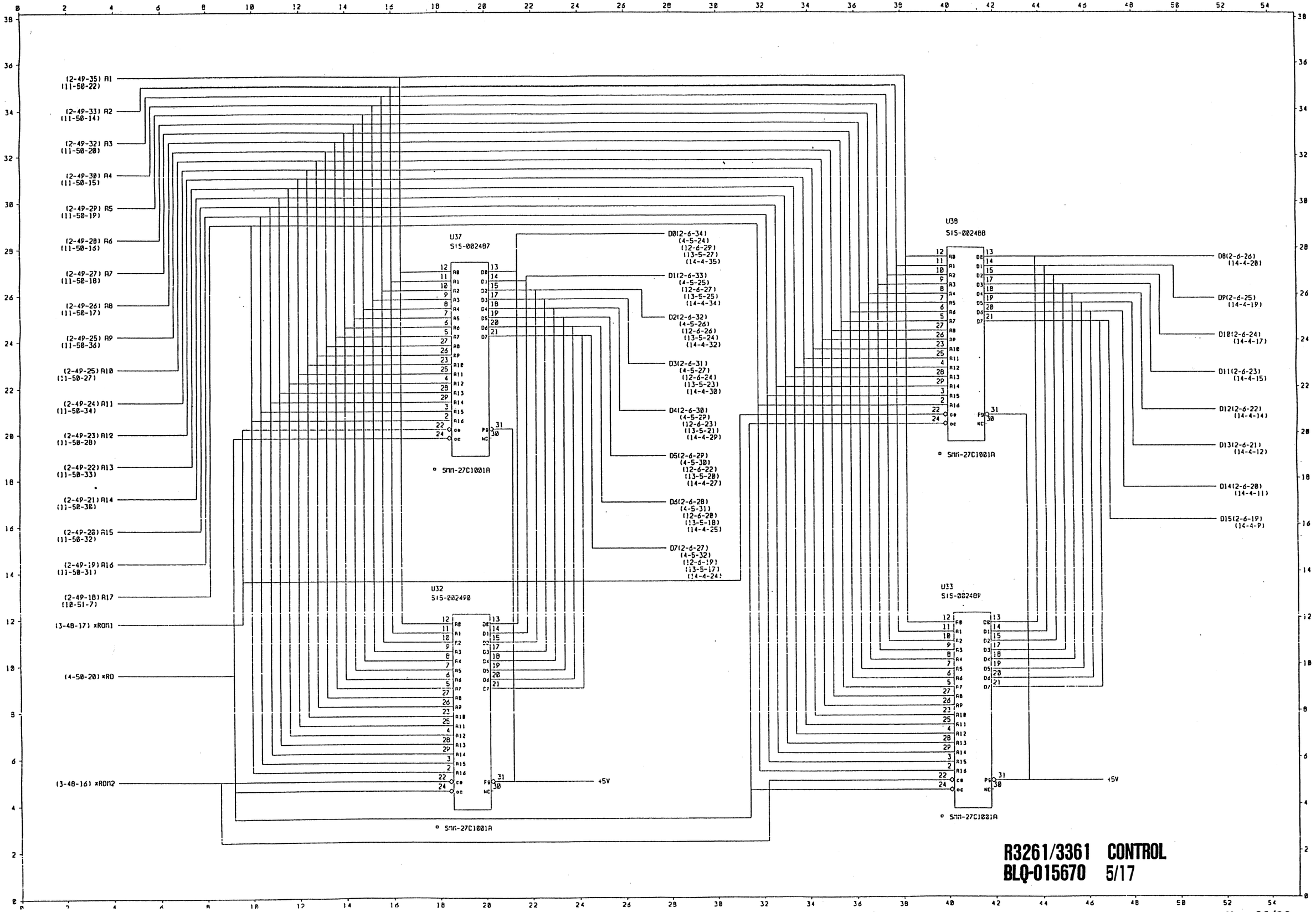
4MI13-5-10)

TRXC(13-5-9)

BZCLK

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(2-49-35) A1
(11-50-22)
(2-49-33) A2
(11-50-14)
(2-49-32) A3
(11-50-20)
(2-49-30) A4
(11-50-15)
(2-49-29) A5
(11-50-19)
(2-49-28) A6
(11-50-16)
(2-49-27) A7
(11-50-18)
(2-49-26) A8
(11-50-17)
(2-49-25) A9
(11-50-36)
(2-49-25) A10
(11-50-27)
(2-49-24) A11
(11-50-34)
(2-49-23) A12
(11-50-28)
(2-49-22) A13
(11-50-33)
(2-49-21) A14
(11-50-30)
(2-49-20) A15
(11-50-32)
(2-49-19) A16
(11-50-31)
(2-49-18) A17
(10-51-7)
(3-48-17) xROM1
(4-50-20) xRD
(3-48-16) xROM2
U37
SIS-002497
U32
SIS-002490
U39
SIS-002488
U33
SIS-002489
SMM-27C1001A
D0(2-6-34)
(4-5-24)
(12-6-29)
(13-5-27)
(14-4-35)
D1(2-6-33)
(4-5-25)
(12-6-27)
(13-5-25)
(14-4-34)
D2(2-6-32)
(4-5-26)
(12-6-26)
(13-5-24)
(14-4-32)
D3(2-6-31)
(4-5-27)
(12-6-24)
(13-5-23)
(14-4-30)
D4(2-6-30)
(4-5-29)
(12-6-23)
(13-5-21)
(14-4-29)
D5(2-6-29)
(4-5-30)
(12-6-22)
(13-5-20)
(14-4-27)
D6(2-6-28)
(4-5-31)
(12-6-20)
(13-5-18)
(14-4-25)
D7(2-6-27)
(4-5-32)
(12-6-19)
(13-5-17)
(14-4-24)
D8(2-6-26)
(14-4-20)
DP(2-6-25)
(14-4-19)
D10(2-6-24)
(14-4-17)
D11(2-6-23)
(14-4-15)
D12(2-6-22)
(14-4-14)
D13(2-6-21)
(14-4-12)
D14(2-6-20)
(14-4-11)
D15(2-6-19)
(14-4-9)
+5V
R3261/3361 CONTROL
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(2-49-35) A1
(11-50-22)

(2-49-33) A2
(11-50-14)

(2-49-32) A3
(11-50-20)

(2-49-30) A4
(11-50-15)

(2-49-29) A5
(11-50-19)

(2-49-28) A6
(11-50-16)

(2-49-27) A7
(11-50-18)

(2-49-26) A8
(11-50-17)

(2-49-25) A9
(11-50-36)

(2-49-25) A10
(11-50-27)

(2-49-24) A11
(11-50-34)

(2-49-23) A12
(11-50-28)

(2-49-22) A13
(11-50-33)

(2-49-21) A14
(11-50-30)

(2-49-20) A15
(11-50-32)

(2-49-19) A16
(11-50-31)

(2-49-18) A17
(10-51-7)

(10-51-17) *CSRAM

(10-51-15) *LWE

(10-51-16) *RDE

(10-51-14) *UWE

+5V

SMM-65B128DP12
U23

SMM-65B128DP12
U18

SMM-658128DP12
D24

SMM-658128DP12
U19

D0(2-6-34)
(4-5-24)
(12-6-29)
(13-5-27)
(14-4-35)

D1(2-6-33)
(4-5-25)
(12-6-27)
(13-5-25)
(14-4-34)

D2(2-6-32)
(4-5-26)
(12-6-26)
(13-5-24)
(14-4-32)

D3(2-6-31)
(4-5-27)
(12-6-24)
(13-5-23)
(14-4-30)

D4(2-6-30)
(4-5-29)
(12-6-23)
(13-5-20)
(14-4-29)

D5(2-6-29)
(4-5-30)
(12-6-22)
(13-5-20)
(14-4-27)

D6(2-6-28)
(4-5-31)
(12-6-20)
(13-5-18)
(14-4-25)

D7(2-6-27)
(4-5-32)
(12-6-15)
(13-5-17)
(14-4-24)

+5V

D8(2-6-26)
(14-4-20)

D9(2-6-25)
(14-4-19)

D10(2-6-24)
(14-4-17)

D11(2-6-23)
(14-4-15)

D12(2-6-22)
(14-4-14)

D13(2-6-21)
(14-4-12)

D14(2-6-20)
(14-4-11)

D15(2-6-19)
(14-4-9)

(3-48-14) *RAM2

+5V

(4-50-13) *LWR

SIM-74HC08
U35

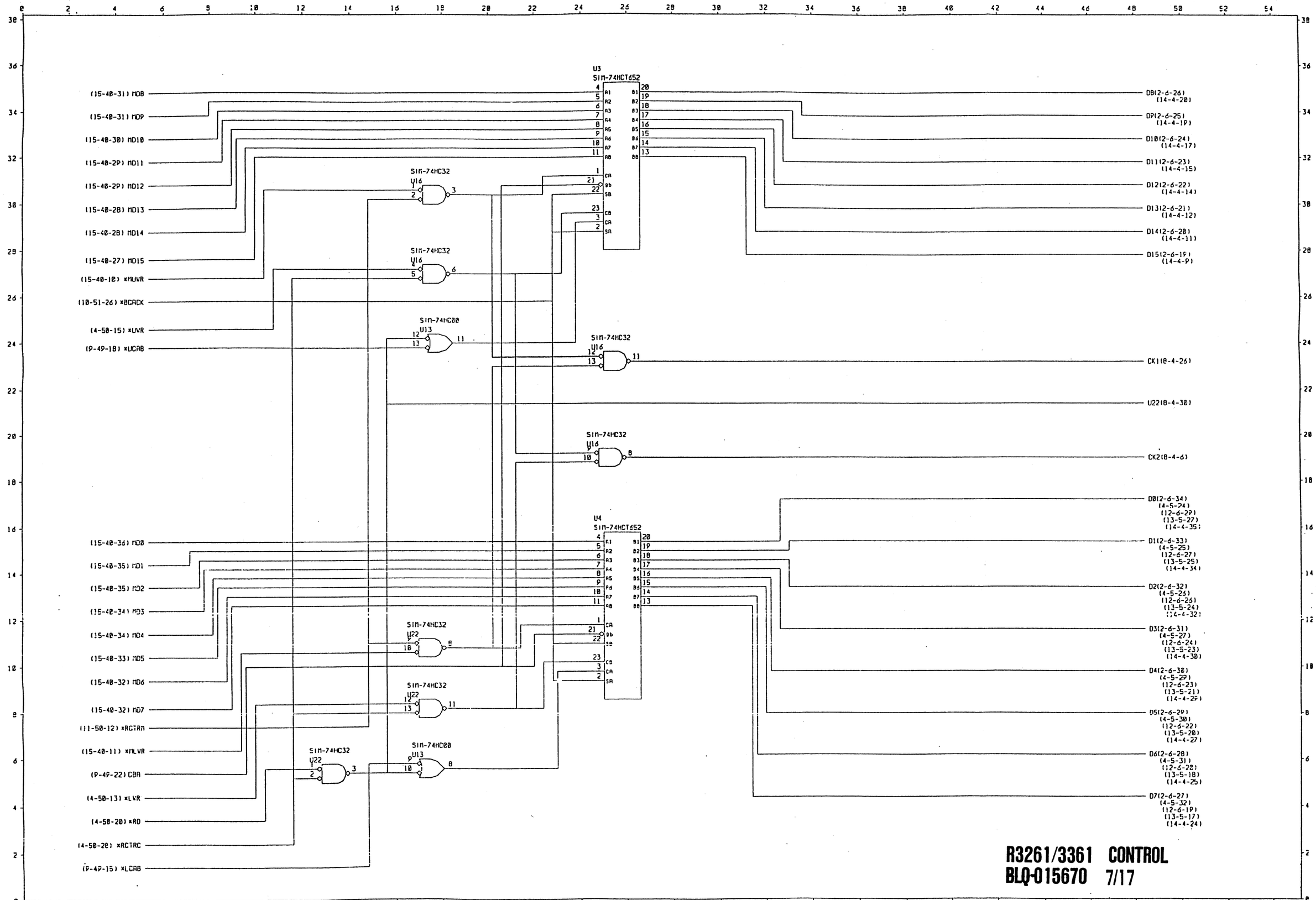
(3-48-33) *RFSH2

(4-50-15) *UWR

(4-50-20) *RD

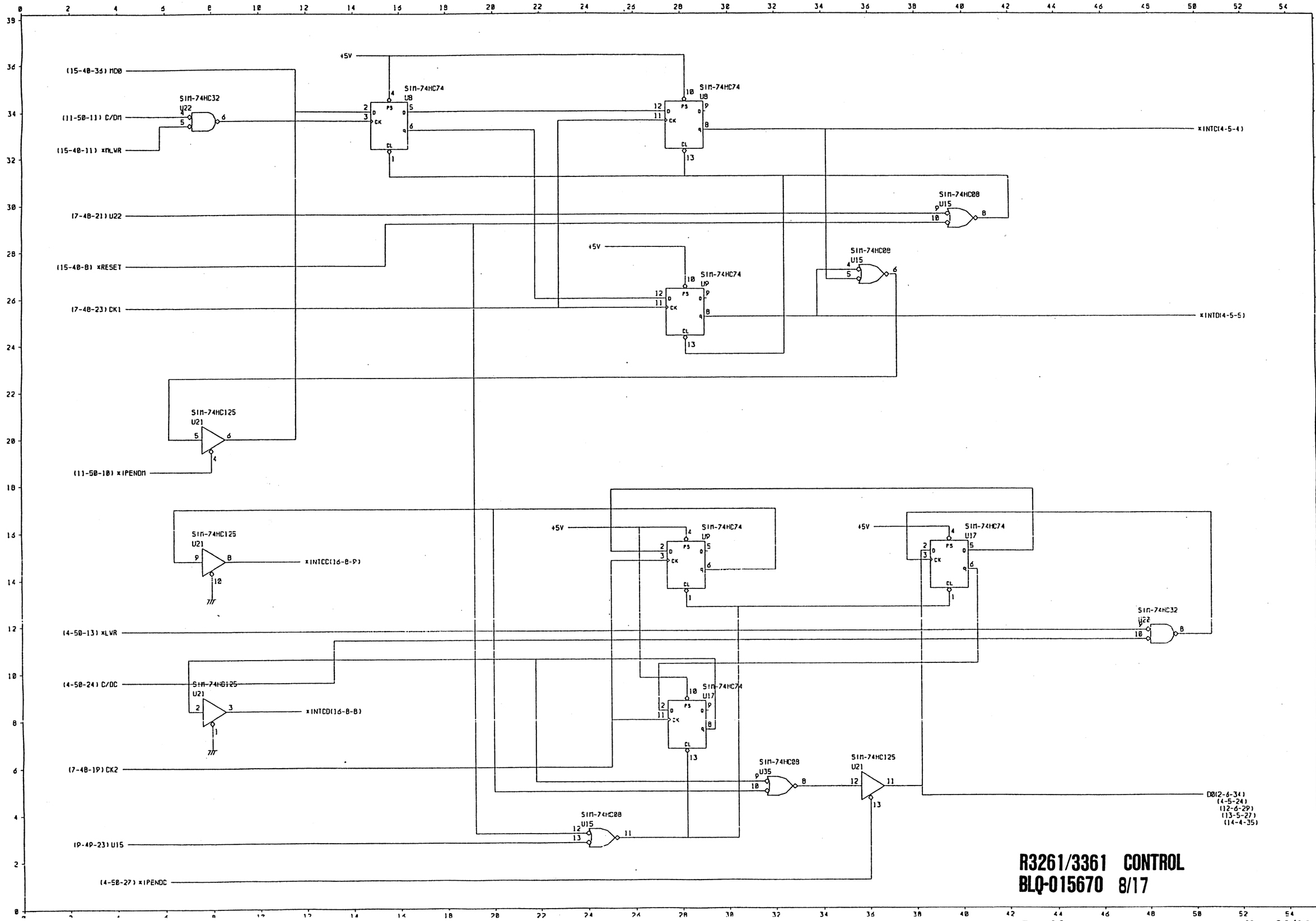
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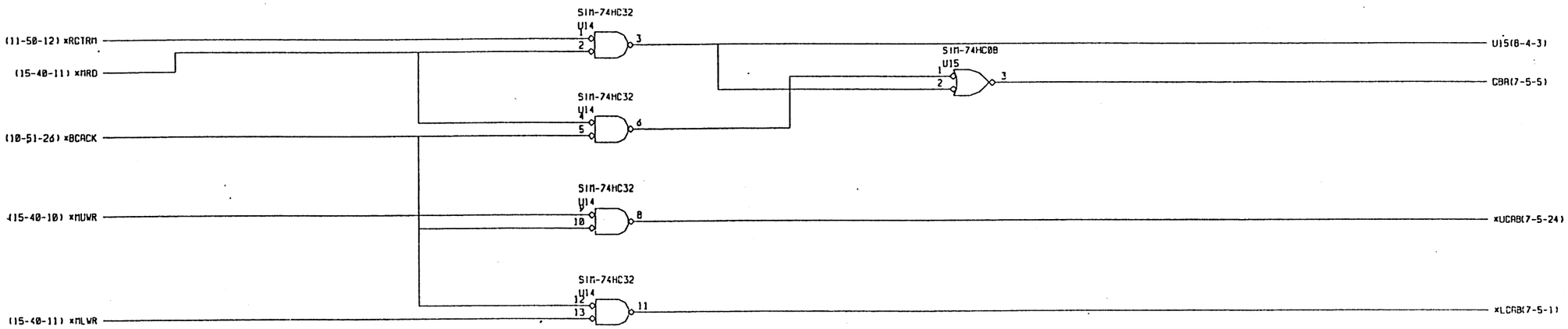
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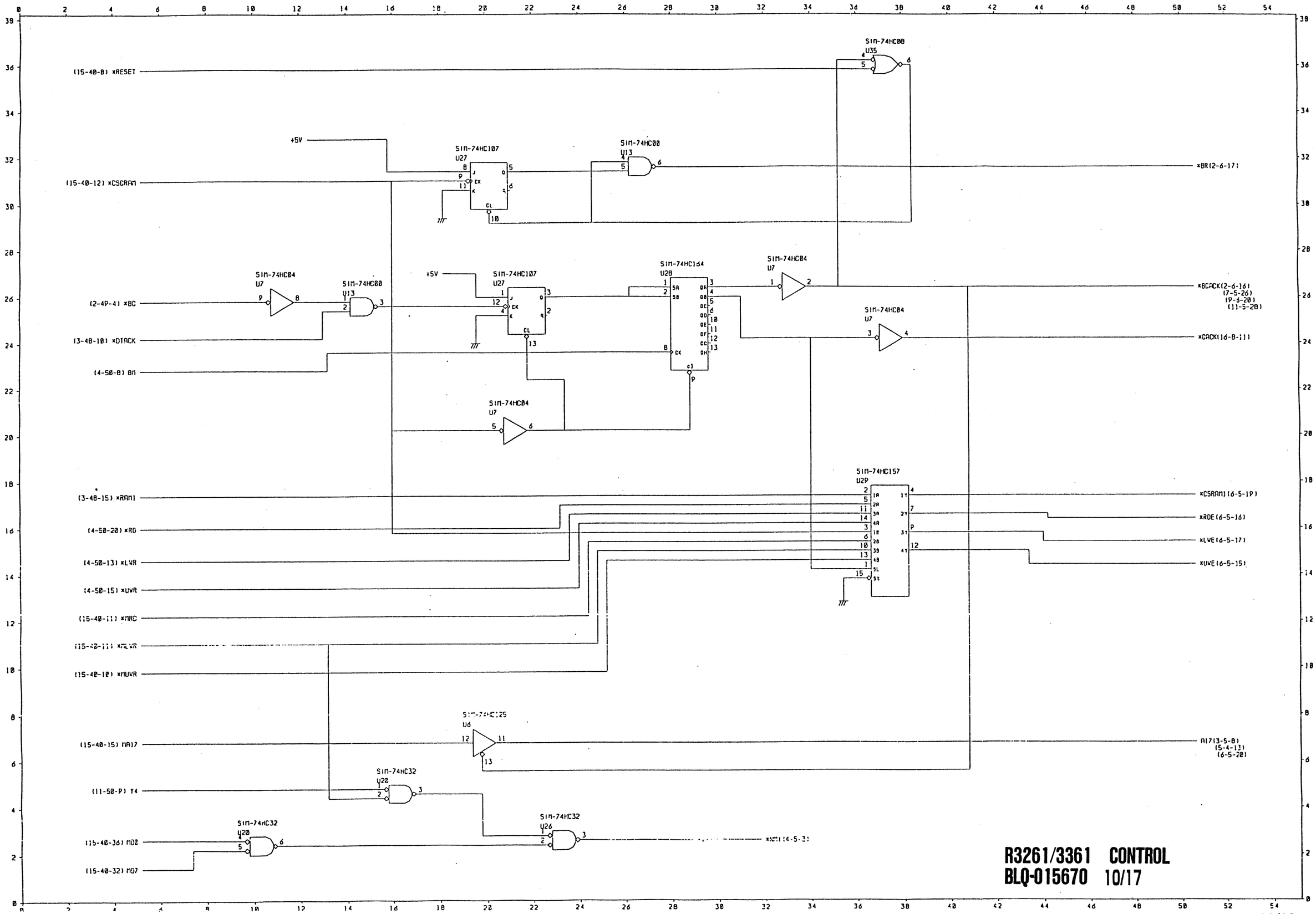
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(15-40-36) MD0
(11-50-11) C/DM
(15-40-11) xMLWR
SIM-74HC32
U22
+5V
SIM-74HC74
U8
SIM-74HC74
U9
xINTC(4-5-4)
SIM-74HC08
U15
(7-48-21) U22
(15-40-8) xRESET
(7-48-23) CK1
+5V
SIM-74HC74
U9
SIM-74HC08
U15
xINTD(4-5-5)
SIM-74HC125
U21
(11-50-10) xIPENDM
SIM-74HC125
U21
xINTCC(16-8-9)
+5V
SIM-74HC74
U9
+5V
SIM-74HC74
U17
SIM-74HC32
U22
(4-50-13) xLWR
(4-50-24) C/DC
SIM-74HC125
U21
xINTCD(16-8-8)
SIM-74HC74
U17
(7-48-19) CK2
SIM-74HC08
U35
SIM-74HC125
U21
D012-6-34)
(4-5-24)
(12-6-29)
(13-5-27)
(14-4-35)
SIM-74HC08
U15
(19-49-23) U15
(4-58-27) xIPENDC
R3261/3361 CONTROL
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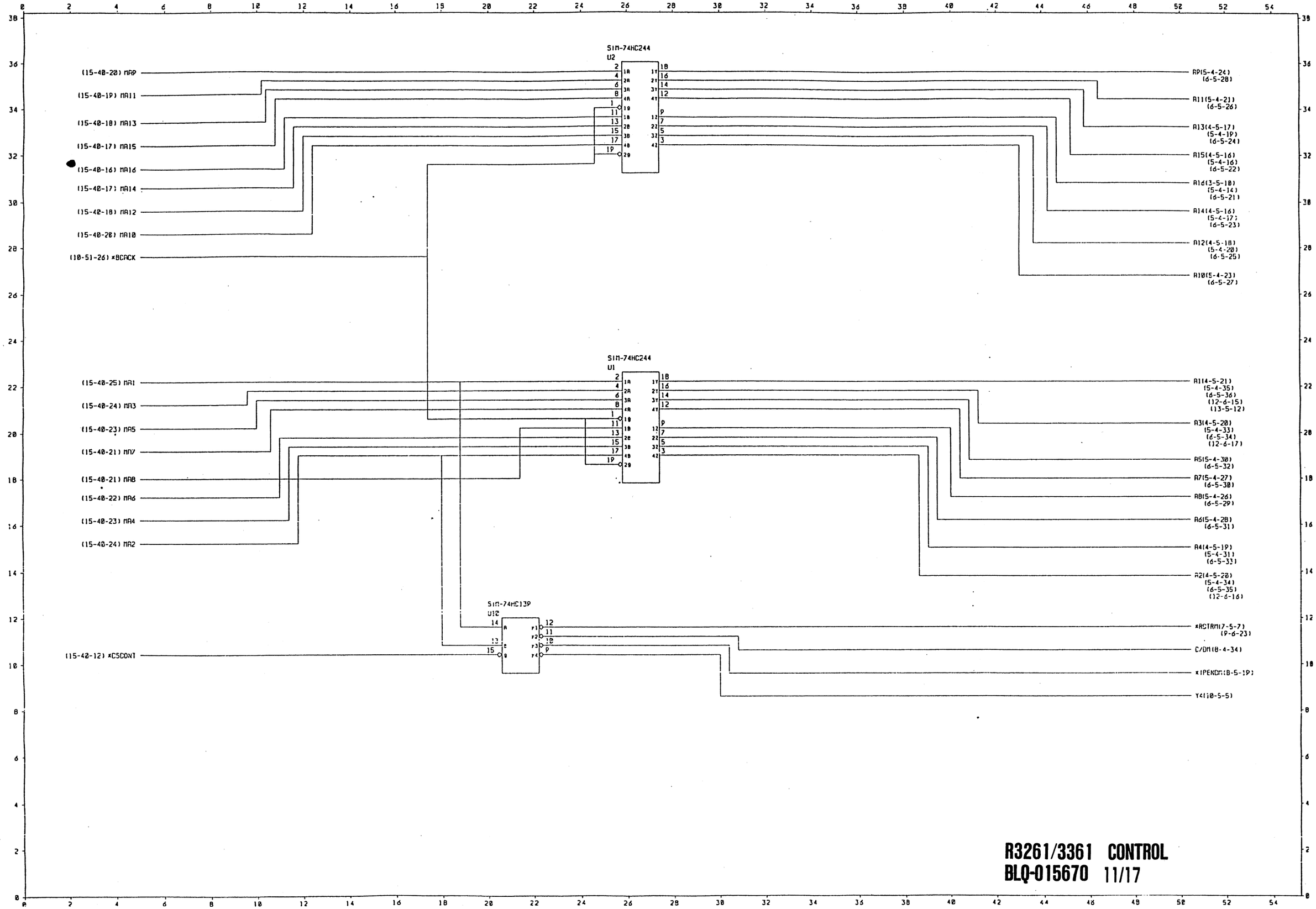
R3261/3361 CONTROL
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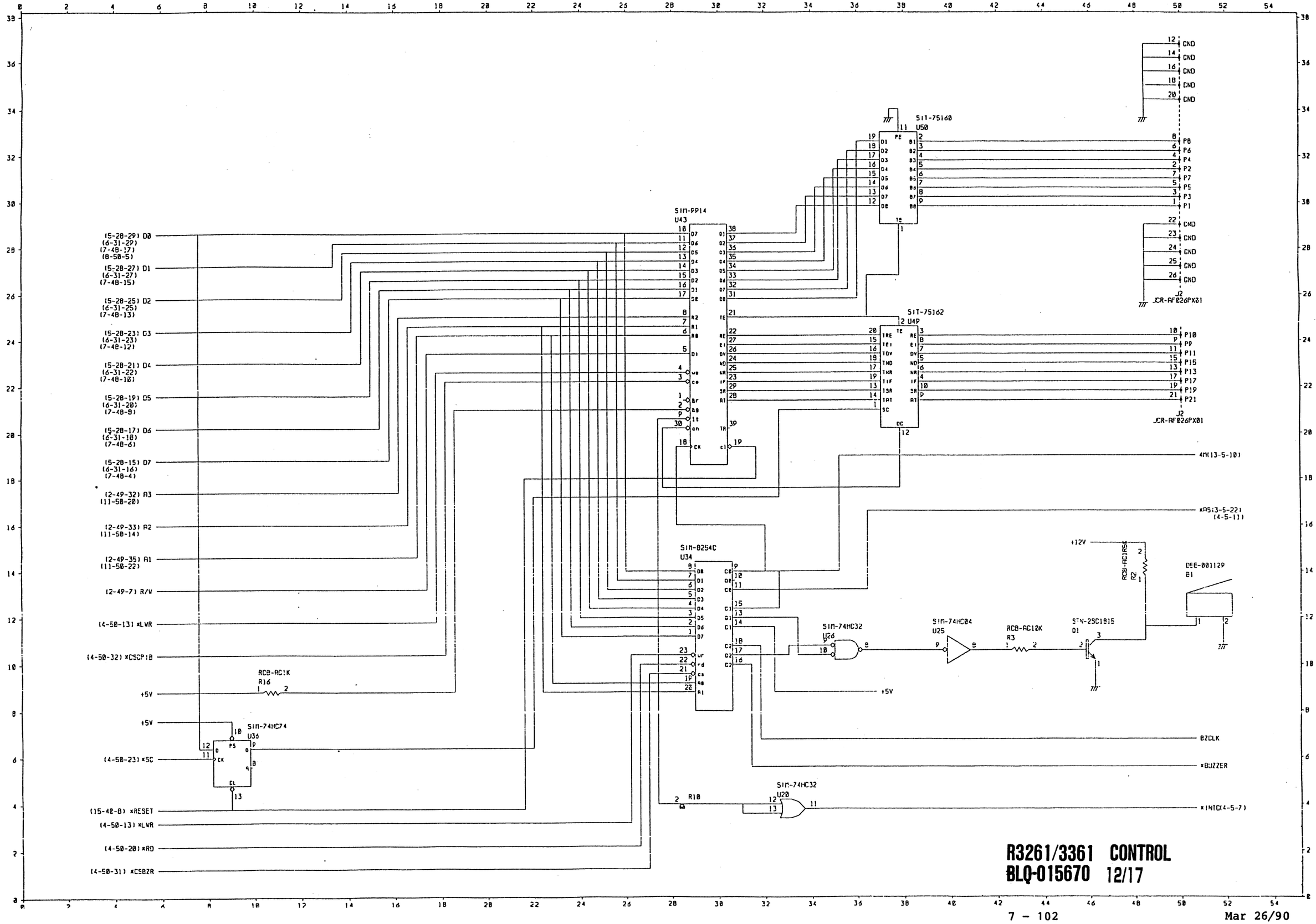


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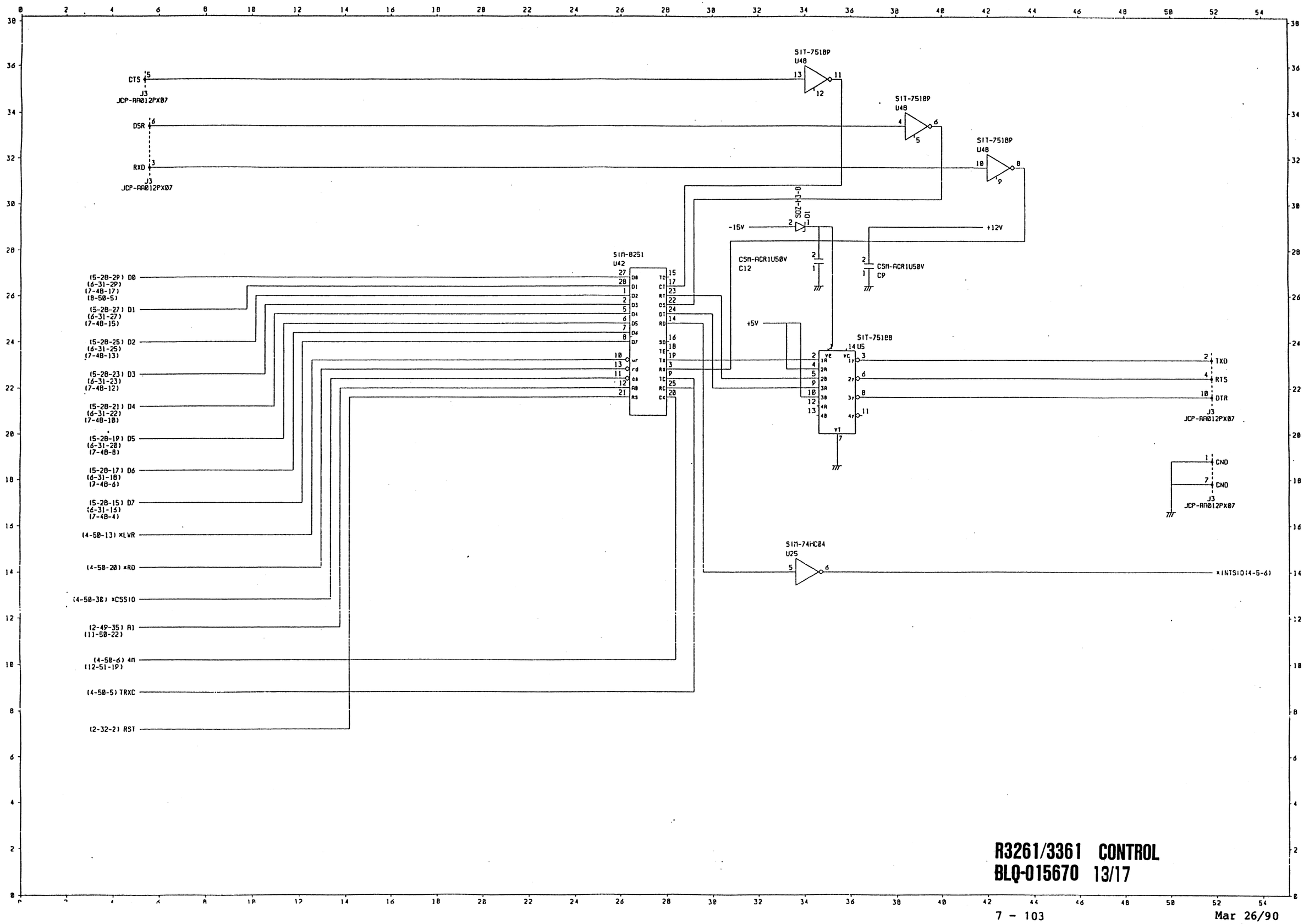
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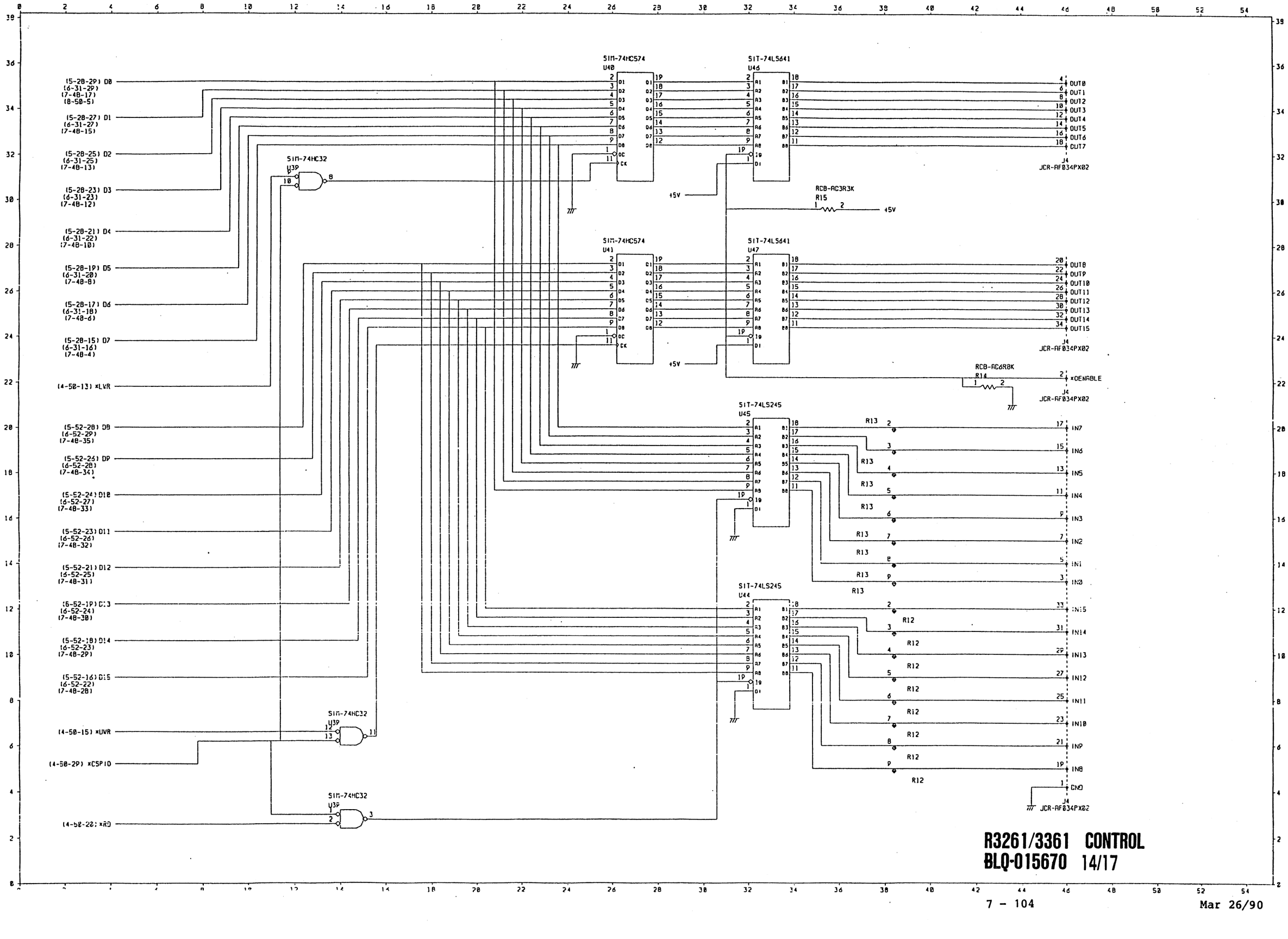
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BLQ-015670 12/17

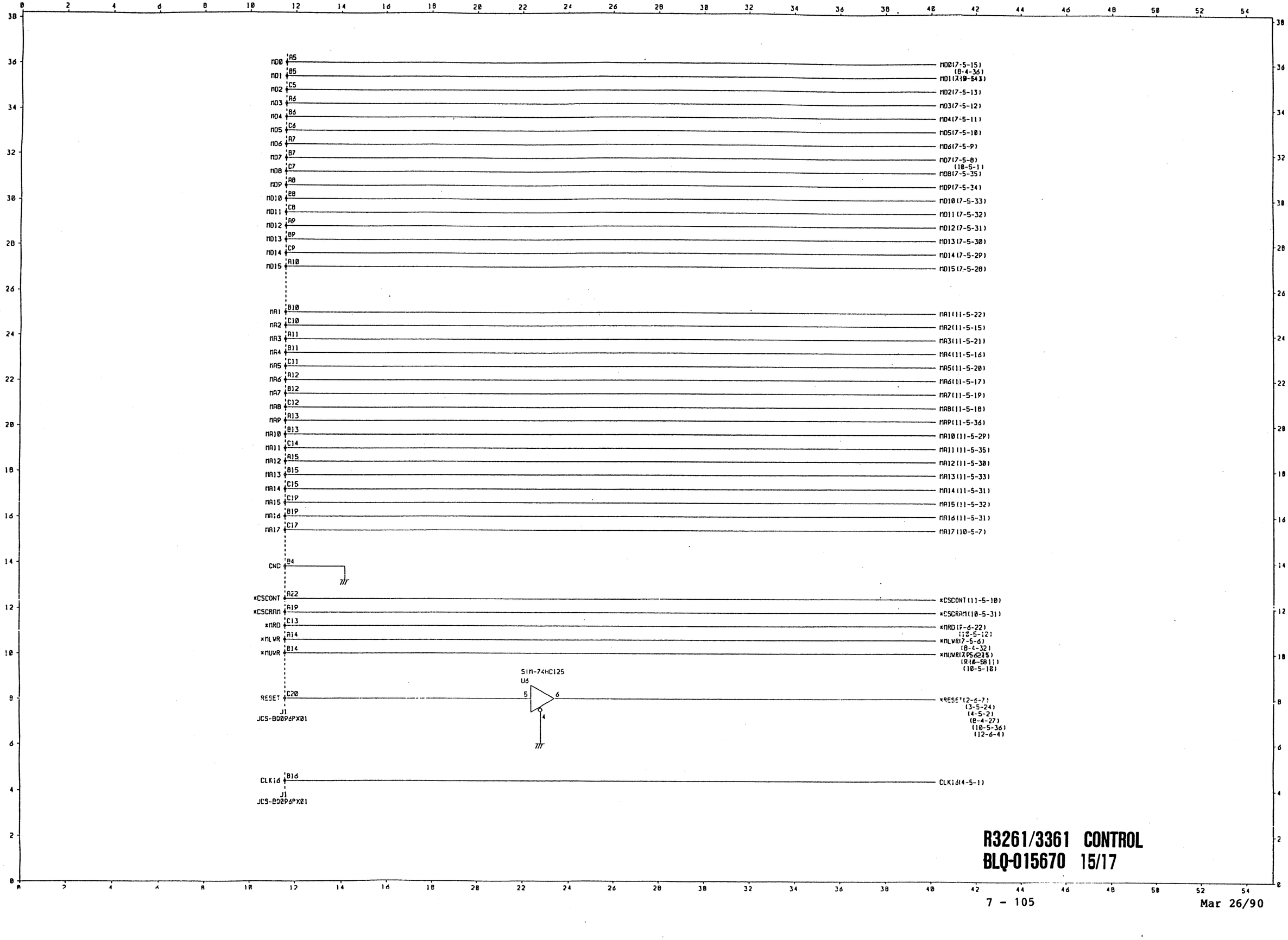


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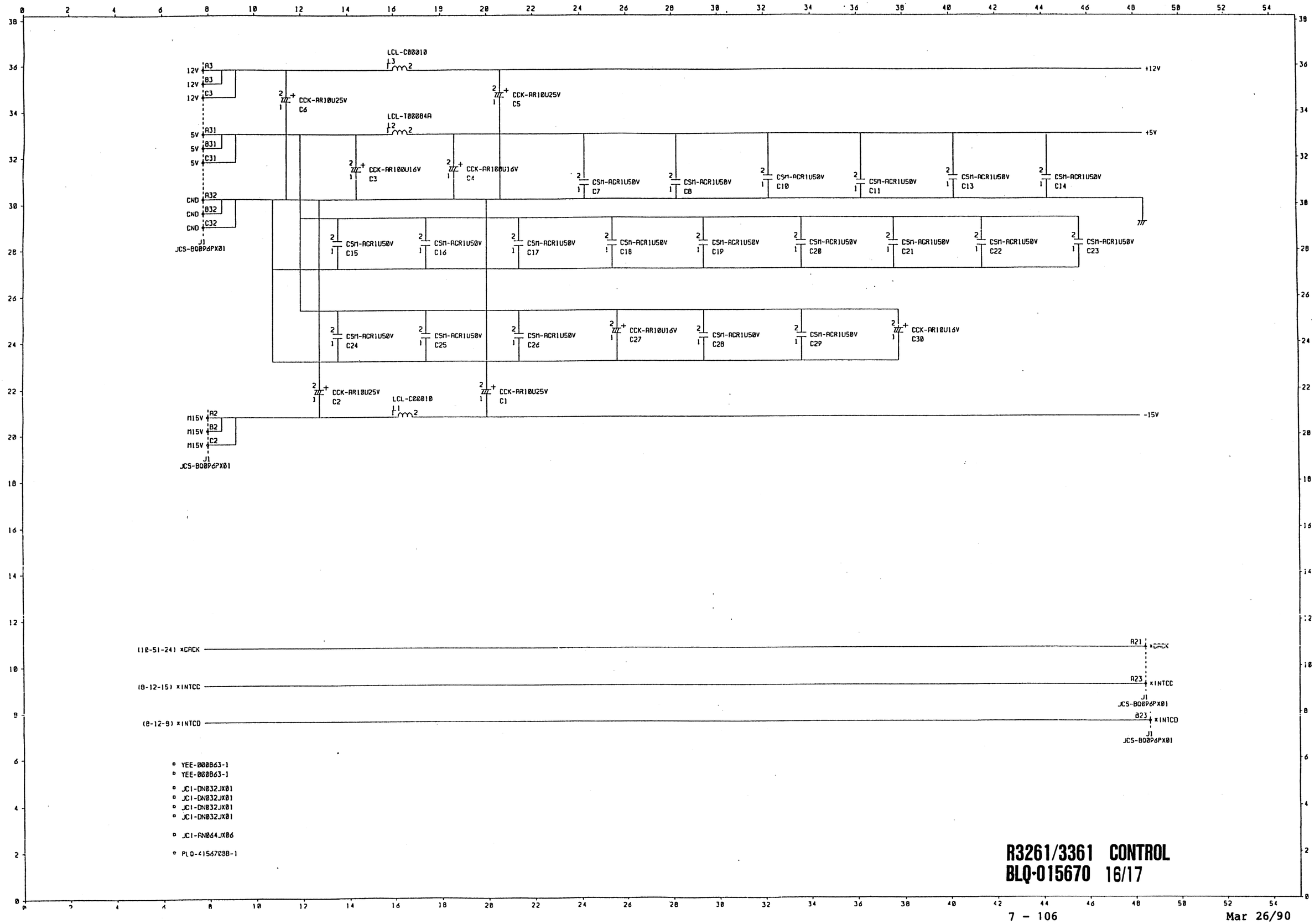


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BLQ-015670 15/17



R3261/3361 CONTROL
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GND
+5V
20 10
xxxxx
SIM-74HC574

GND
+5V
14 7
xxxxx
SIM-74HC32

GND
+5V
20 10
xxxxx
SIM-74HC244

GND
+5V
14 7
xxxxx
SIM-74HC164

GND
+5V
16 8
xxxxx
SIM-74HC157

GND
+5V
16 8
xxxxx
SIM-74HC139

GND
+5V
16 8
xxxxx
SIM-74HC138

GND
+5V
14 7
xxxxx
SIM-74HC125

GND
+5V
14 7
xxxxx
SIM-74HC107

GND
+5V
14 7
xxxxx
SIM-74HC08

GND
+5V
14 7
xxxxx
SIM-74HC05

GND
+5V
14 7
xxxxx
SIM-74HC04

GND
+5V
14 7
xxxxx
SIM-74HC00

GND
+5V
24 12
xxxxx
SIM-74HCT652

GND
+5V
33 73 12 52
xxxxx
SIM-654815

GND
+5V
10 1
xxxxx
RAY-BCX8015

+5V
1
xxxxx
RAY-AL3RPK8

GND
+5V
32 16
xxxxx
SNM-65B128DP12

GND
+5V
1 32 16
U32
SIS-00249P

GND
+5V
1 32 16
U33
SIS-00248P

GND
+5V
1 32 16
U37
SIS-002487

GND
+5V
1 32 16
U38
SIS-002488

GND
+5V
14 7
xxxxx
SIT-7518P

GND
+5V
22 11
xxxxx
SIT-75162

GND
+5V
20 10
xxxxx
SIT-75160

GND
+5V
20 10
xxxxx
SIT-74LS641

GND
+5V
20 10
xxxxx
SIT-74LS245

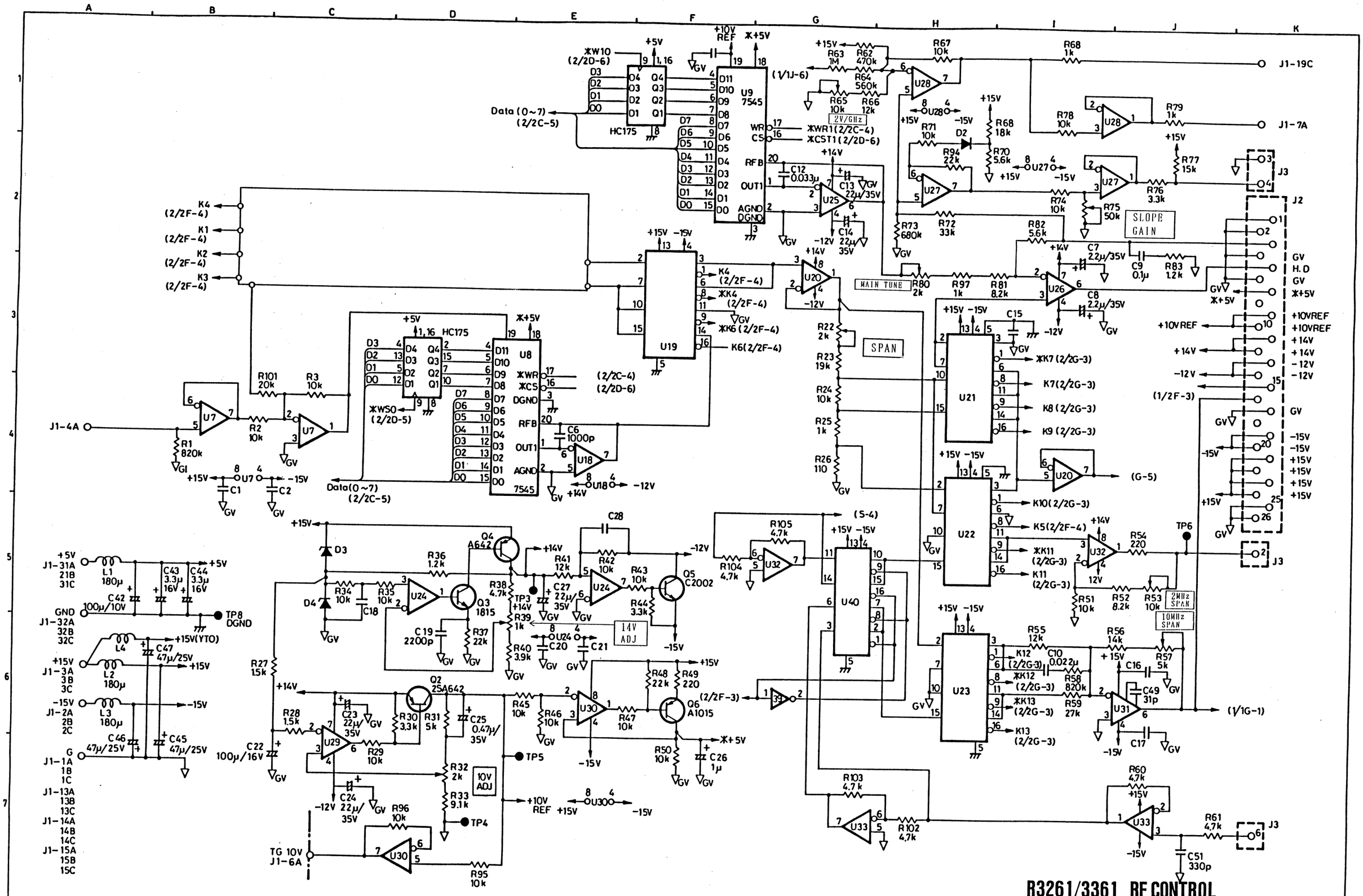
GND
+5V
40 20
xxxxx
SIM-P914

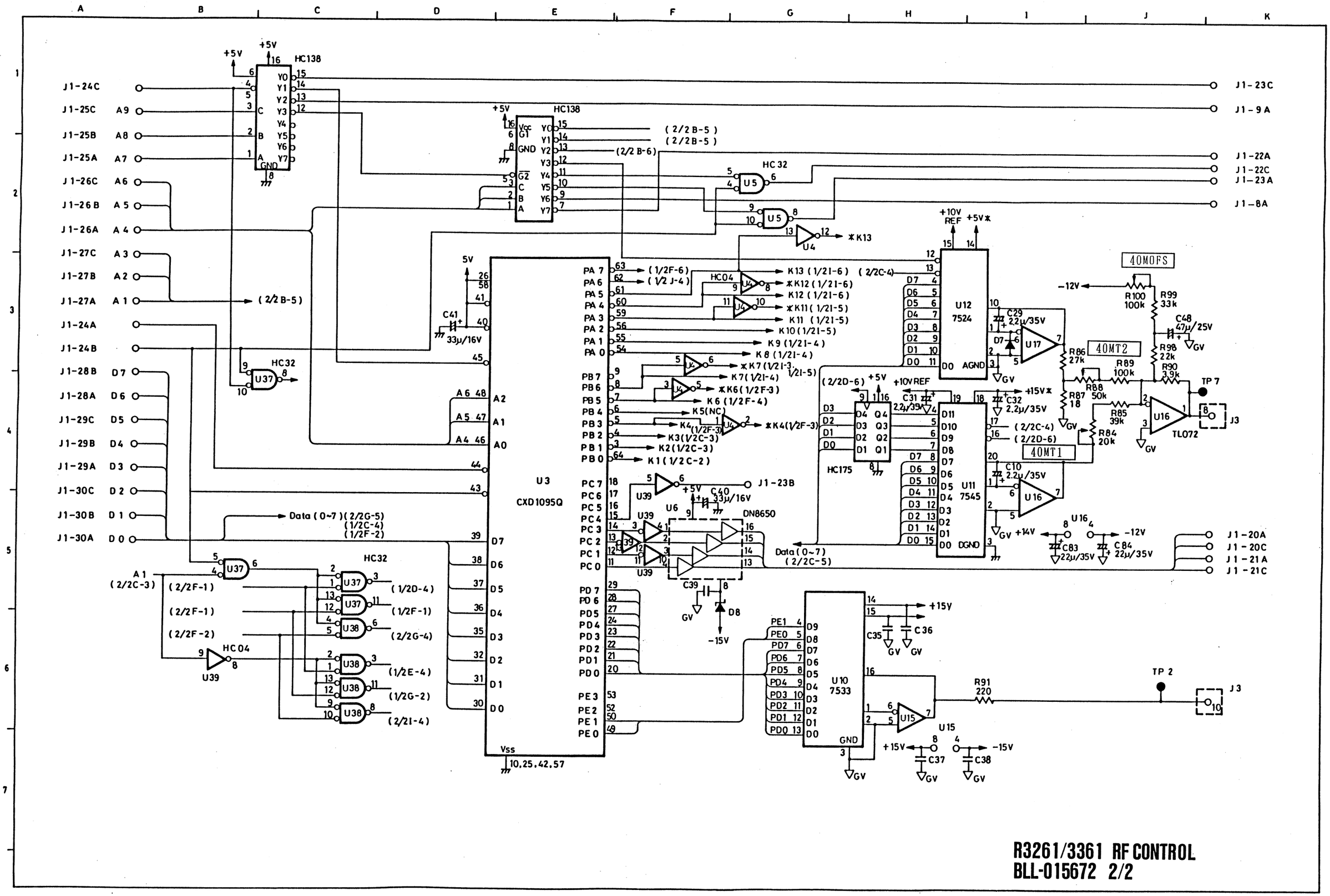
GND
+5V
24 12
xxxxx
SIM-8254C

GND
+5V
26 4
xxxxx
SIM-8251

GND
+5V
14 7
xxxxx
SIM-74HC74

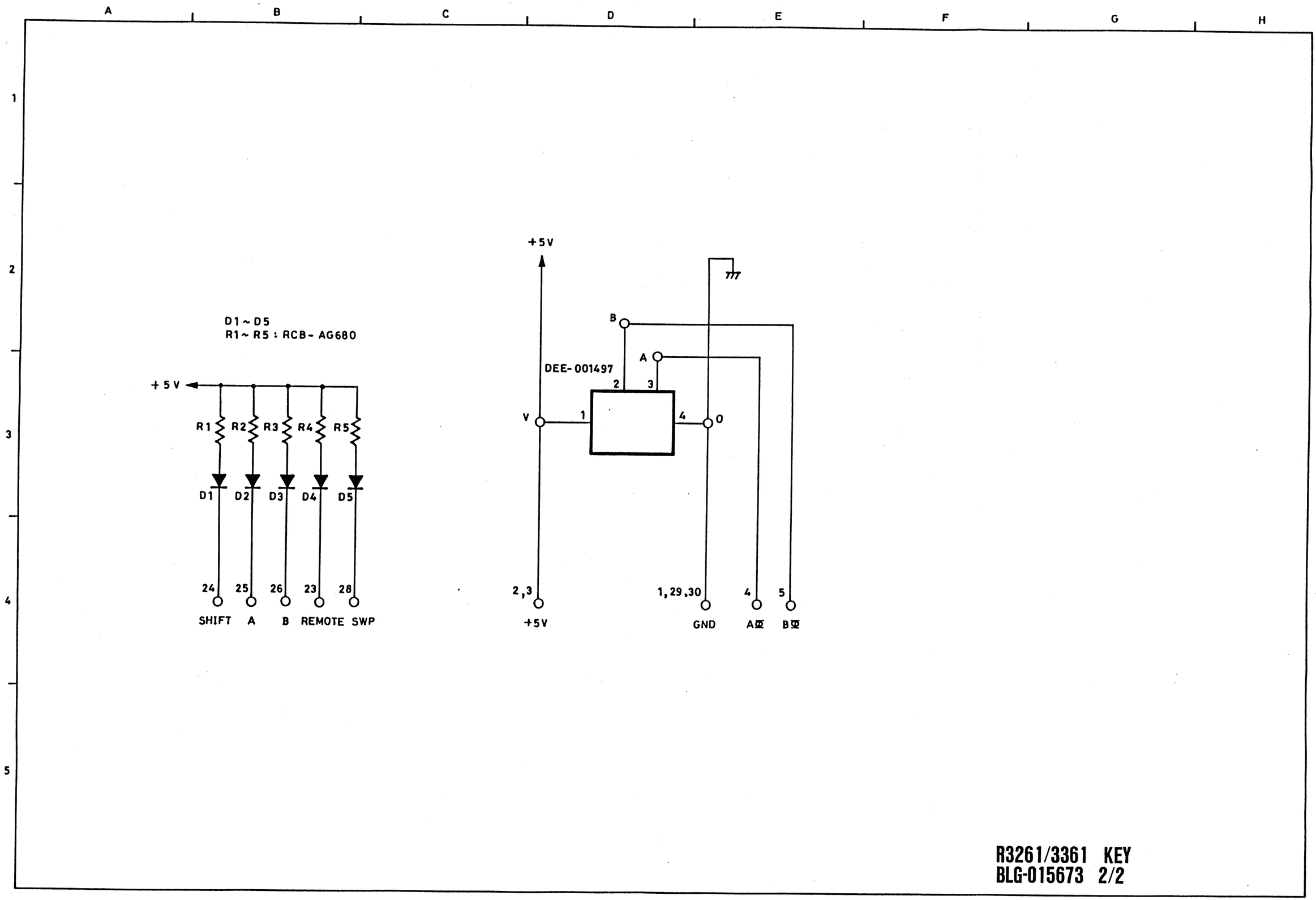
R3261/3361 CONTROL
BLQ-015670 17/17


R3261/3361 RF CONTROL
BLL-015672 1/2
SPAN
SLOPE GAIN
MAIN TUNE
14V ADJ
10V ADJ
2V/GHz
2MHz SPAN
10MHz SPAN
HC175
7545
Data (0~7) (2/2C-5)
(1/1J-6)
J1-19C
J1-7A
J1-4A
J1-31A 21B 31C
J1-32A 32B 32C
J1-3A 3B 3C
J1-2A 2B 2C
J1-1A 1B 1C
J1-13A 13B 13C
J1-14A 14B 14C
J1-15A 15B 15C
TG 10V J1-6A
(1/1G-1)
(1/2F-3)
(G-5)
(S-4)
(2/2F-3)
(2/2C-4)
(2/2D-6)
(2/2C-5)
TP3
TP4
TP5
TP6
TP8 DGND
+15V(YTO)



R3261/3361 RF CONTROL
BLL-015672 2/2

R3261/3361 KEY
BLG-015673 1/2



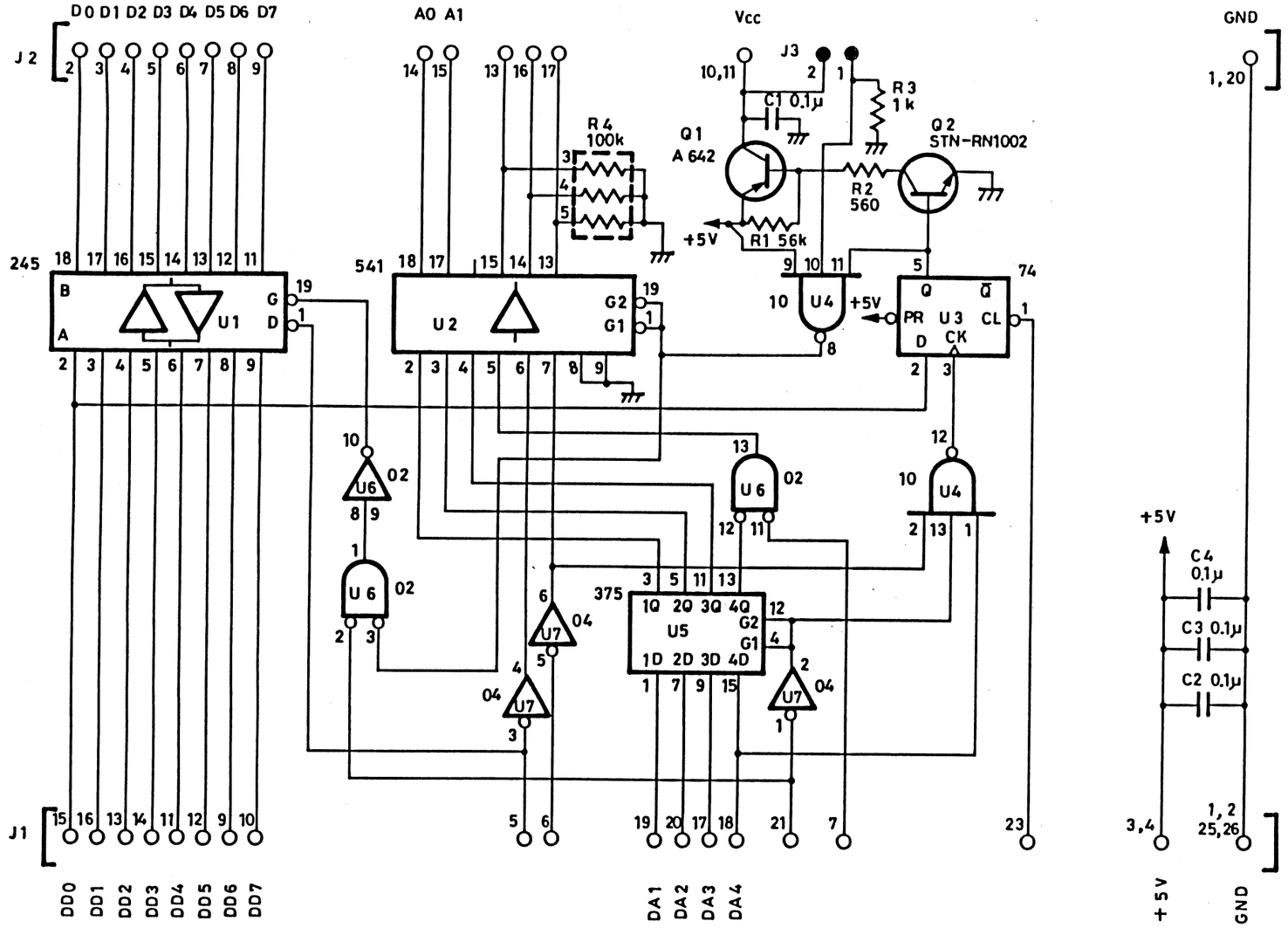
R3261/3361 KEY
BLG-015673 2/2

1. DIAGRAMS ILLUSTRATION

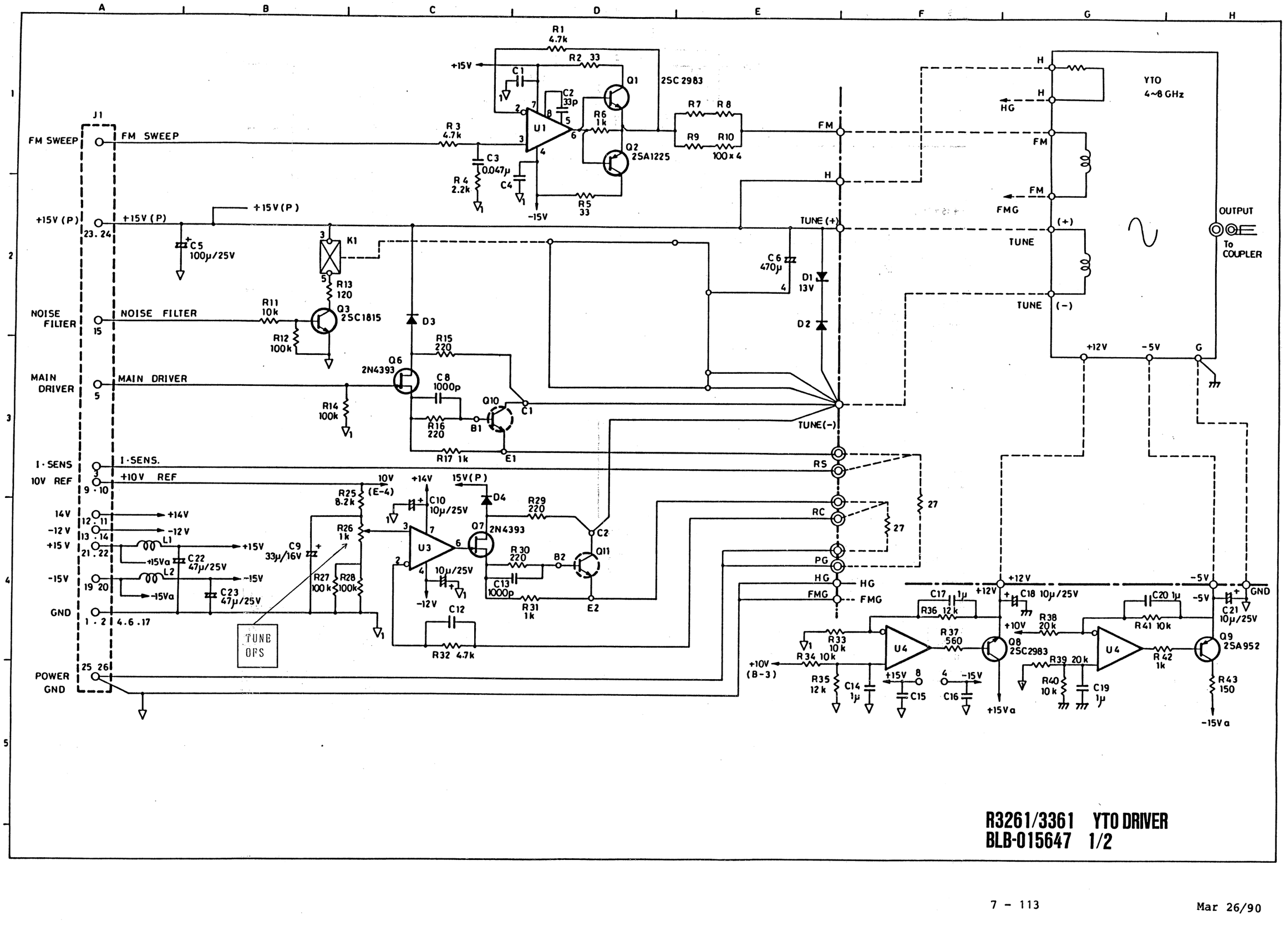
1-1 SYMBOLS REFERENCE DESIGNATORS

1) IC PIN NAME (PIN-NAME OF NEGATIVE LOGIC ARE DISPLAYED BY SMALL LETTER)



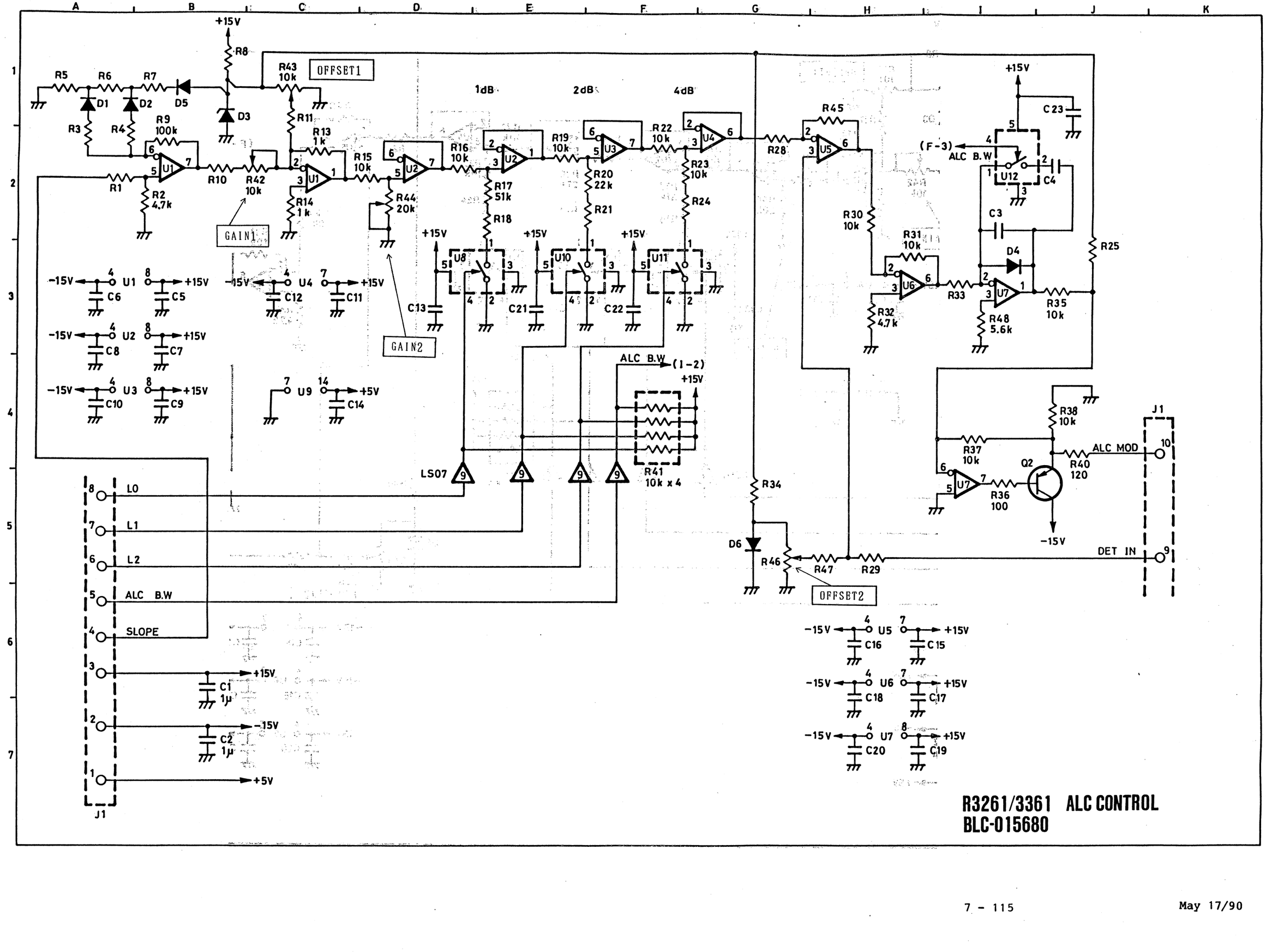


R3261/3361 IC CARD
BGC-015745 2/2

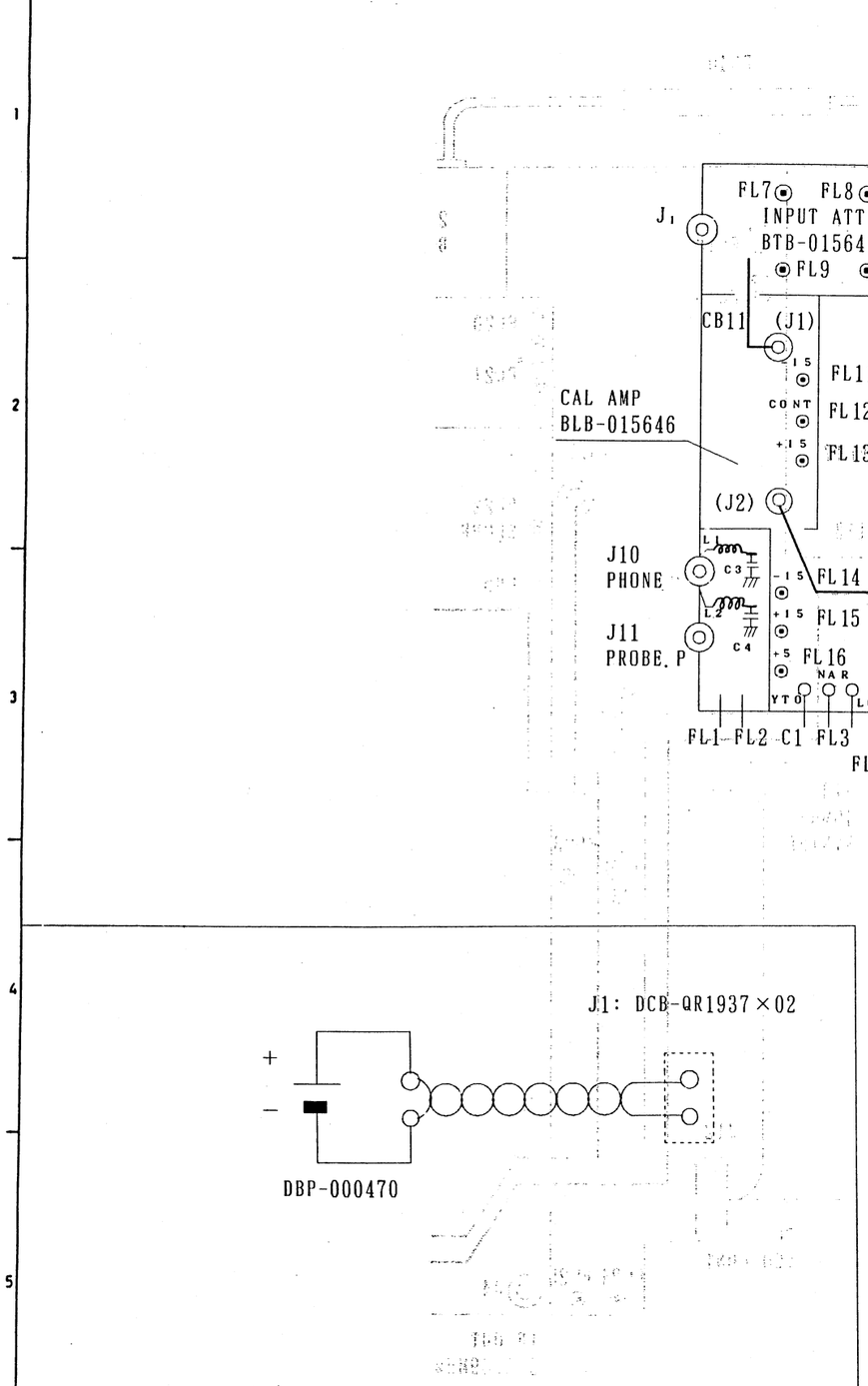


R3261/3361 YTO DRIVER
BLB-015647 1/2

R3261/3361 YTO DRIVER (REFERENCE VOLTAGE)
BLB-015647 2/2

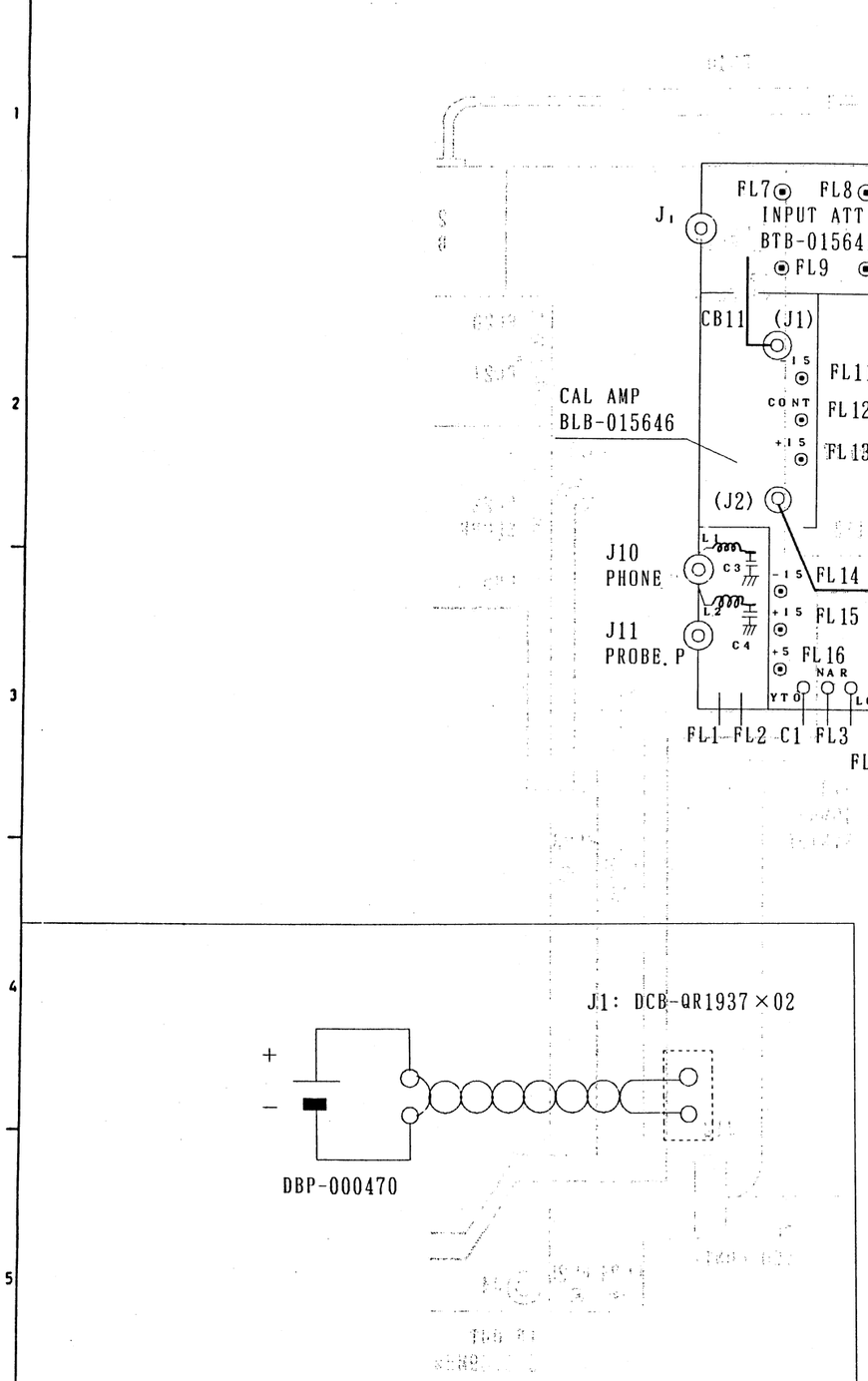


R3261/3361 ALC CONTROL
BLC-015680



R3261/3361 BATT,HOLDER
BLB-016053

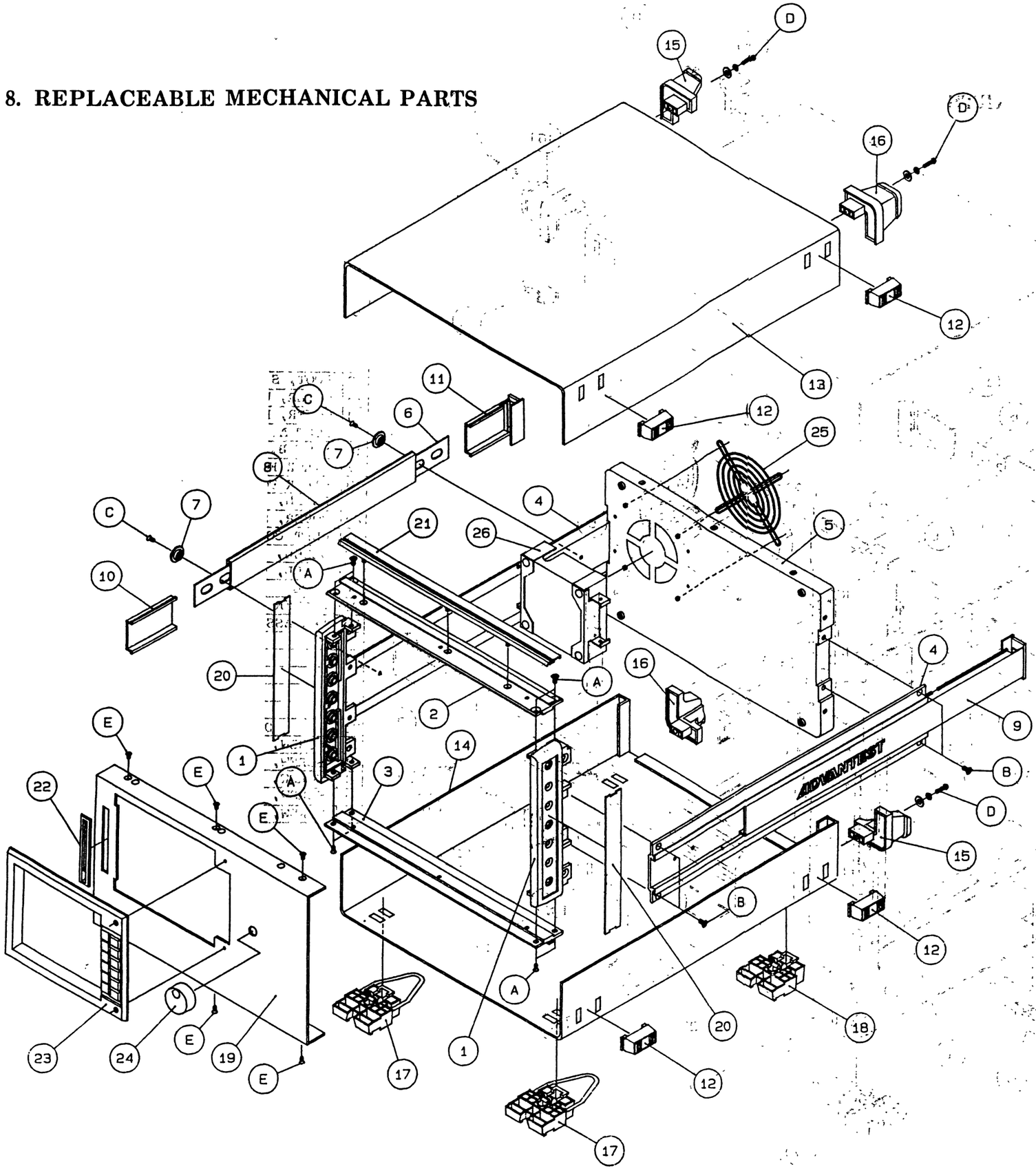
R3261/3361 RF BLOCK
WBL-3361RF



R3261/3361 BATT,HOLDER
BLB-016053

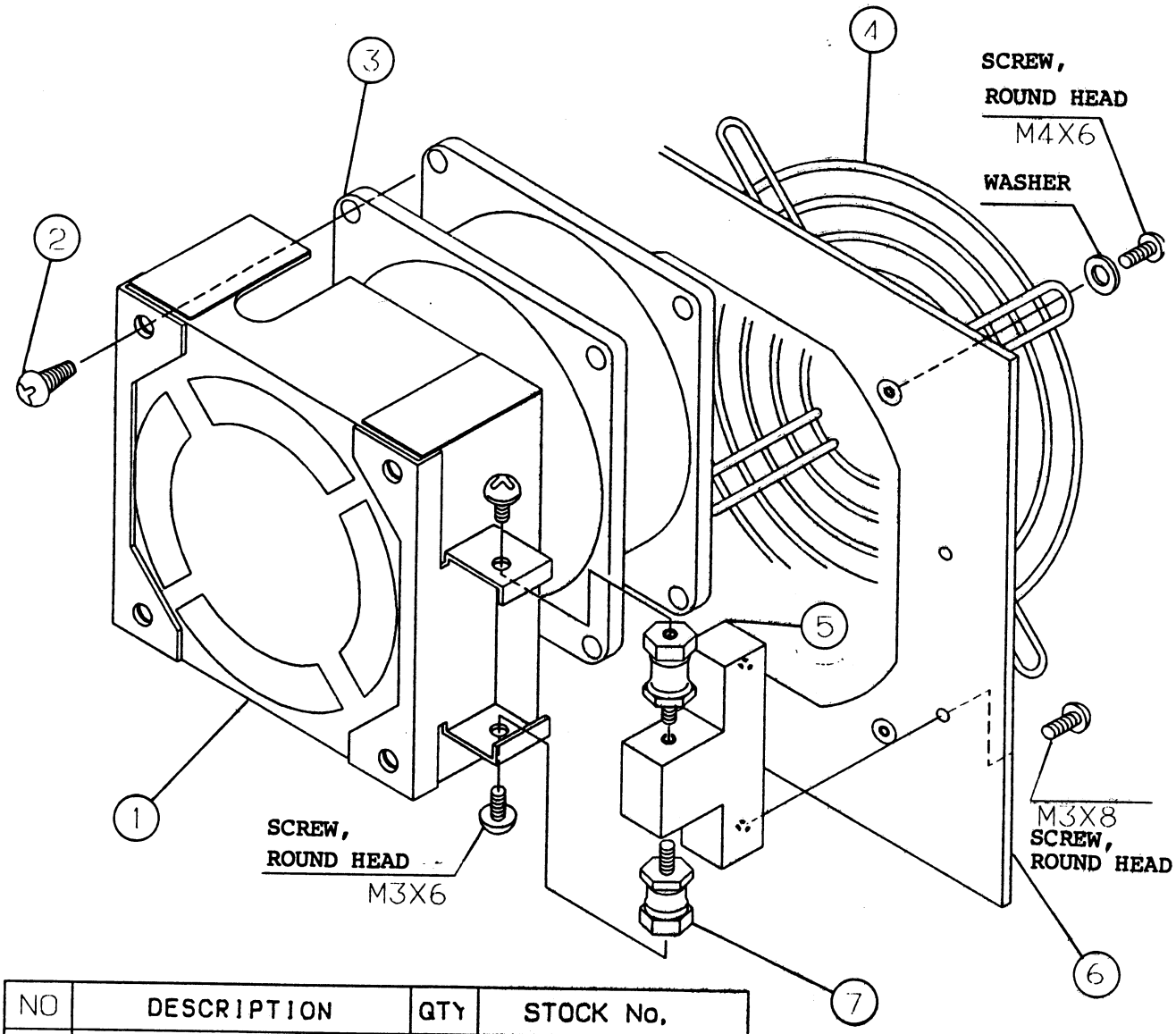
R3261/3361 RF BLOCK
WBL-3361RF

8. REPLACEABLE MECHANICAL PARTS



No.	DESCRIPTION	STOCK No.	QTY
1	BRACKET	MCT-55609A	2
2	SUBFRAME, UPPER	MHE-61712	1
3	SUBFRAME, LOWER	MHE-61713	1
4	SIDEFRAME	MHT-61471	2
5	PANEL, REAR	MCT-58898A	1
6	HANDLE, INNER	MBE-57474A	1
7	HANDLE, SPACER	MKX-32181A	2
8	HANDLE, SLEEVE	MPS-57469A001A	1
9	SIDE COVER, DECORATIVE	MPS-54301A001A	1
10	SIDE COVER, FRONT	MPX-57551A	1
11	SIDE COVER, REAR	MPX-57552A	1
12	FOOT, SIDE	MMX-20403A	4
13	COVER, UPPER	MBX-58917B	1
14	COVER, LOWER	MBX-58918B	1
15	FOOT A, REAR	MMX-54288A	2
16	FOOT B, REAR	MMX-54289A	2
17	STACK FOOT A	MEX-57550A	2
18	STACK FOOT B	MEX-54287A	2
19	PANEL, FRONT	MBS-59689A	1
20	COVER, SIDE BELT	MBX-55737A	2
21	BELT COVER	MPX-59711A	1
22	BEZEL, MEMORY CARD	MMX-61756A	1
23	BEZEL, CRT	MMX-59742A	1
24	KNOB	MMX-54164A	1
25	FINGER GUARD	YEE-000271	1
26	FAN ASSEMBLY	WK4-8110703	1

No.	DESCRIPTION	SIZE	QTY
A	SCREW, FLAT HEAD	M4X8	8
B	SCREW, FLAT HEAD	M4X6	8
C	SCREW, UNLOOSEN PROCESS	M4X8	2
D	SCREW, ROUND HEAD	M4X30	4
E	SCREW, FLAT HEAD	M3X6	6



NO	DESCRIPTION	QTY	STOCK No.
1	FAN SHIELD	1	MBE-32047
2	SELF TAPPING SCREW	3	YKG-V01508
3	FAN	(1)	DMF-001006
4	FINGER GUARD	1	YEE-000271
5	FAN SUPPORT C	2	MKJ-32046
6	REAR PANEL	1	MCT-58898
7	RUBBER CUSHION	4	YEE-001489